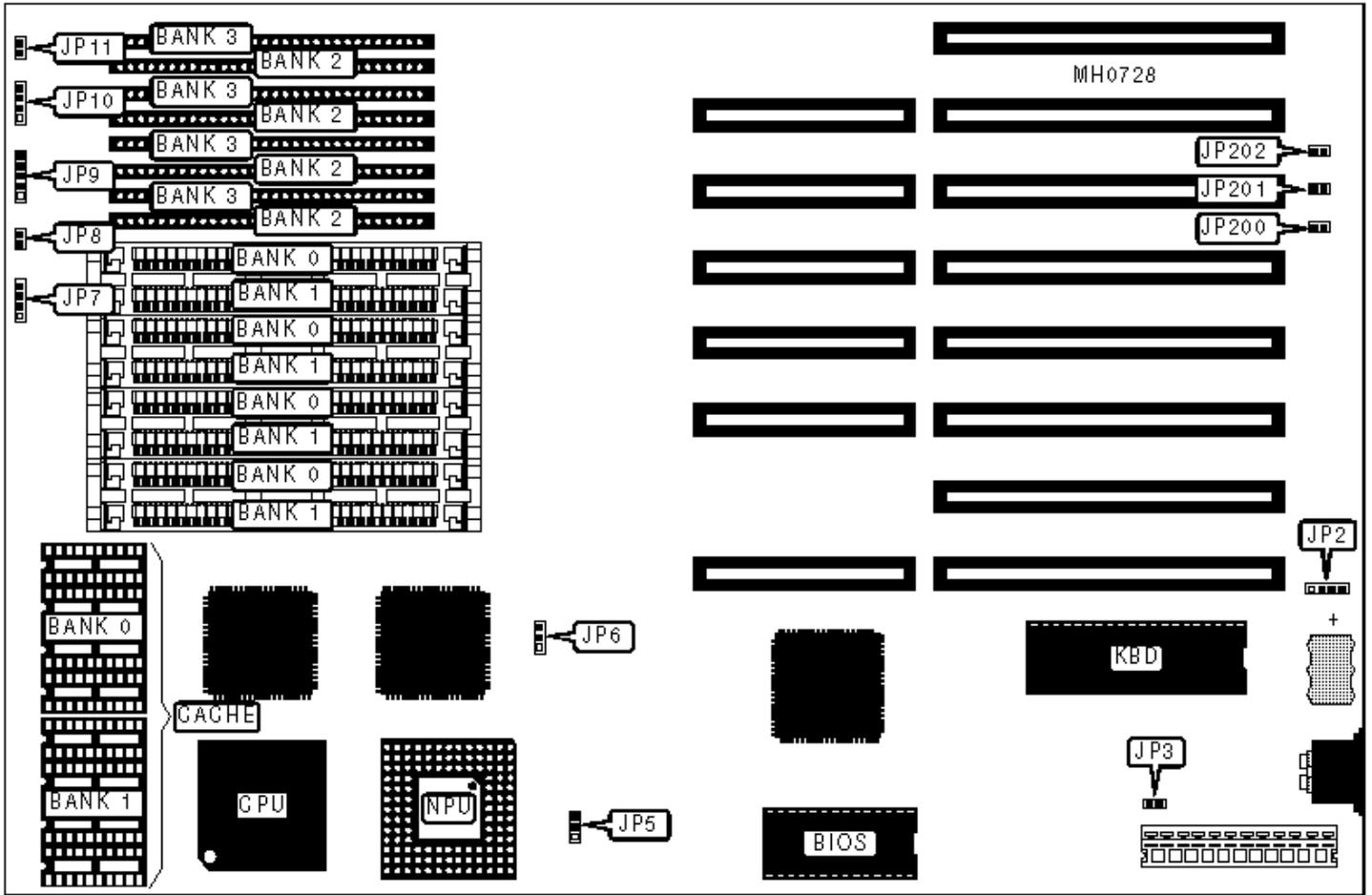


CORE PACIFIC ELECTRONICS, INC.

80386 33/40

Configuration



## CONNECTIONS

Purpose	Location	Purpose	Location
External battery	JP2	Power LED & keylock	JP9
Turbo LED	JP7	Speaker	JP10
Reset switch	JP8	Turbo switch	JP11

## USER CONFIGURABLE SETTINGS

Function	Jumper	Position
» Monitor type select color	JP3	Closed
Monitor type select monochrome	JP3	Open
» NPU mode select synchronous with CPU	JP5	pins 1 & 2 closed
NPU mode select asynchronous with CPU	JP5	pins 2 & 3 closed
» External battery select 3.6VDC	JP200	Closed
External battery select 6.0VDC	JP200	Open
» Battery type select rechargeable	JP201	Open
Battery type select non-rechargeable	JP201	Closed
» CMOS memory normal operation	JP202	Open
CMOS memory clear	JP202	Closed

## DRAM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2	Bank 3
1MB	(4) 256K x 9	NONE	NONE	NONE
2MB	(4) 256K x 9	(4) 256K x 9	NONE	NONE
4MB	(4) 1M x 9	NONE	NONE	NONE
5MB	(4) 256K x 9	(4) 1M x 9	NONE	NONE
8MB	(4) 1M x 9	(4) 1M x 9	NONE	NONE

12MB	(4) 1M x 9	(4) 1M x 9	(4) 1M x 9	NONE
16MB	(4) 1M x 9			

<b>CACHE CONFIGURATION</b>		
Size	Bank 0	Bank 1
32KB	(4) 8K x 8	NONE
64KB	(4) 8K x 8	(4) 8K x 8

<b>CACHE JUMPER CONFIGURATION</b>	
Size	JP6
32KB	pins 1 & 2 closed
64KB	pins 2 & 3 closed