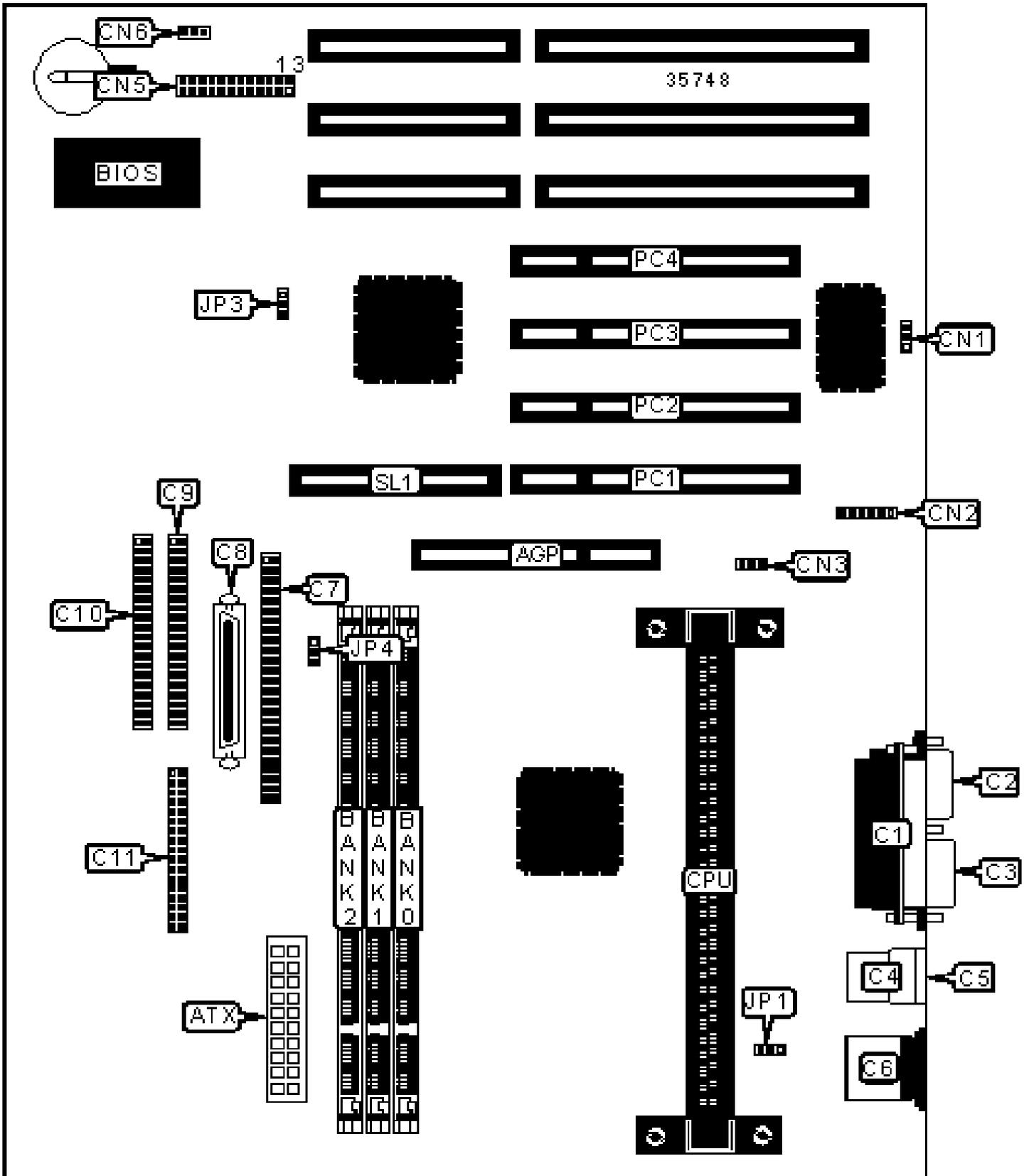


CHAINTECH COMPUTER COMPANY, LTD.

6LTS

Configuration



## CONNECTIONS

Purpose	Location	Purpose	Location
AGP slot	AGP	IR connector	CN2
ATX power connector	ATX	CPU fan power	CN3
Parallel port	C1	Power LED & keylock	CN5/pins 1 - 5
Serial port 2	C2	Speaker	CN5/pins 7 - 10
Serial port 1	C3	Soft off power supply	CN5/pins 11 & 12
USB connector 1	C4	Turbo LED	CN5/pins 13 & 14
USB connector 2	C5	Green PC connector	CN5/pins 16 & 17
PS/2 mouse port	C6	Green PC LED	CN5/pins 18 & 19
SCSI interface	C7	Reset switch	CN5/pins 21 & 22
Wide Ultra SCSI interface	C8	IDE interface LED	CN5/pins 23 & 24
IDE interface 2	C9	Chassis fan power	CN6
IDE interface 1	C10	32-bit PCI slots	PC1 - PC4
Floppy drive interface	C11	RAID slot	SL1
Chassis intrusion connector	CN1		

## USER CONFIGURABLE SETTINGS

Function	Label	Position
» Keyboard power on disabled	JP1	Pins 1 & 2 closed
Keyboard power on enabled	JP1	Pins 2 & 3 closed
» CMOS memory normal operation	JP3	Pins 1 & 2 closed
CMOS memory clear	JP3	Pins 2 & 3 closed
Internal/external SCSI select normal	JP4	Pins 1 & 2 closed
Internal/external SCSI select install internal/external at same time	JP4	Pins 2 & 3 closed

### DIMM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2
8MB	(1) 1M x 64	None	None
16MB	(1) 2M x 64	None	None
16MB	(1) 1M x 64	(1) 1M x 64	None
24MB	(1) 2M x 64	(1) 1M x 64	None
24MB	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64
32MB	(1) 4M x 64	None	None
32MB	(1) 2M x 64	(1) 1M x 64	(1) 1M x 64
32MB	(1) 2M x 64	(1) 2M x 64	None

### DIMM CONFIGURATION (CON'T)

Size	Bank 0	Bank 1	Bank 2
40MB	(1) 4M x 64	(1) 1M x 64	None
40MB	(1) 2M x 64	(1) 2M x 64	(1) 1M x 64
48MB	(1) 4M x 64	(1) 1M x 64	(1) 1M x 64
48MB	(1) 4M x 64	(1) 2M x 64	None
48MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64
56MB	(1) 4M x 64	(1) 2M x 64	(1) 1M x 64
64MB	(1) 8M x 64	None	None
64MB	(1) 4M x 64	(1) 2M x 64	(1) 2M x 64
64MB	(1) 4M x 64	(1) 4M x 64	None
72MB	(1) 8M x 64	(1) 1M x 64	None
72MB	(1) 4M x 64	(1) 4M x 64	(1) 1M x 64
80MB	(1) 8M x 64	(1) 1M x 64	(1) 1M x 64
80MB	(1) 8M x 64	(1) 2M x 64	None

80MB	(1) 4M x 64	(1) 4M x 64	(1) 2M x 64
88MB	(1) 8M x 64	(1) 2M x 64	(1) 1M x 64
96MB	(1) 8M x 64	(1) 2M x 64	(1) 2M x 64
96MB	(1) 8M x 64	(1) 4M x 64	None
96MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64
104MB	(1) 8M x 64	(1) 4M x 64	(1) 1M x 64
112MB	(1) 8M x 64	(1) 4M x 64	(1) 2M x 64
128MB	(1) 16M x 64	None	None
128MB	(1) 8M x 64	(1) 4M x 64	(1) 4M x 64
128MB	(1) 8M x 64	(1) 8M x 64	None
136MB	(1) 16M x 64	(1) 1M x 64	None
136MB	(1) 8M x 64	(1) 8M x 64	(1) 1M x 64
144MB	(1) 16M x 64	(1) 1M x 64	(1) 1M x 64
144MB	(1) 16M x 64	(1) 2M x 64	None
144MB	(1) 8M x 64	(1) 8M x 64	(1) 2M x 64
152MB	(1) 16M x 64	(1) 2M x 64	(1) 1M x 64
160MB	(1) 16M x 64	(1) 2M x 64	(1) 2M x 64
160MB	(1) 16M x 64	(1) 4M x 64	None
160MB	(1) 8M x 64	(1) 8M x 64	(1) 4M x 64
168MB	(1) 16M x 64	(1) 4M x 64	(1) 1M x 64
176MB	(1) 16M x 64	(1) 4M x 64	(1) 2M x 64

**DIMM CONFIGURATION (CON'T)**

Size	Bank 0	Bank 1	Bank 2
192MB	(1) 16M x 64	(1) 4M x 64	(1) 4M x 64
192MB	(1) 16M x 64	(1) 8M x 64	None

192MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64
200MB	(1) 16M x 64	(1) 8M x 64	(1) 1M x 64
208MB	(1) 16M x 64	(1) 8M x 64	(1) 2M x 64
224MB	(1) 16M x 64	(1) 8M x 64	(1) 4M x 64
256MB	(1) 16M x 64	(1) 8M x 64	(1) 8M x 64
384MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64

Note: Board accepts EDO & SDRAM memory.

### CACHE CONFIGURATION

Note: 256KB/512KB cache is located on the Pentium II CPU.