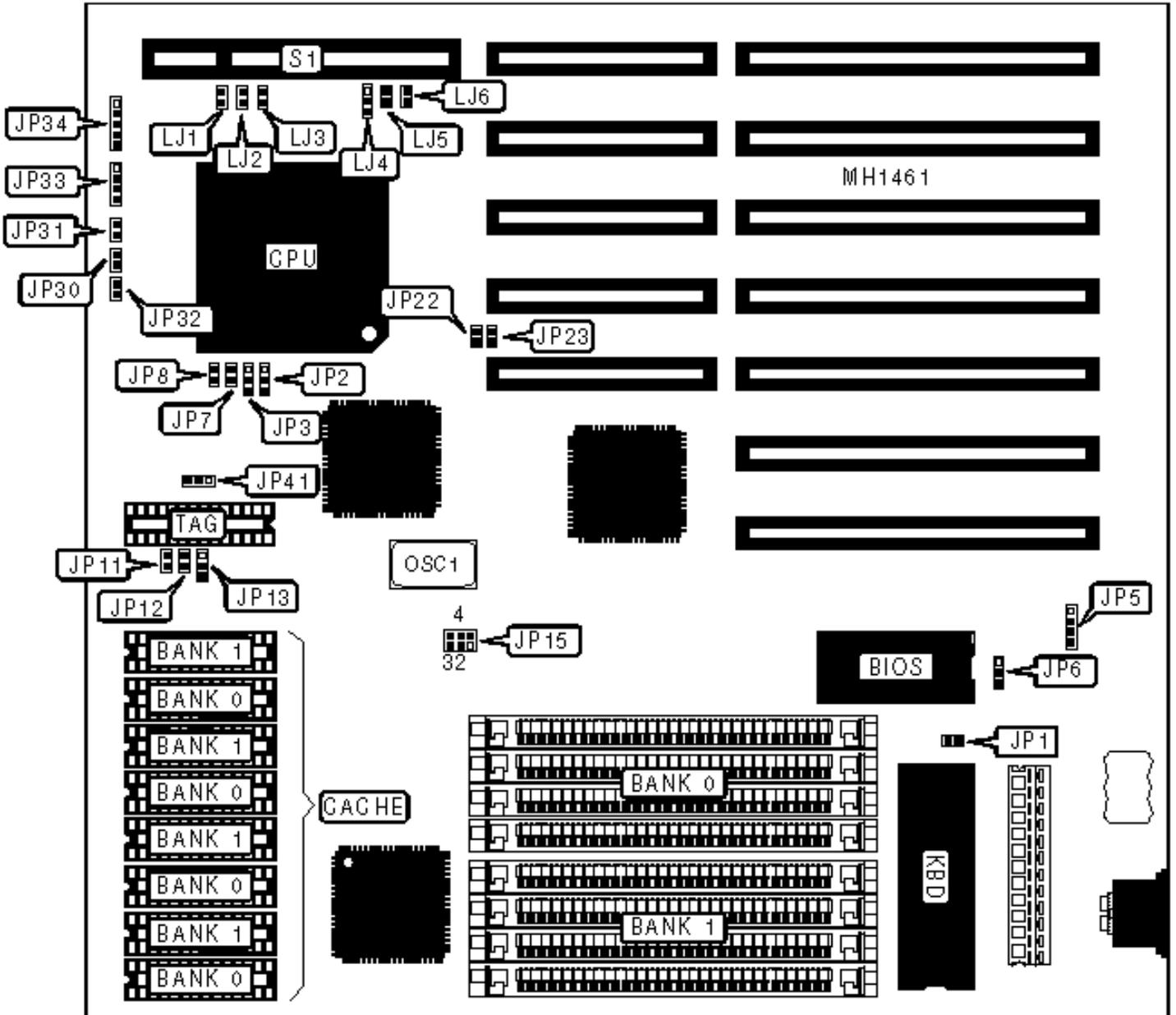


ATLANTIC COMPUTER PRODUCTS, INC.

486 CONTAQ CACHE

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
External battery	JP5	Speaker	JP33
Turbo switch	JP30	Power LED & keylock	JP34
Turbo LED	JP31	32-bit VESA local bus slot	S1
Reset switch	JP32		

USER CONFIGURABLE SETTINGS

Function	Jumper	Position
» Monitor type select color	JP1	Closed
Monitor type select monochrome	JP1	Open
» CMOS memory normal operation	JP6	pins 1 & 2 closed
CMOS memory clear	JP6	pins 2 & 3 closed

DRAM CONFIGURATION

Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION

Size	Bank 0	Bank 1	TAG
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64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION

Size	JP11	JP12	JP13	JP41
64KB	Open	Open	Open	pins 2 & 3 closed
256KB	Closed	Closed	pins 1 & 2 closed	pins 2 & 3 closed

CPU TYPE CONFIGURATION

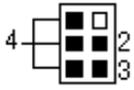
CPU Type	JP2	JP3	JP7	JP8
80486SX	Open	pins 1 & 2 closed	Open	Open
80487SX	pins 1 & 2 closed	pins 2 & 3 closed	Closed	Closed
80486DX	pins 2 & 3 closed	pins 2 & 3 closed	Open	Closed
80486DX2	pins 2 & 3 closed	pins 2 & 3 closed	Open	Closed

CPU SPEED CONFIGURATION

CPU	OSC1	JP15 Type A	JP15 Type B	JP22	JP23
20MHz	40MHz	pins 3 & 4	pins 2 & 3	Open	Open
25MHz	25MHz	pins 2 & 4	pins 2 & 4	Closed	Open
33MHz	33MHz	pins 2 & 4	pins 2 & 4	Open	Closed
50i MHz	25MHz	pins 2 & 4	pins 2 & 4	Closed	Open
50MHz	50MHz	pins 2 & 4	pins 2 & 4	Closed	Closed
66i MHz	33MHz	pins 2 & 4	pins 2 & 4	Open	Closed

JP15 is type A or type B. Pins designated should be in the closed position.

CPU CLOCK SOURCE CONFIGURATION JP15



Type A

CPUCLK = OSG1 : 2 & 4 or 1 & 4 closed
 CPUCLK = OSG1/2 : 3 & 4 closed



Type B

CPUCLK = OSG1 : 2 & 4 or 1 & 2 closed
 CPUCLK = OSG1/2 : 2 & 3 closed

CHIPSET CONFIGURATION

Chipset	Mode	LJ4	LJ5	LJ6
82C591	Synchronous mode	pins 2 & 3 closed	Closed	Open
82C591	Asynchronous mode	pins 1 & 2 closed	Open	Open
82C591A	Synchronous mode	pins 2 & 3 closed	Open	Closed
82C591A	Asynchronous mode	pins 1 & 2 closed	Open	Open

VESA WAIT STATE (ID2) CONFIGURATION

Wait states	CPU speed	LJ1 (ID2)
0 wait states	≤ 33MHz	Open
1 wait state	> 33MHz	Closed

VESA BUS SPEED (ID3) CONFIGURATION

CPU speed	LJ2 (ID3)
≤ 33MHz	Open
> 33MHz	Closed

VESA ID4 CONFIGURATION

Setting	LJ3 (ID4)
Reserved for future use	Closed

