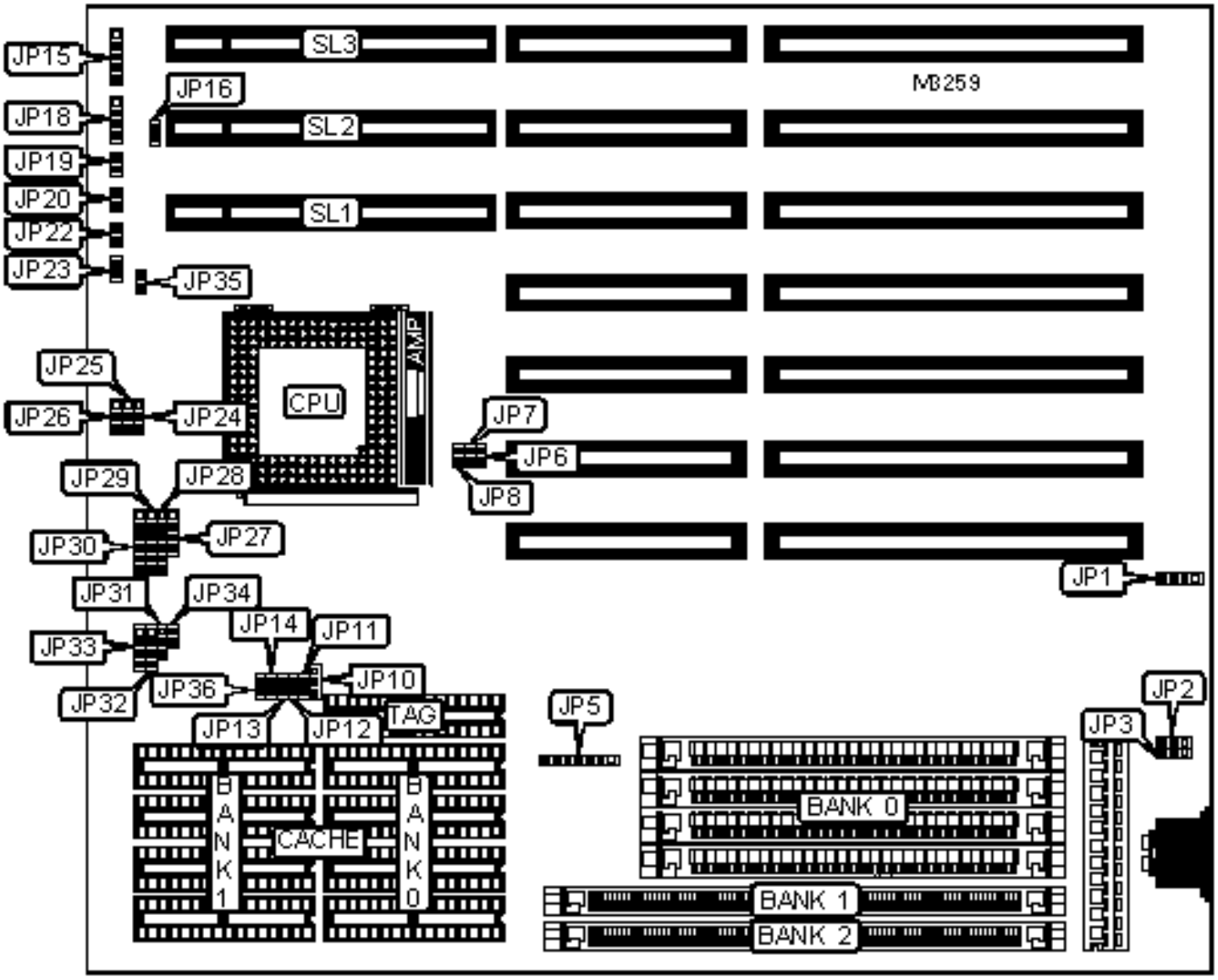
AMPTRON INTERNATIONAL, INC.

DX-6900 VER. 1.75

Configuration



CONNECTIONS

Purpose	Location	Purpose	Location
External battery	JP1	Reset switch	JP20
Power LED & keylock	JP15	Turbo switch	JP22
Speaker	JP18	Green PC connector	JP23
Turbo LED	JP19	32-bit VESA local bus slots	SL1 - SL3

USER CONFIGURABLE SETTINGS

	Function	Jumper	Position
	Battery type select internal	JP1	Pins 2 & 3 closed
	Battery type select external	JP1	Closed
	CMOS memory clear	JP1	Pins 3 & 4 closed
»	Factory configured - do not alter	JP2	Pins 2 & 3 closed
»	Factory configured - do not alter	JP3	Open
»	Factory configured - do not alter	JP36	Closed

DRAM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2
1MB	(4) 256K x 9	None	None
1MB	None	(1) 256K x 36	None
2MB	(4) 256K x 9	(1) 256K x 36	None
2MB	None	(1) 512K x 36	None
2MB	None	(1) 256K x 36	(1) 256K x 36
3MB	(4) 256K x 9	(1) 256K x 36	(1) 256K x 36
3MB	None	(1) 512K x 36	(1) 256K x 36
3MB	None	(1) 256K x 36	(1) 512K x 36

4MB	(4) 1M x 9	None	None
4MB	None	(1) 512K x 36	(1) 512K x 36
4MB	None	(1) 1M x 36	None
5MB	(4) 1M x 9	(1) 256K x 36	None
5MB	None	(1) 256K x 36	(1) 1M x 36
5MB	None	(1) 1M x 36	(1) 256K x 36
6MB	(4) 256K x 9	(1) 256K x 36	(1) 1M x 36
6MB	(4) 1M x 9	(1) 256K x 36	(1) 256K x 36
6MB	None	(1) 512K x 36	(1) 1M x 36
6MB	None	(1) 1M x 36	(1) 512K x 36
8MB	None	(1) 1M x 36	(1) 1M x 36
8MB	None	(1) 2M x 36	None
9MB	(4) 1M x 9	(1) 256K x 36	(1) 1M x 36
9MB	None	(1) 256K x 36	(1) 2M x 36
9MB	None	(1) 2M x 36	(1) 256K x 36
10MB	None	(1) 512K x 36	(1) 2M x 36
10MB	None	(1) 2M x 36	(1) 512K x 36
12MB	None	(1) 1M x 36	(1) 2M x 36
12MB	None	(1) 2M x 36	(1) 1M x 36
16MB	(4) 4M x 9	None	None
16MB	None	(1) 4M x 36	None
16MB	None	(1) 2M x 36	(1) 2M x 36
17MB	(1) 4M x 36	(1) 256K x 36	None
17MB	None	(1) 256K x 36	(1) 4M x 36
17MB	None	(1) 4M x 36	(1) 256K x 36
18MB	(4) 256K x 9	(1) 256K x 36	(1) 4M x 36

DRAM CONFIGURATION (CON'T)			
Size	Bank 0	Bank 1	Bank 2
18MB	(4) 4M x 9	(1) 256K x 36	(1) 256K x 36
18MB	None	(1) 512K x 36	(1) 4M x 36
18MB	None	(1) 4M x 36	(1) 512K x 36
20MB	None	(1) 1M x 36	(1) 4M x 36
20MB	None	(1) 4M x 36	(1) 1M x 36
21MB	(4) 1M x 9	(1) 256K x 36	(1) 4M x 36
21MB	(4) 4M x 9	(1) 256K x 36	(1) 1M x 36
24MB	None	(1) 4M x 36	(1) 2M x 36
24MB	None	(1) 2M x 36	(1) 4M x 36
32MB	None	(1) 8M x 36	None
32MB	None	(1) 4M x 36	(1) 4M x 36
33MB	(4) 4M x 9	(1) 256K x 36	(1) 4M x 36
33MB	None	(1) 256K x 36	(1) 8M x 36
33MB	None	(1) 8M x 36	(1) 256K x 36
34MB	None	(1) 512K x 36	(1) 8M x 36
34MB	None	(1) 8M x 36	(1) 512K x 36
36MB	None	(1) 1M x 36	(1) 8M x 36
36MB	None	(1) 8M x 36	(1) 1M x 36
40MB	None	(1) 8M x 36	(1) 2M x 36
40MB	None	(1) 2M x 36	(1) 8M x 36
48MB	None	(1) 8M x 36	(1) 4M x 36
48MB	None	(1) 4M x 36	(1) 8M x 36
64MB	(4) 16M x 9	None	None

64MB	None	(1) 16M x 36	None
64MB	None	(1) 8M x 36	(1) 8M x 36

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	None	(1) 8K x 8
256KB (A)	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8
256KB (B)	(4) 64K x 8	None	(1) 32K x 8
512KB (A)	(4) 64K x 8	(4) 64K x 8	(1) 32K x 8
512KB (B)	(4) 128K x 8	None	(1) 32K x 8
1MB	(4) 128K x 8	(4) 128K x 8	(1) 64K x 8

CACHE JUMPER CONFIGURATION						
Size	JP5	JP10	JP11	JP12	JP13	JP14
64KB	2 & 3	2 & 3	Open	Open	Open	Open
128KB	1 & 2	1 & 2	Open	Open	Open	Closed
256KB (A)	2 & 3	2 & 3	Open	Open	Closed	Closed
256KB (B)	1 & 2, 3 & 4	1 & 2	Open	Open	Closed	Closed
512KB (A)	2 & 3, 4 & 5	2 & 3	Open	Closed	Closed	Closed
512KB (B)	1 & 2, 3 & 4, 5 & 6	1 & 2	Open	Closed	Closed	Closed
1MB	2 & 3, 4 & 5, 6 & 7	2 & 3	Closed	Closed	Closed	Closed
Note: Pins designated should be in the closed position.						

CPU TYPE CONFIGURATION				
Type	JP27	JP28	JP29	JP30

AM486SX	Open	2 & 3	Open	Open
80486SX	Open	2 & 3	Open	Open
CX486DX	2 & 3	1 & 2, 3 & 4, 5 & 6	1 & 2, 3 & 4, 5 & 6	2 & 3, 4 & 5
AM486DX	Open	2 & 3	Open	Open
IBM486DX	2 & 3	1 & 2, 3 & 4, 5 & 6	1 & 2, 3 & 4, 5 & 6	2 & 3, 4 & 5
TI486DX	2 & 3	1 & 2, 3 & 4, 5 & 6	1 & 2, 3 & 4, 5 & 6	2 & 3, 4 & 5
80486DX	Open	2 & 3	Open	Open
SL80486DX	1 & 2, 3 & 4	1 & 2	1 & 2	5 & 6
CX486DX2	2 & 3	1 & 2, 3 & 4, 5 & 6	1 & 2, 3 & 4, 5 & 6	2 & 3, 4 & 5
CX486DX2-V80	2 & 3	1 & 2, 3 & 4, 5 & 6	1 & 2, 3 & 4, 5 & 6	2 & 3, 4 & 5
AM486DX2	Open	2 & 3	Open	Open
AM486DX2(3.3v)	Open	2 & 3	Open	Open
IBM486DX2	2 & 3	1 & 2, 3 & 4, 5 & 6	1 & 2, 3 & 4, 5 & 6	2 & 3, 4 & 5
TI486DX2	2 & 3	1 & 2, 3 & 4, 5 & 6	1 & 2, 3 & 4, 5 & 6	2 & 3, 4 & 5
80486DX2	Open	2 & 3	Open	Open
SL80486DX2	1 & 2, 3 & 4	1 & 2	1 & 2	5 & 6
CX486DX4-GP	2 & 3	1 & 2, 3 & 4, 5 & 6	1 & 2, 3 & 4, 5 & 6	2 & 3, 4 & 5
CX486DX4-GP4	1 & 2, 3 & 4	1 & 2, 4 & 5	1 & 2, 4 & 5	3 & 4, 5 & 6
AM486DX4 (3.3v)	Open	2 & 3	Open	Open
AM486DX4 (8B)	1 & 2, 3 & 4	1 & 2, 4 & 5	1 & 2, 4 & 5	3 & 4, 5 & 6
IBM486DX4-GC	2 & 3	1 & 2, 3 & 4, 5 & 6	1 & 2, 3 & 4, 5 & 6	2 & 3, 4 & 5
IBM486DX4-GIC	1 & 2, 3 & 4	1 & 2, 4 & 5	1 & 2, 4 & 5	3 & 4, 5 & 6
80486DX4	1 & 2, 3 & 4	1 & 2	1 & 2	5 & 6
AM5X86-133	1 & 2, 3 & 4	1 & 2, 4 & 5	1 & 2, 4 & 5	3 & 4, 5 & 6

Note: Pins designated should be in the closed position.

CPU TYPE CONFIGURATION (CON'T)

Type	JP31	JP32	JP33	JP34	JP35
AM486SX	Open	Open	2 & 3	Closed	Closed
80486SX	Open	Open	2 & 3	Closed	Closed
CX486DX	Open	1 & 2	1 & 2, 3 & 4	Open	Closed
AM486DX	Open	1 & 2	1 & 2, 3 & 4	Closed	Closed
IBM486DX	Open	1 & 2	1 & 2, 3 & 4	Open	Closed
TI486DX	Open	1 & 2	1 & 2, 3 & 4	Open	Closed
80486DX	Open	1 & 2	1 & 2, 3 & 4	Closed	Closed
SL80486DX	Open	1 & 2	1 & 2, 3 & 4	Open	Closed
CX486DX2	Open	1 & 2	1 & 2, 3 & 4	Open	Closed
CX486DX2-V80	Open	1 & 2	1 & 2, 3 & 4	Open	Open
AM486DX2	Open	1 & 2	1 & 2, 3 & 4	Closed	Closed
AM486DX2(3.3v)	Open	1 & 2	1 & 2, 3 & 4	Closed	Closed
IBM486DX2	Open	1 & 2	1 & 2, 3 & 4	Open	Closed
TI486DX2	Open	1 & 2	1 & 2, 3 & 4	Open	Closed
80486DX2	Open	1 & 2	1 & 2, 3 & 4	Closed	Closed
SL80486DX2	Open	1 & 2	1 & 2, 3 & 4	Open	Closed
CX486DX4-GP	Open	1 & 2	1 & 2, 3 & 4	Open	Closed
CX486DX4-GP4	Open	1 & 2	1 & 2, 3 & 4	Open	Closed
AM486DX4 (3.3v)	Open	1 & 2	1 & 2, 3 & 4	Open	Closed
AM486DX4 (8B)	Open	1 & 2	1 & 2, 3 & 4	Open	Closed
IBM486DX4-GC	Open	1 & 2	1 & 2, 3 & 4	Open	Closed
IBM486DX4-GIC	Open	1 & 2	1 & 2, 3 & 4	Open	Closed
80486DX4	Open	1 & 2	1 & 2, 3 & 4	Open	Closed
AM5X86-133	2 & 3	1 & 2	1 & 2, 3 & 4	Open	Closed

Note: Pins designated should be in the closed position.

CPU VOLTAGE CONFIGURATION

Voltage	JP24	JP25	JP26
3.3v	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
4v	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
5v	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed

CPU SPEED CONFIGURATION

Speed	JP6	JP7	JP8
25MHz	Open	Open	Closed
33MHz	Closed	Closed	Closed
40MHz	Open	Closed	Closed
50iMHz	Open	Open	Closed
50MHz	Closed	Open	Open
66iMHz	Closed	Closed	Closed
80iMHz	Open	Closed	Closed
100iMHz	Closed	Closed	Closed
120iMHz	Open	Closed	Closed
133iMHz	Closed	Closed	Closed

VL BUS WAIT STATE CONFIGURATION

Wait states	JP17
0 wait states	Open
1 wait state	Closed

Note: The location of JP17 is unidentified.

VL BUS SPEED CONFIGURATION	
CPU speed	JP16
<= 33MHz	Open
> 33MHz	Closed