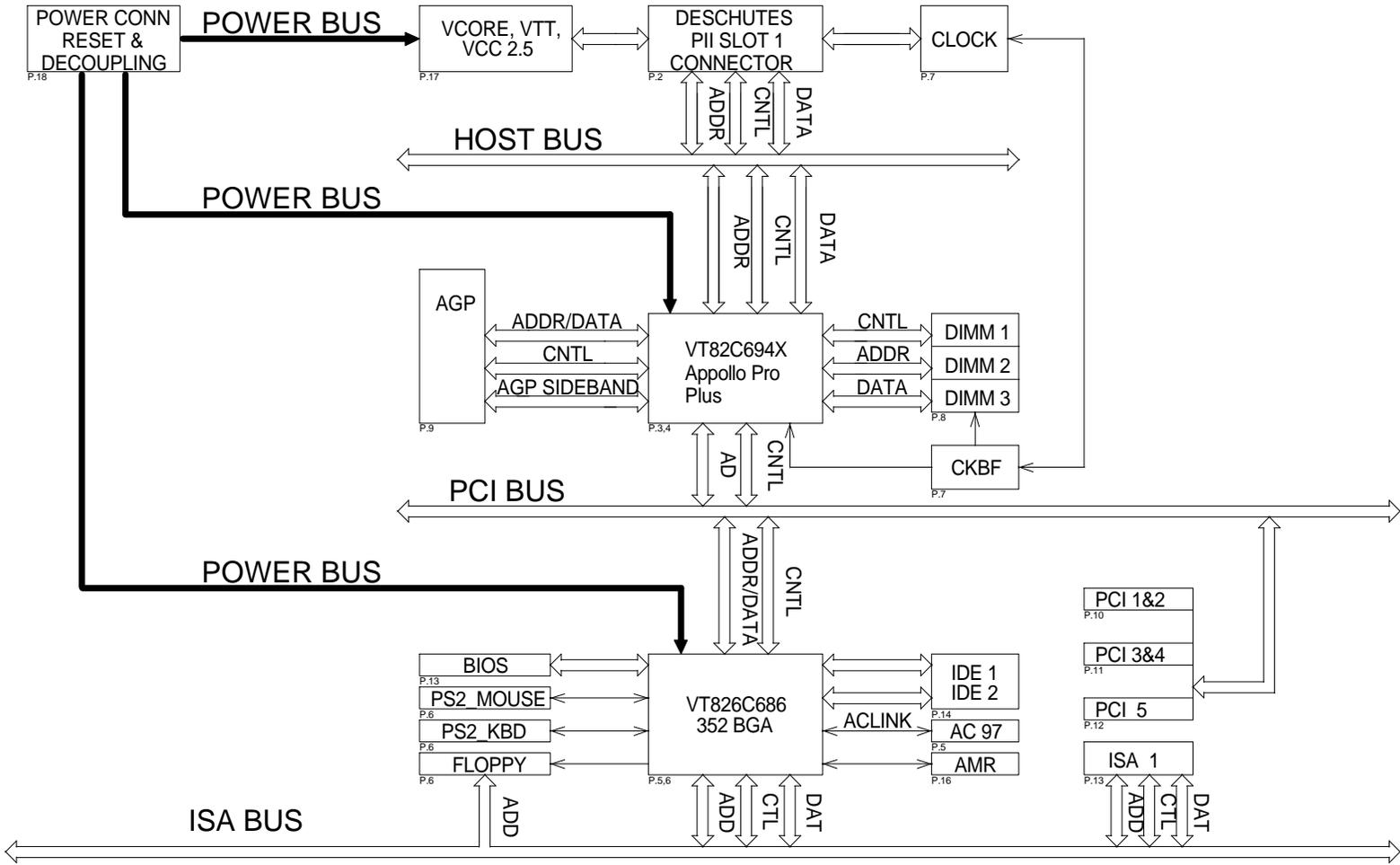


# GMB-6292 V0 RD SCHEMATIC DRAWING

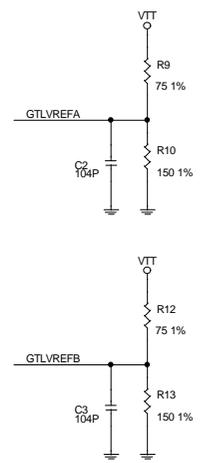
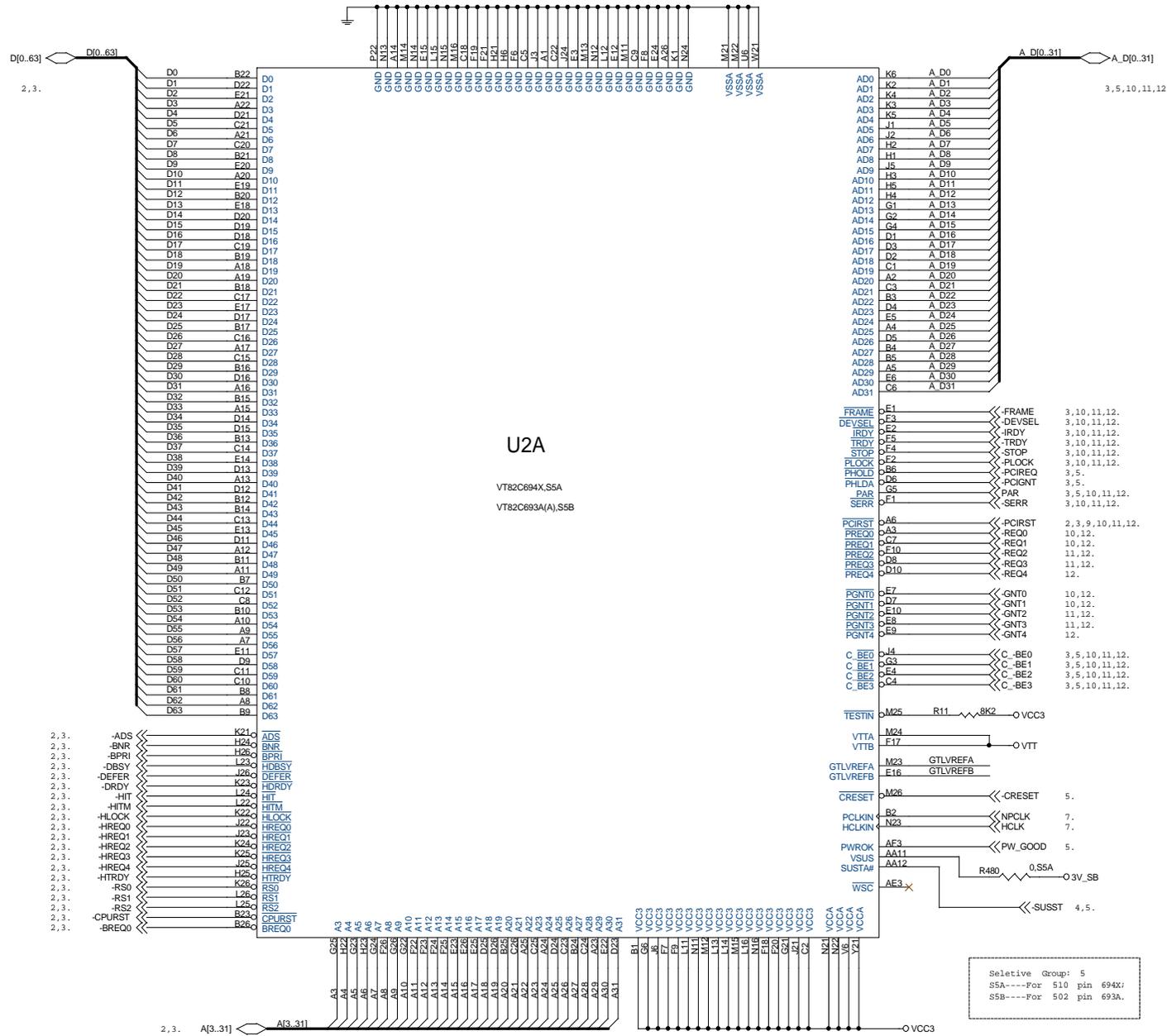
Rev D Released on 21 February 2000



- | LINK
- | 02\_CPU
- | 03\_694A
- | 04\_694B
- | 05\_686A
- | 06\_686B
- | 07\_CLK
- | 08\_DIMM
- | 09\_AGP
- | 10\_PCI12
- | 11\_PCI34
- | 12\_PCI5
- | 13\_ISA
- | 14\_PORT
- | 15\_AUDIO
- | 16\_AMR
- | 17\_CPUPWR
- | 18\_ATX

<b>Gemlight Computer Ltd.</b>			
Title BLOCK DIAGRAM AND VERSION CONTROL			
Size B	Document Number GMB-6292 V0 RD	Rev D	
Date: Monday, February 21, 2000	Sheet 1	of 18	



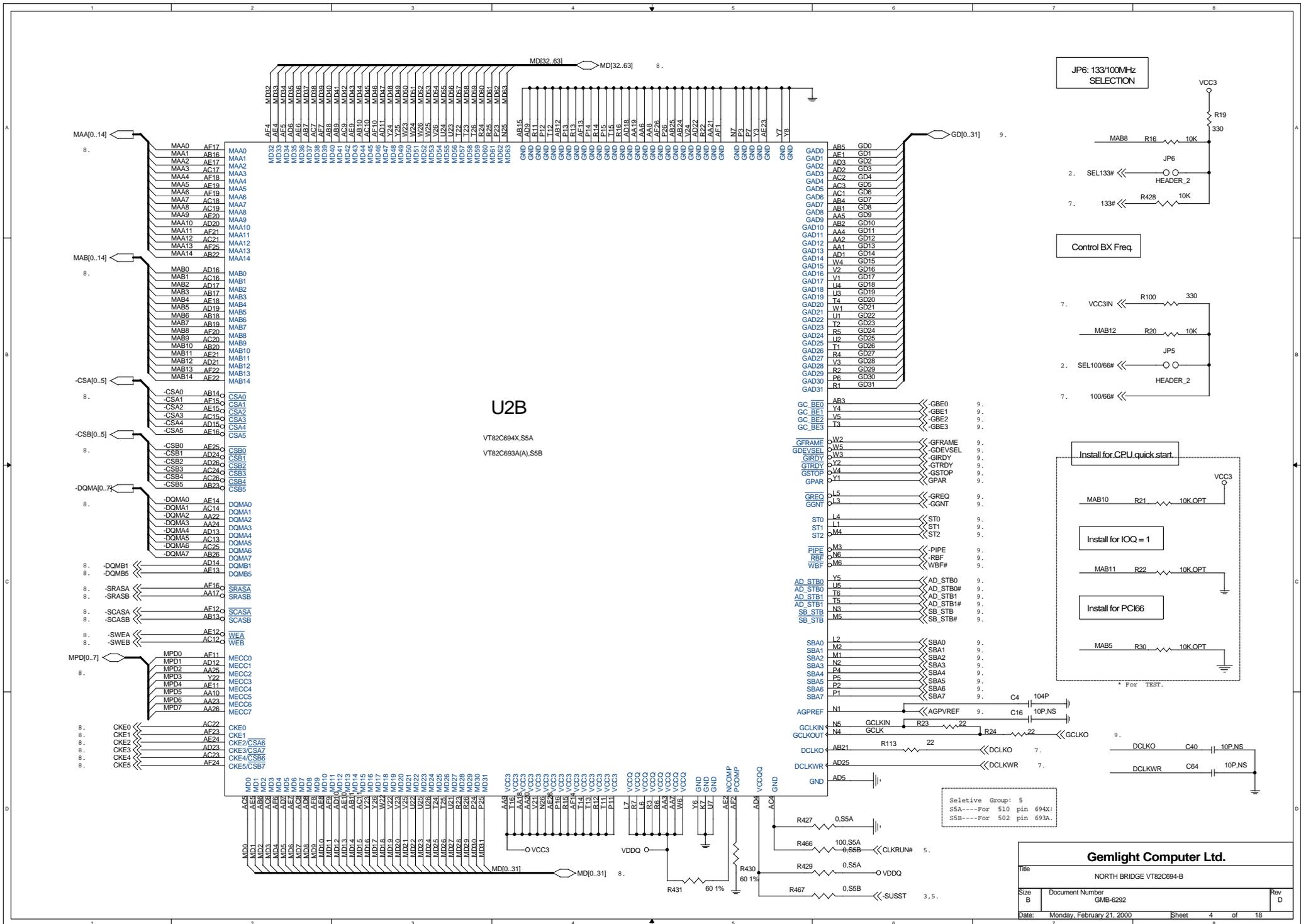


U2A

VT82C694X,SSA  
VT82C693A(A),SSB

Selective Group: 5  
SSA----For 510 pin 694X;  
SSB----For 502 pin 693A.

<b>Gemlight Computer Ltd.</b>		
Title NORTH BRIDGE VT82C694-A		
Size B	Document Number GMB-6292	Rev D
Date Monday, February 21, 2000	Sheet 3	of 18



**U2B**

VT82C694X, S5A  
VT82C693A(A), S5B

JP6: 133/100MHz SELECTION

Control BX Freq.

Install for CPU quick start.

Install for IOQ = 1

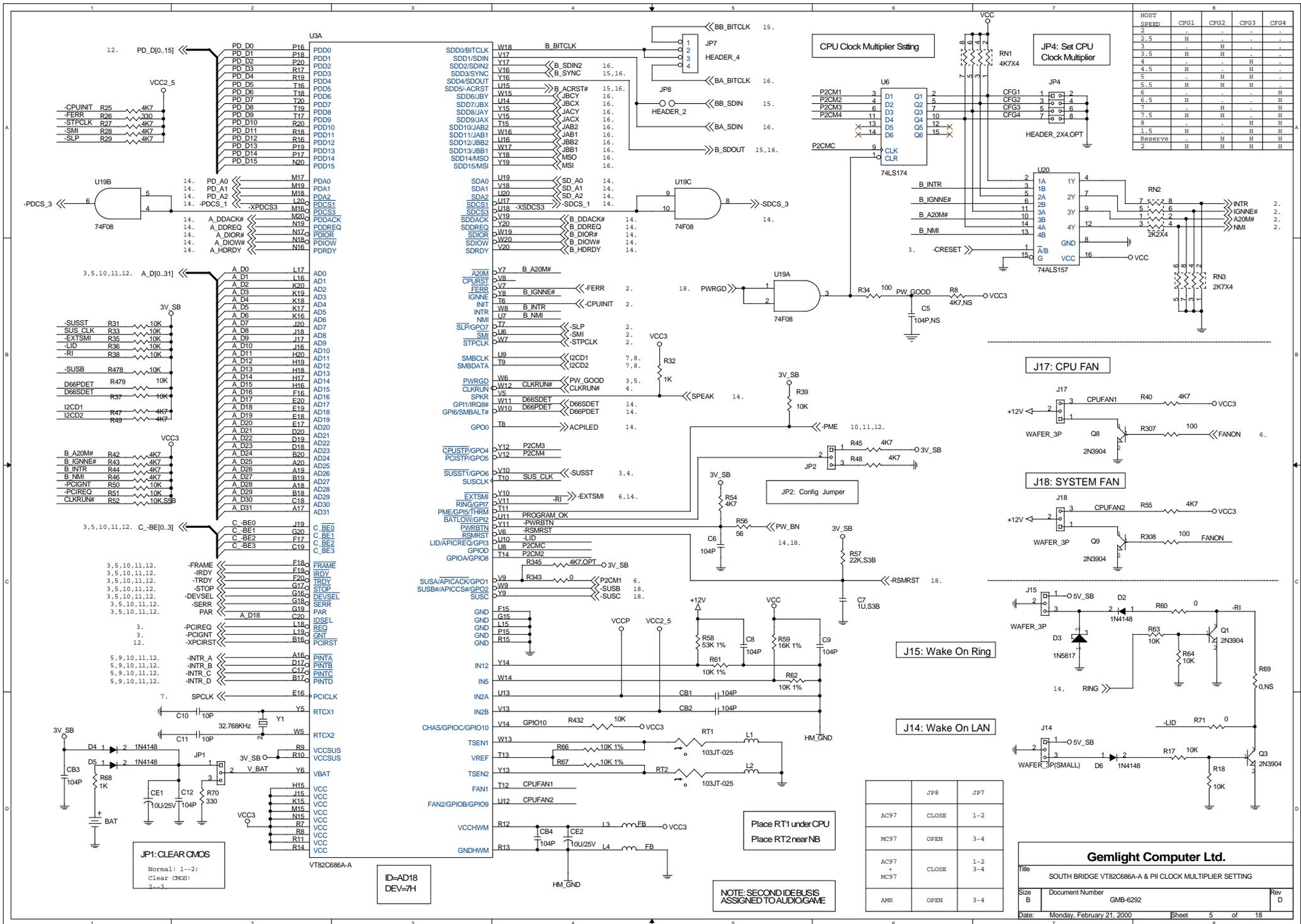
Install for PCI66

Selective Group: 5  
S5A----For 510 pin 694X;  
S5B----For 502 pin 693A.

**Gemlight Computer Ltd.**

Title NORTH BRIDGE VT82C694-B

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Date: Monday, February 21, 2000	Sheet 4	of 18



HOST SPEED	CPG1	CPG2	CPG3	CPG4
2	.	.	.	.
2.5	H	.	.	.
3	.	H	.	.
3.5	H	H	.	.
4	.	.	H	.
4.5	H	.	H	.
5	.	H	H	.
5.5	H	H	H	.
6	.	.	.	H
6.5	H	.	.	H
7	.	H	.	H
7.5	H	H	.	H
8	.	.	H	H
1.5	H	.	H	H
Reserve	.	H	H	H
2	H	H	H	H

	JP8	JP7
AC'97	CLOSE	1-2
NC'97	OPEN	3-4
AC'97 + NC'97	CLOSE	1-2 3-4
AMR	OPEN	3-4

Gemlight Computer Ltd.			
Title SOUTH BRIDGE VT82C686A-A & PII CLOCK MULTIPLIER SETTING			
Size	Document Number	Rev D	
B	GMB-6292		
Date:	Monday, February 21, 2000	Sheet	5 of 18

NOTE: SECOND IDEBUS ASSIGNED TO AUDIOGAME

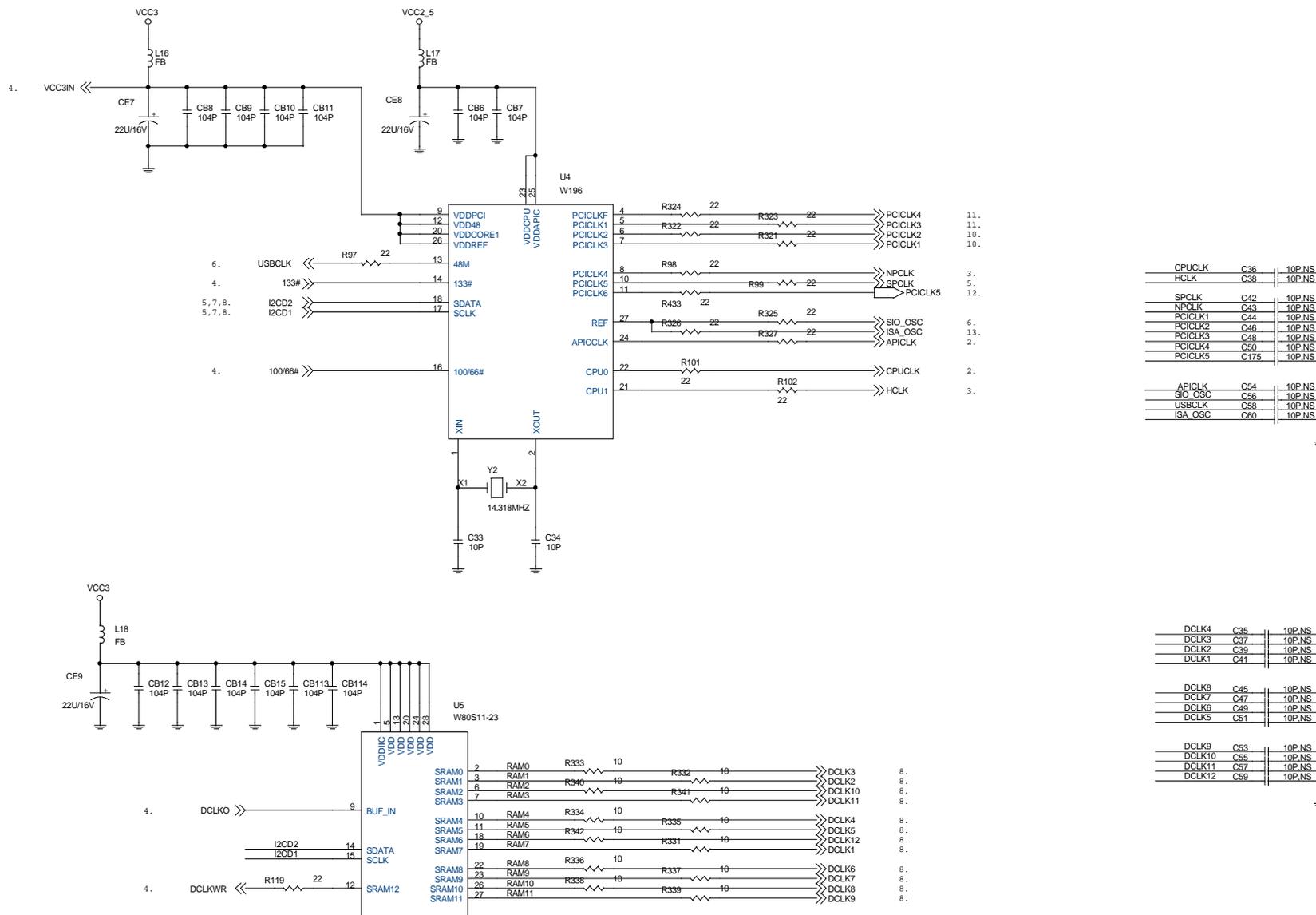
Place RT1 under CPU  
Place RT2 near NB

ID=AD18  
DEV=7H

JP1: CLEAR CMOS  
Normal: 1-2;  
Clear CMOS: 2-3.



# CLOCK GENERATOR & BUFFER

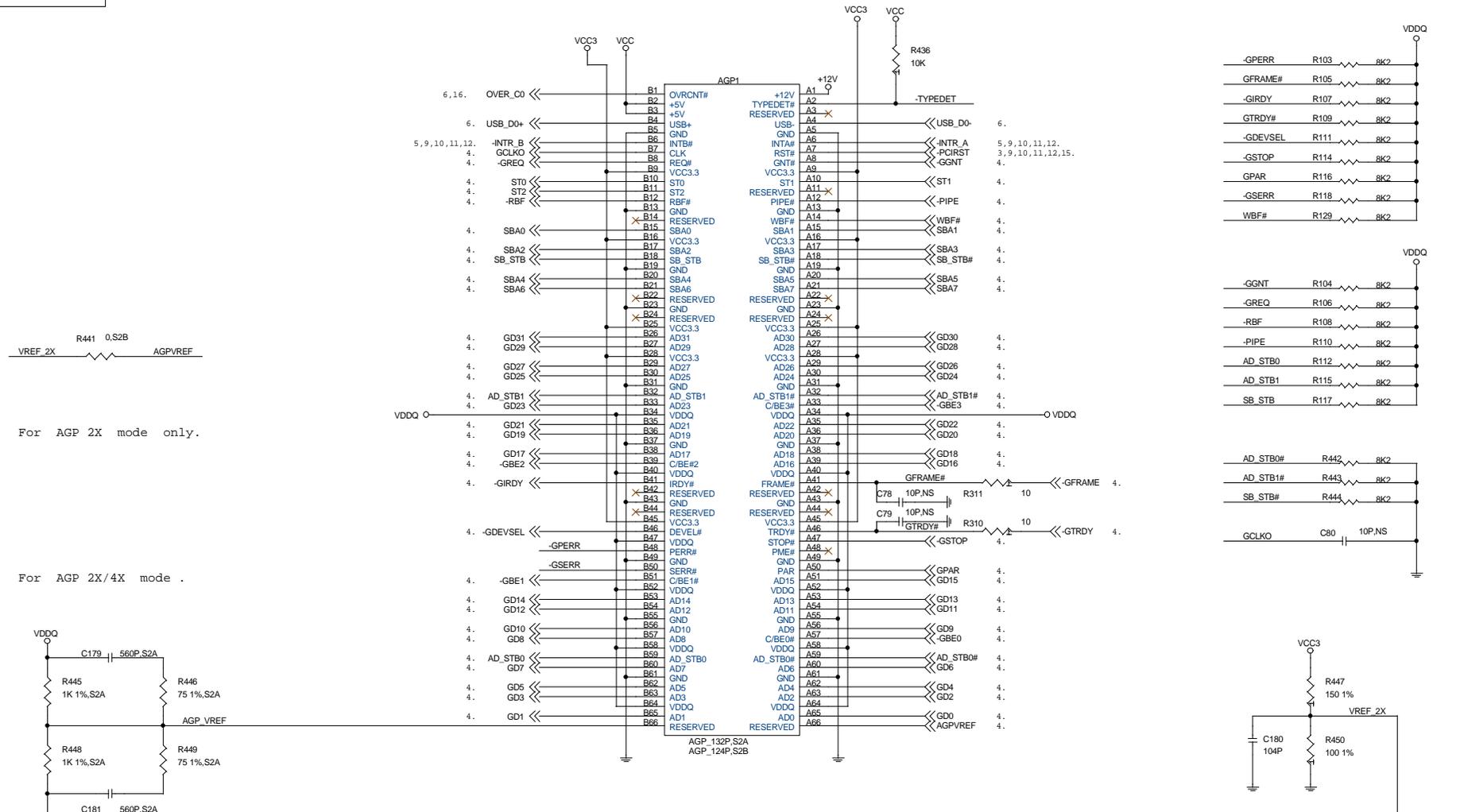


CPUCLK	C36	10P.NS
HCLK	C38	10P.NS
SPCLK	C42	10P.NS
NPCLK	C43	10P.NS
PCICLK1	C44	10P.NS
PCICLK2	C46	10P.NS
PCICLK3	C48	10P.NS
PCICLK4	C50	10P.NS
PCICLK5	C175	10P.NS
APICLK	C54	10P.NS
SIO_OSC	C56	10P.NS
USBCLK	C58	10P.NS
ISA_OSC	C60	10P.NS

DCLK4	C35	10P.NS
DCLK3	C37	10P.NS
DCLK2	C39	10P.NS
DCLK1	C41	10P.NS
DCLK8	C45	10P.NS
DCLK7	C47	10P.NS
DCLK6	C49	10P.NS
DCLK5	C51	10P.NS
DCLK9	C53	10P.NS
DCLK10	C55	10P.NS
DCLK11	C57	10P.NS
DCLK12	C59	10P.NS

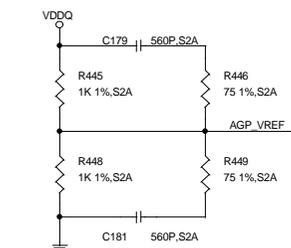


# AGP SLOT

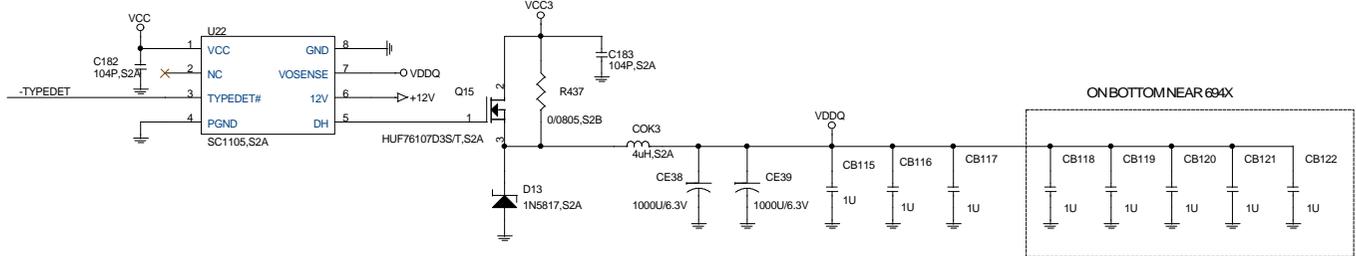
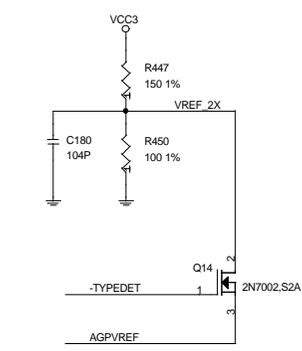
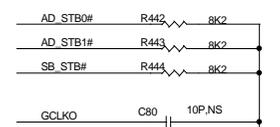
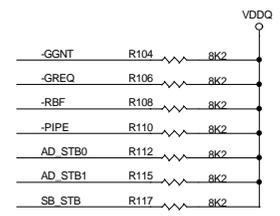
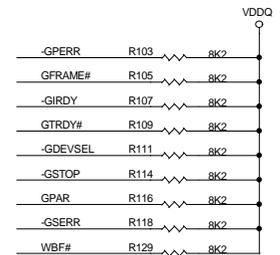


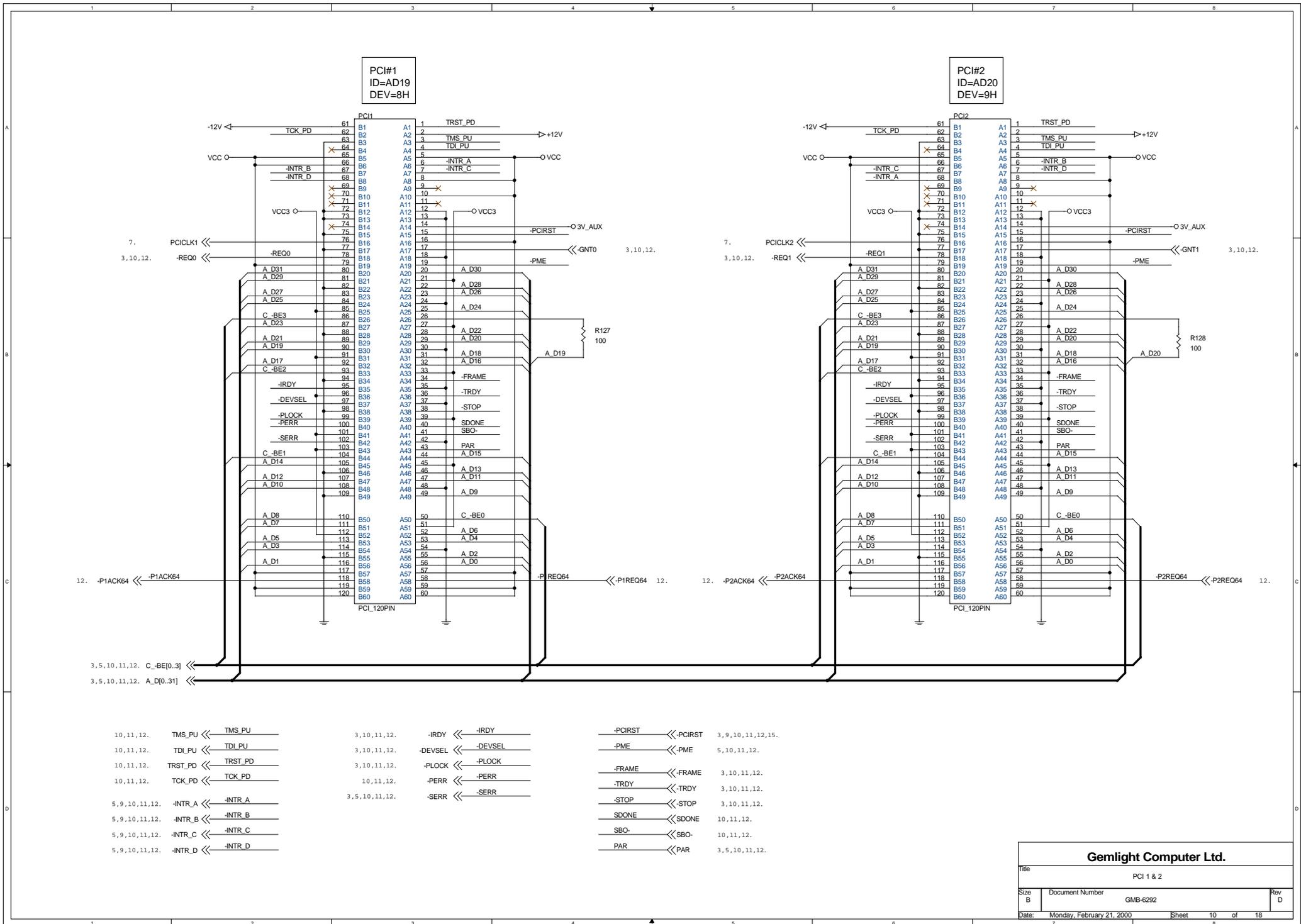
For AGP 2X mode only.

For AGP 2X/4X mode.



Selective Group: 2  
 S2A----For Support AGP2X/4X;  
 S2B----For Only Support AGP2X.





PCI#1  
ID=AD19  
DEV=8H

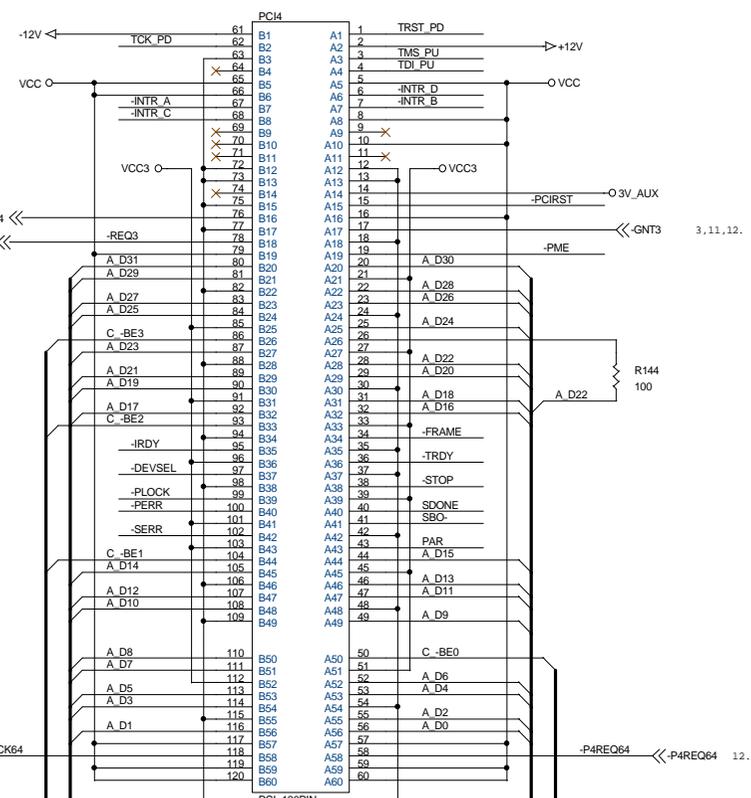
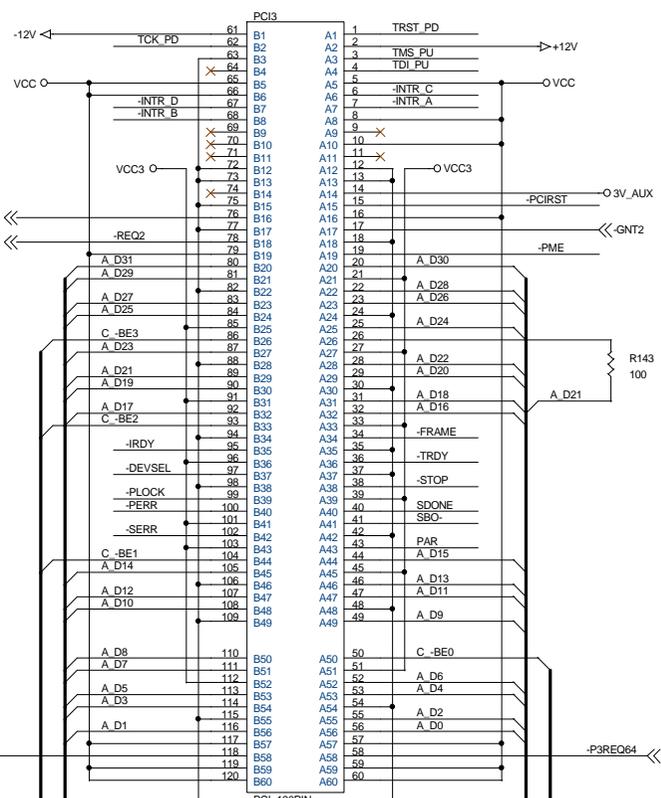
PCI#2  
ID=AD20  
DEV=9H

- |               |                    |               |                    |                    |                  |
|---------------|--------------------|---------------|--------------------|--------------------|------------------|
| 10,11,12      | TMS_PU << TMS_PU   | 3,10,11,12    | -IRDY << -IRDY     | -PCIRST << -PCIRST | 3,9,10,11,12,15. |
| 10,11,12      | TDI_PU << TDI_PU   | 3,10,11,12    | -DEVSEL << -DEVSEL | -PME << -PME       | 5,10,11,12.      |
| 10,11,12      | TRST_PD << TRST_PD | 3,10,11,12    | -PLOCK << -PLOCK   | -FRAME << -FRAME   | 3,10,11,12.      |
| 10,11,12      | TCK_PD << TCK_PD   | 10,11,12.     | -PERR << -PERR     | -TRDY << -TRDY     | 3,10,11,12.      |
| 5,9,10,11,12. | -INTR_A << -INTR_A | 3,5,10,11,12. | -SERR << -SERR     | -STOP << -STOP     | 3,10,11,12.      |
| 5,9,10,11,12. | -INTR_B << -INTR_B |               |                    | -SDONE << -SDONE   | 10,11,12.        |
| 5,9,10,11,12. | -INTR_C << -INTR_C |               |                    | -SBO << -SBO       | 10,11,12.        |
| 5,9,10,11,12. | -INTR_D << -INTR_D |               |                    | PAR << PAR         | 3,5,10,11,12.    |

<b>Gemlight Computer Ltd.</b>		
Title PCI 1 & 2		
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PCI#3  
ID=AD21  
DEV=AH

PCI#4  
ID=AD22  
DEV=BH



10, 11, 12. TMS\_PU << TMS\_PU  
10, 11, 12. TDI\_PU << TDI\_PU  
10, 11, 12. TRST\_PD << TRST\_PD  
10, 11, 12. TCK\_PD << TCK\_PD  
5, 9, 10, 11, 12. -INTR\_A << -INTR\_A  
5, 9, 10, 11, 12. -INTR\_B << -INTR\_B  
5, 9, 10, 11, 12. -INTR\_C << -INTR\_C  
5, 9, 10, 11, 12. -INTR\_D << -INTR\_D

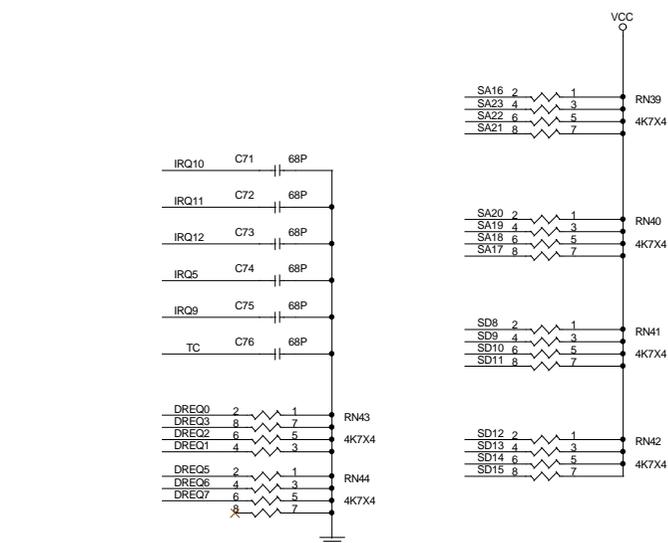
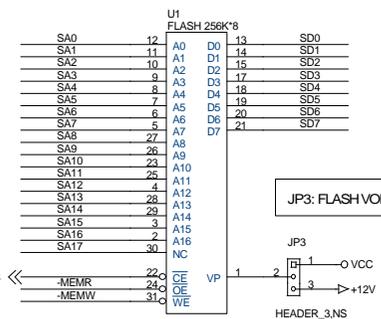
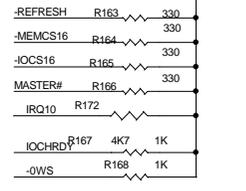
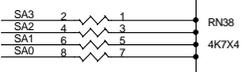
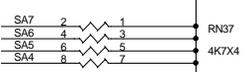
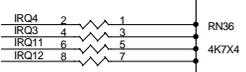
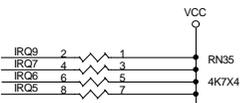
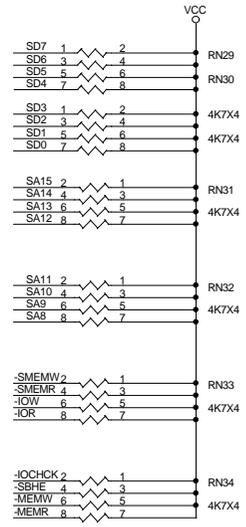
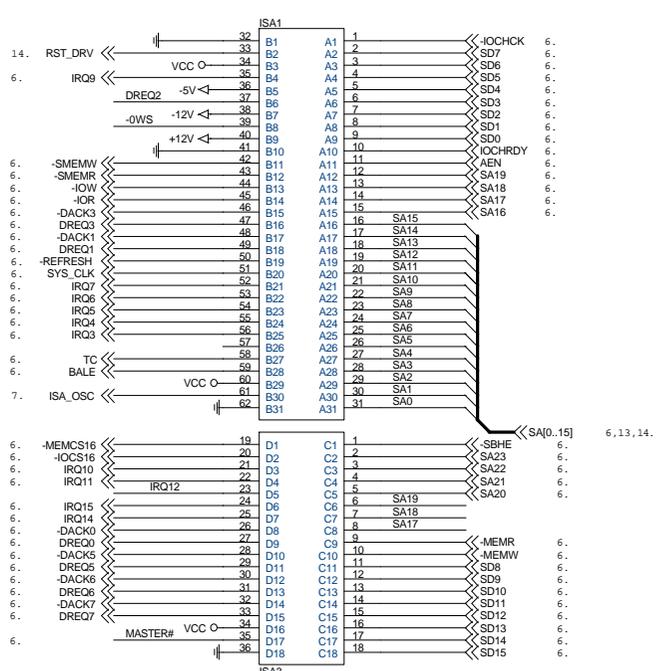
3, 10, 11, 12. -IRDY << -IRDY  
3, 10, 11, 12. -DEVSEL << -DEVSEL  
3, 10, 11, 12. -PLOCK << -PLOCK  
10, 11, 12. -PERR << -PERR  
3, 5, 10, 11, 12. -SERR << -SERR

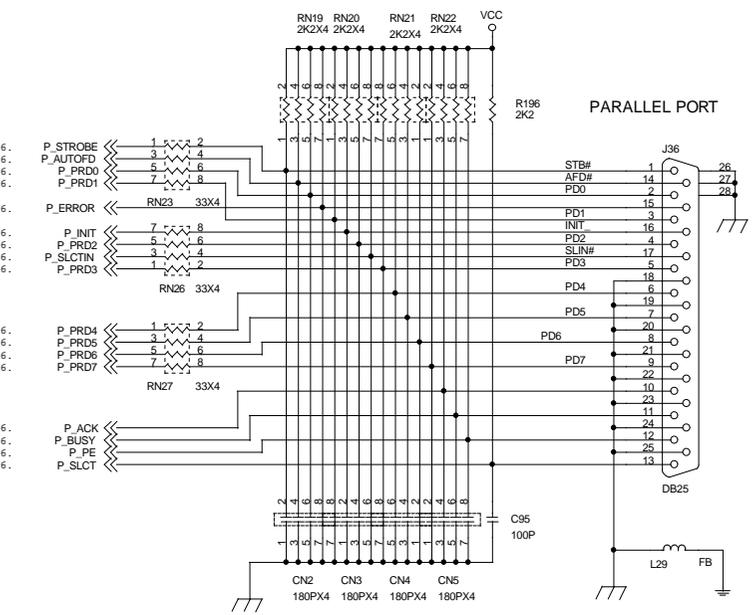
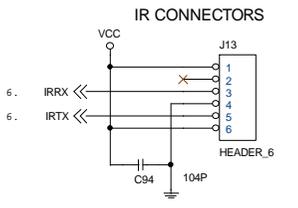
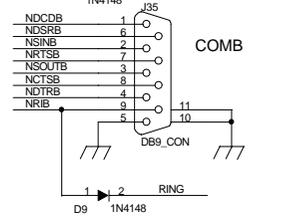
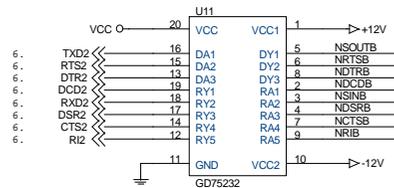
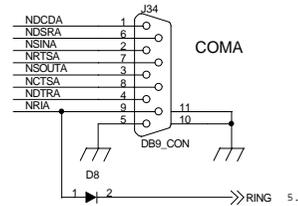
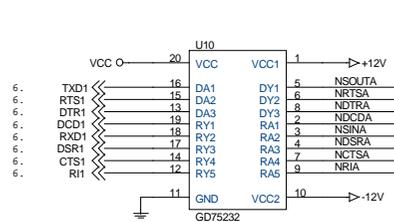
-PCIRST << PCIRST 3, 9, 10, 11, 12, 15.  
-PME << PME 5, 10, 11, 12.  
-FRAME << FRAME 3, 10, 11, 12.  
-TRDY << TRDY 3, 10, 11, 12.  
-STOP << STOP 3, 10, 11, 12.  
-SDONE << SDONE 10, 11, 12.  
-SBO << SBO 10, 11, 12.  
-PAR << PAR 3, 5, 10, 11, 12.

**Gemlight Computer Ltd.**

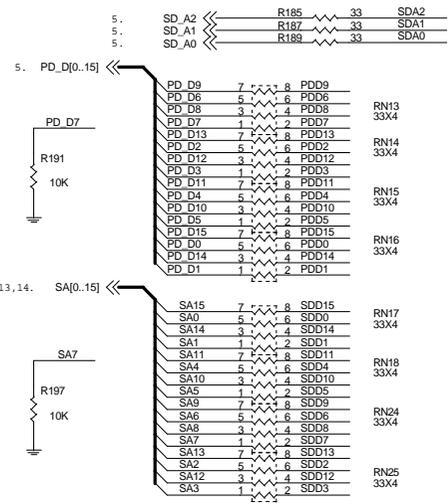
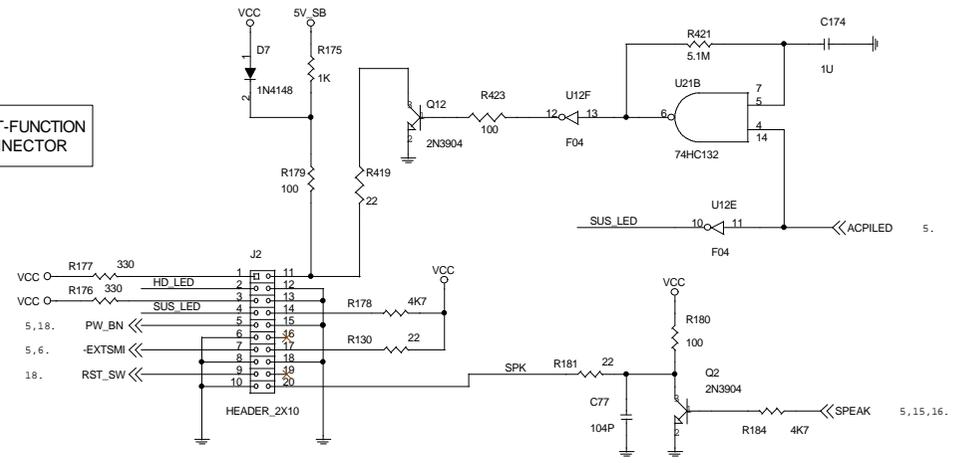
Title PCI 3 & 4		
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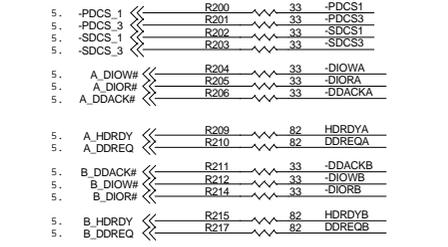
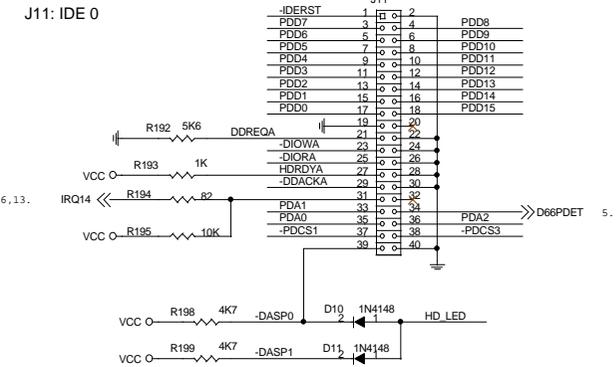




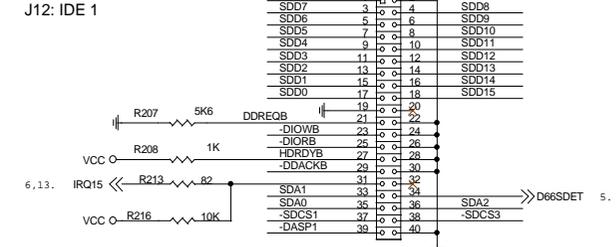
J2: MULTI-FUNCTION CONNECTOR



J11: IDE 0



J12: IDE 1

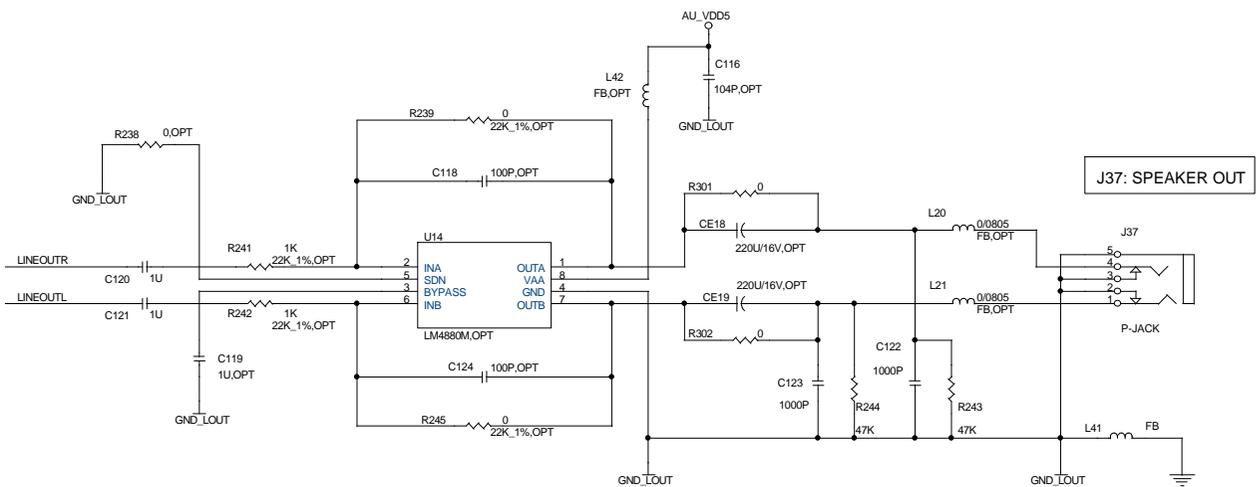
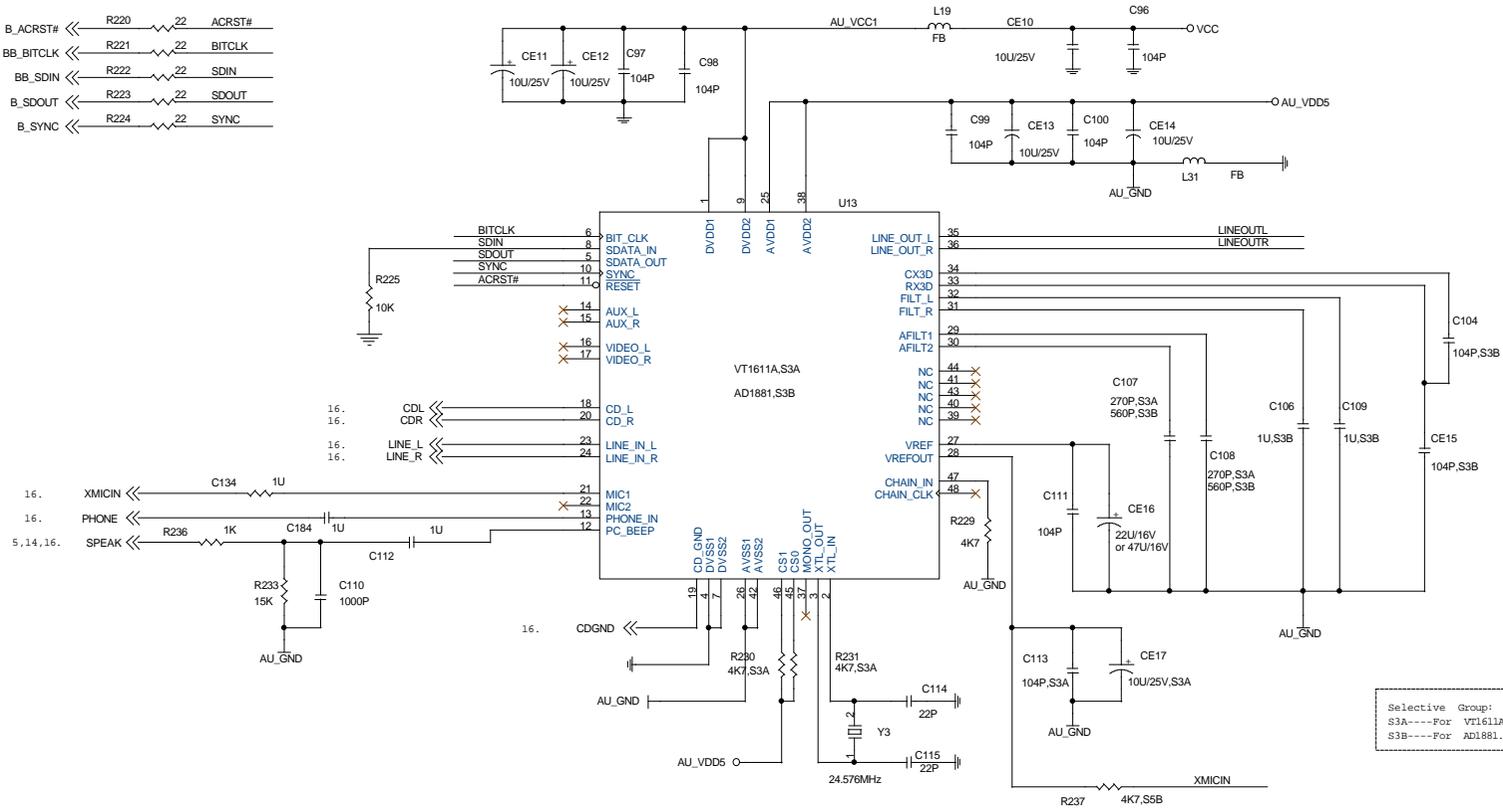


**Gemlight Computer Ltd.**

Title  
COM, PRT, IR, MULTI-FUNCTION CONNECTOR & IDE PORTS

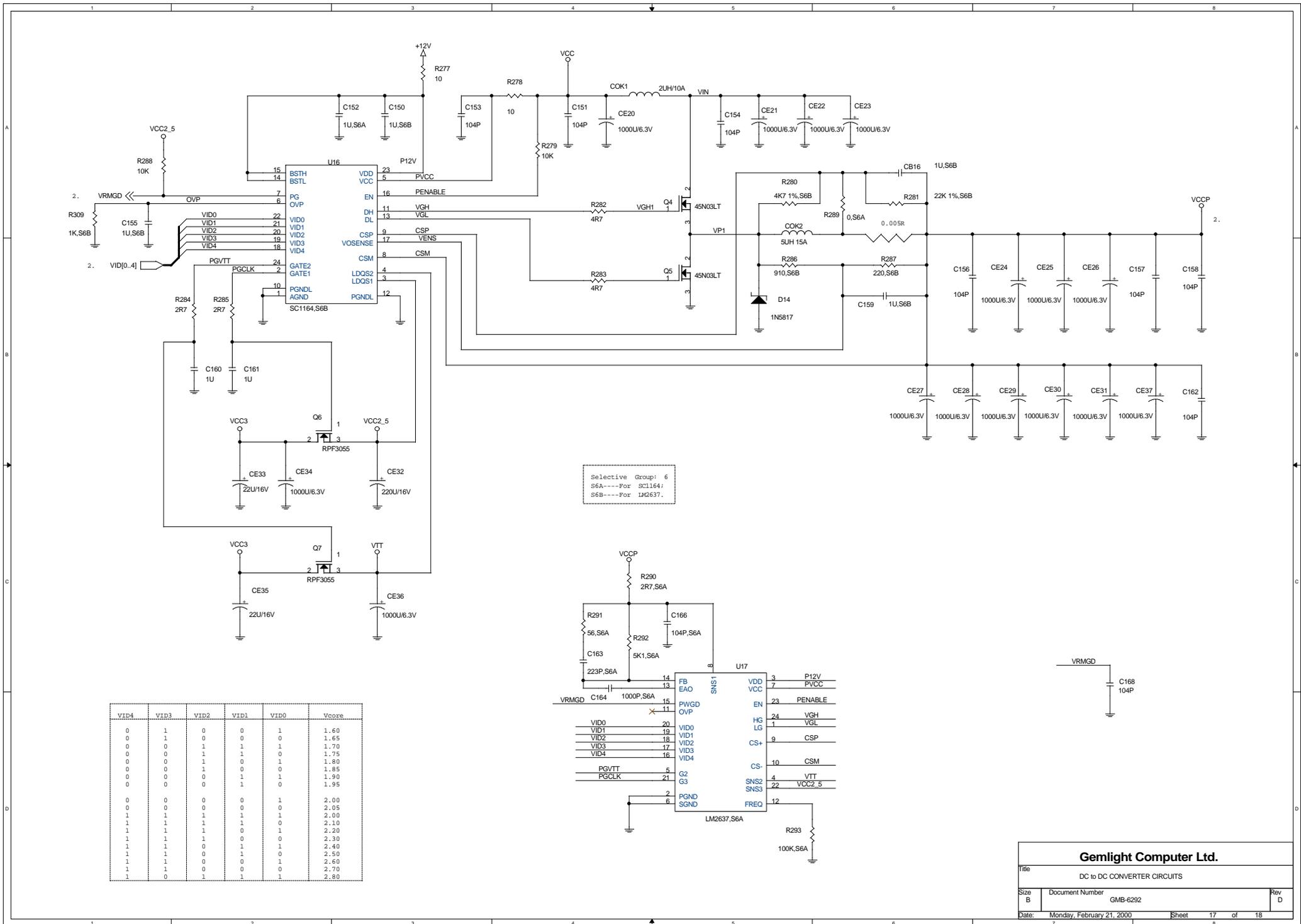
Size B	Document Number GMB-6292	Rev D
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- 5, 16. B\_ACRST# << R220 22 ACRST#
- 5. BB\_BITCLK << R221 22 BITCLK
- 5. BB\_SDIN << R222 22 SDIN
- 5, 16. B\_SDOUT << R223 22 SDOUT
- 5, 16. B\_SYNC << R224 22 SYNC



<b>Gemlight Computer Ltd.</b>		
Title: AUDIO PORT		
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Selective Group: 6  
 S6A---For SC1164;  
 S6B---For IM2637.

VID4	VID3	VID2	VID1	VID0	Vcore
0	1	0	0	1	1.60
0	1	0	0	1	1.65
0	0	1	1	1	1.70
0	0	1	1	0	1.75
0	0	1	0	1	1.80
0	0	1	0	0	1.85
0	0	0	1	1	1.90
0	0	0	1	0	1.95
0	0	0	0	1	2.00
0	0	0	0	0	2.05
1	1	1	1	1	2.00
1	1	1	1	0	2.10
1	1	1	0	1	2.20
1	1	1	0	0	2.30
1	1	0	1	1	2.40
1	1	0	1	0	2.50
1	1	0	0	1	2.60
1	1	0	0	0	2.70
1	0	1	1	1	2.80

