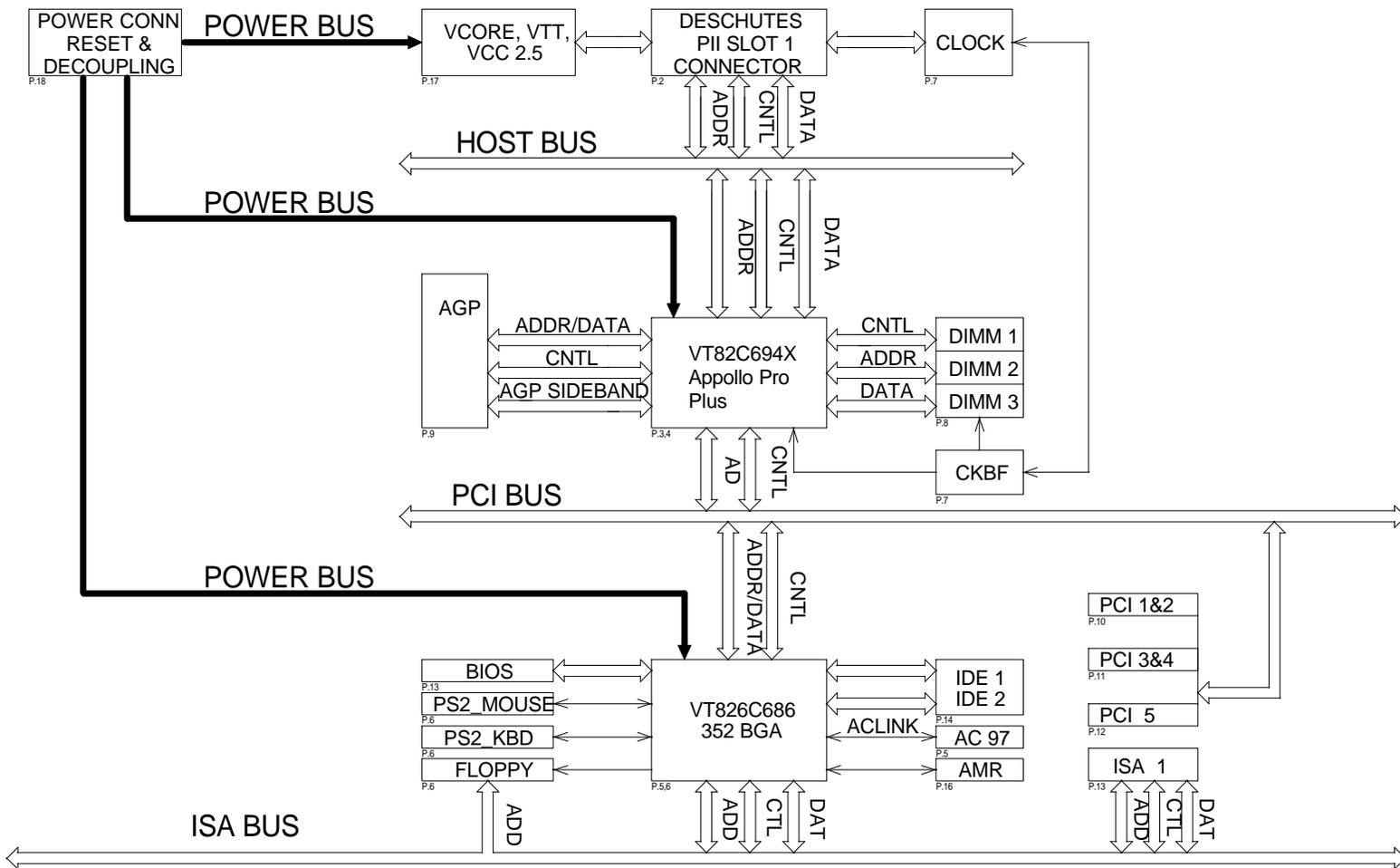
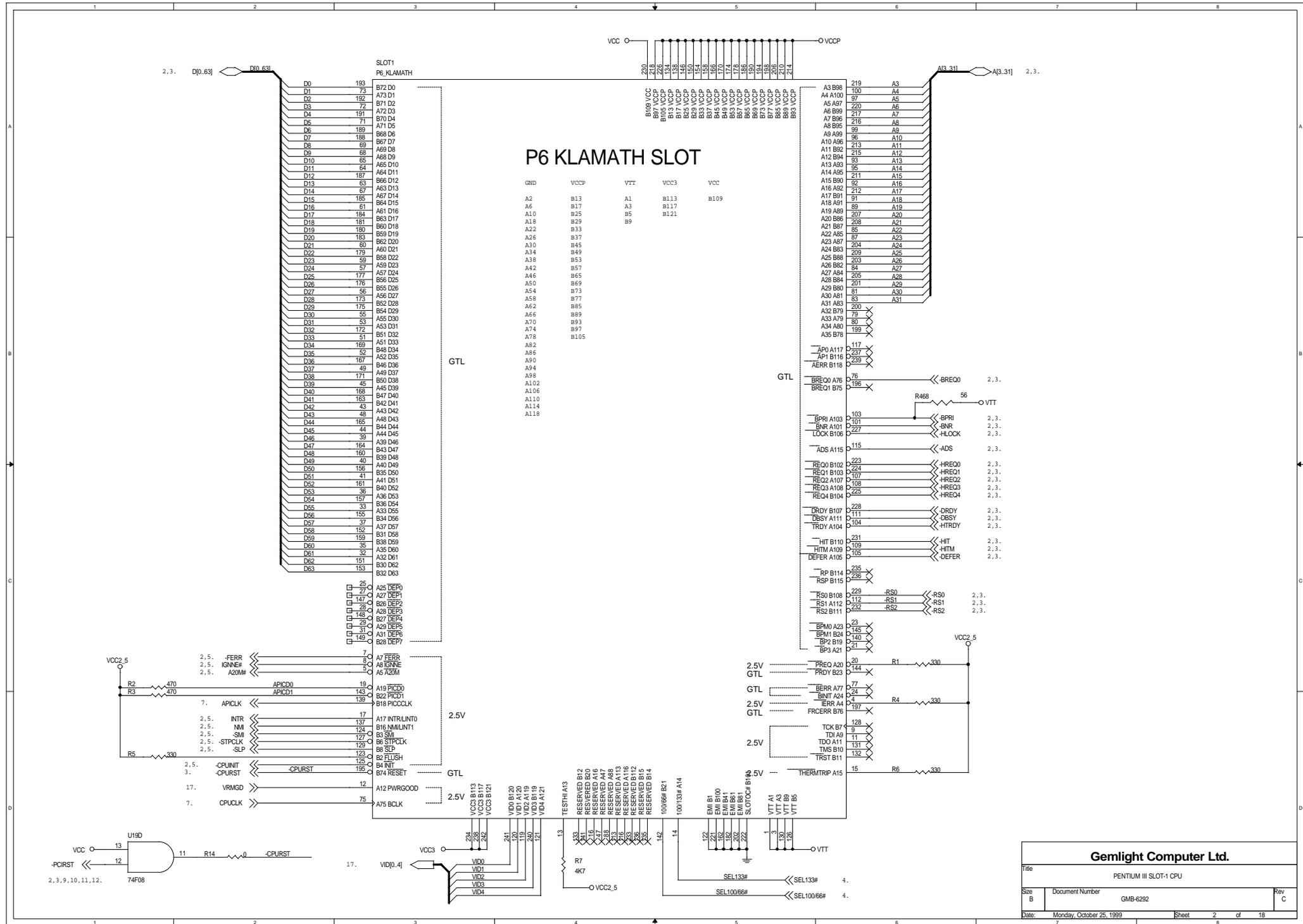


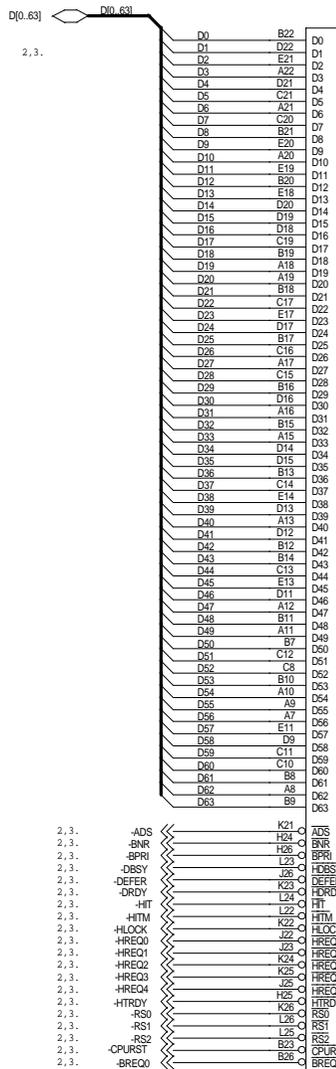
# GMB-6292 V0 RC SCHEMATIC DRAWING

Rev C Released on 28 October 1999



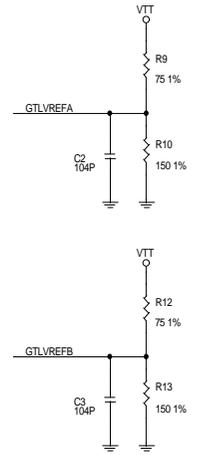
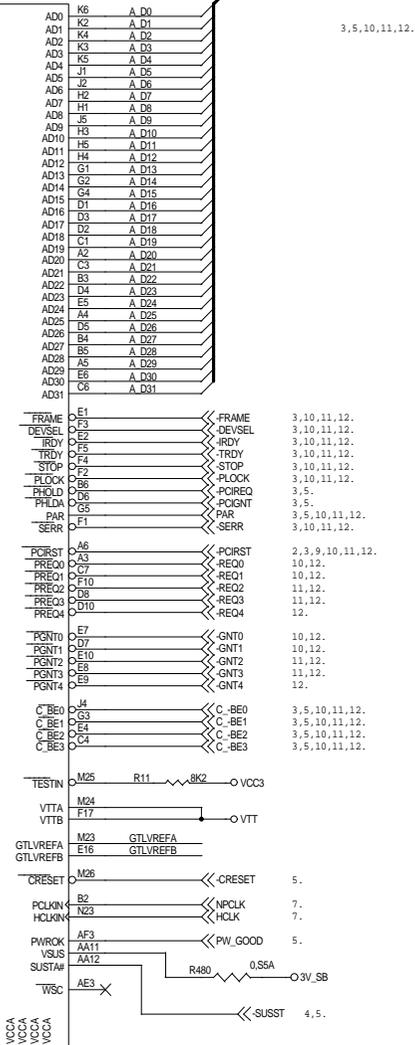
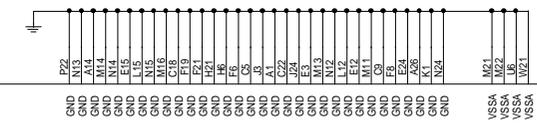
- | LINK
- | 02\_CPU
- | 03\_694A
- | 04\_694B
- | 05\_686A
- | 06\_686B
- | 07\_CLK
- | 08\_DIMM
- | 09\_AGP
- | 10\_PCI12
- | 11\_PCI34
- | 12\_PCI5
- | 13\_ISA
- | 14\_PORT
- | 15\_AUDIO
- | 16\_AMR
- | 17\_CPUPWR
- | 18\_ATX





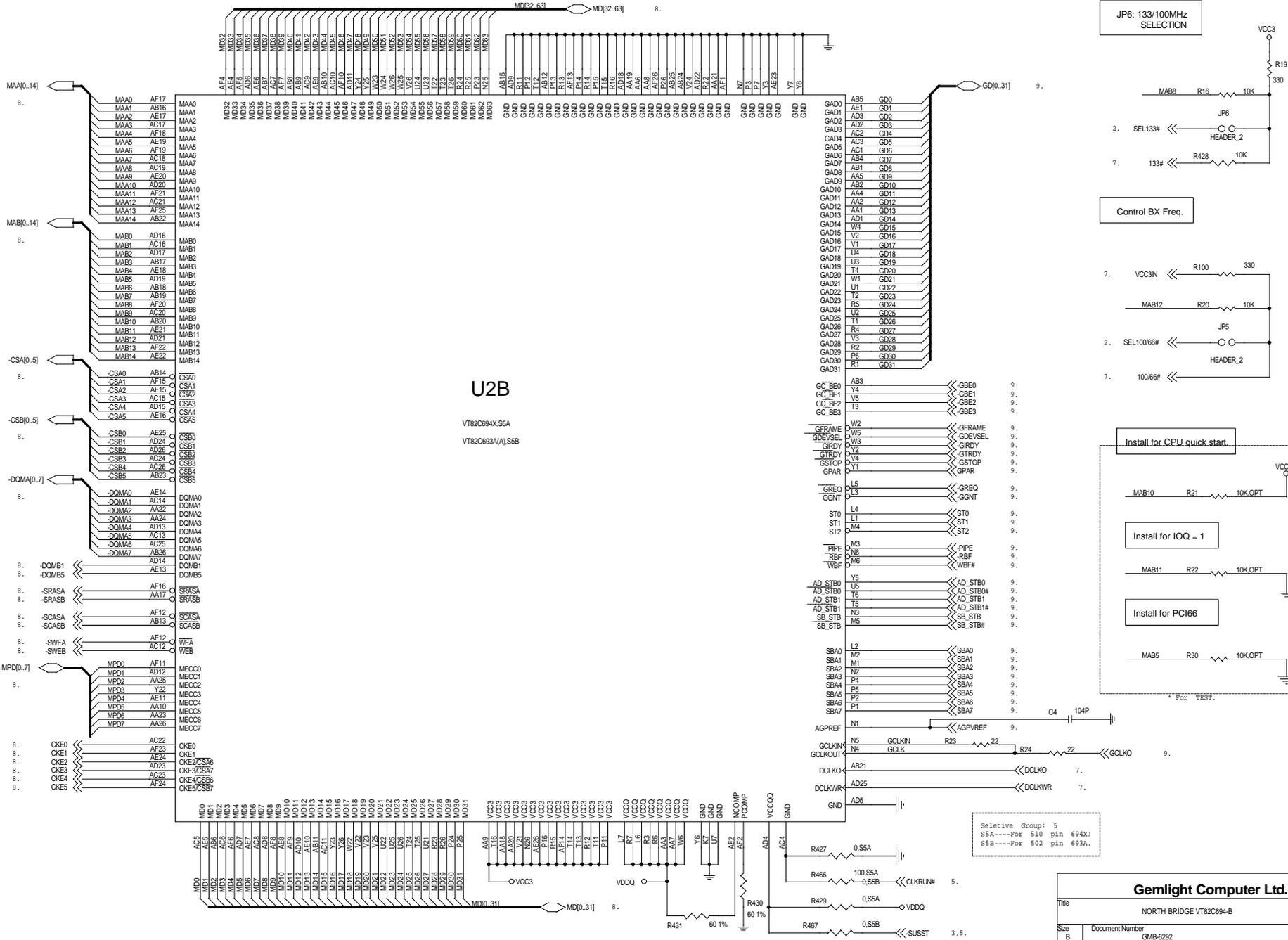
**U2A**

VT82C694X,SSA  
VT82C693A(A),SSB



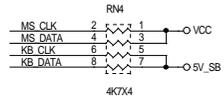
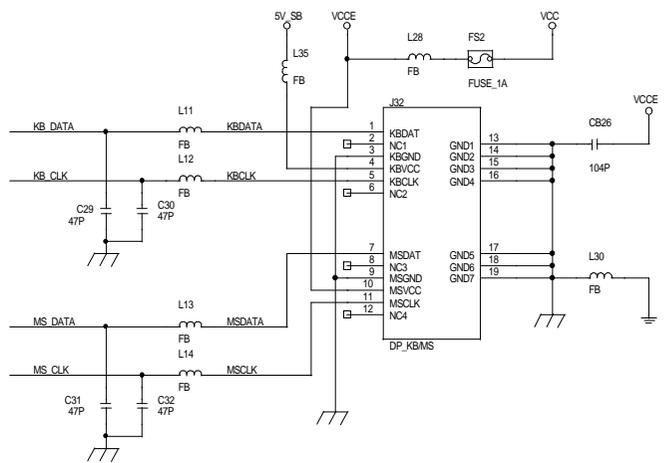
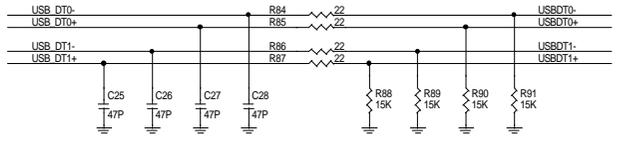
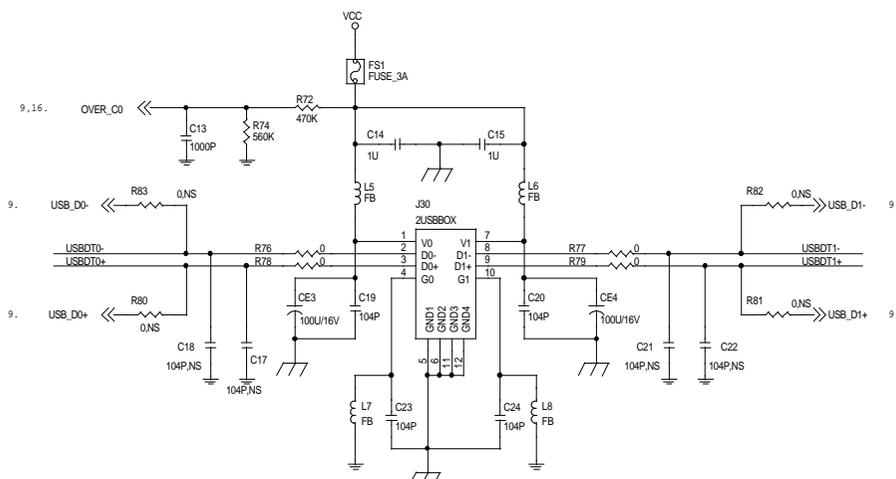
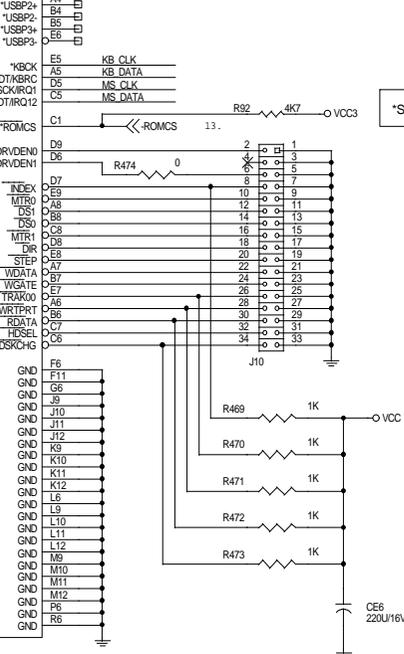
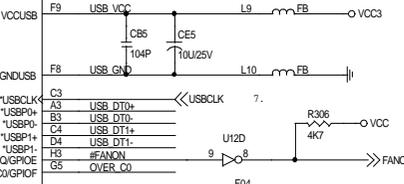
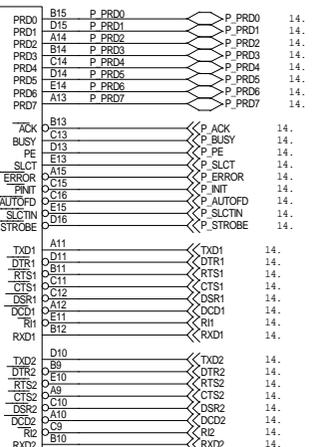
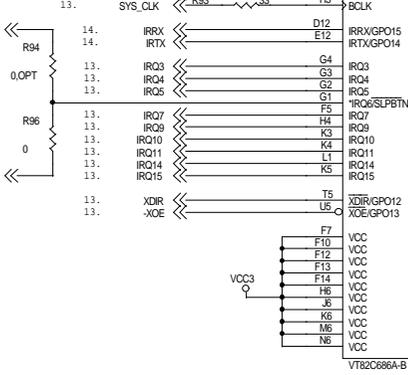
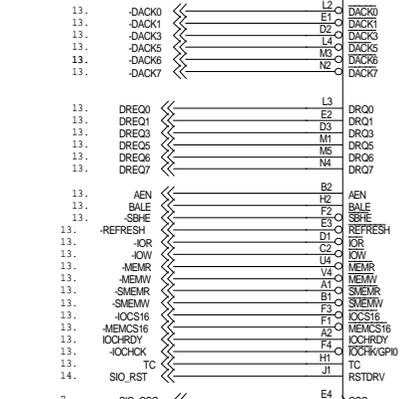
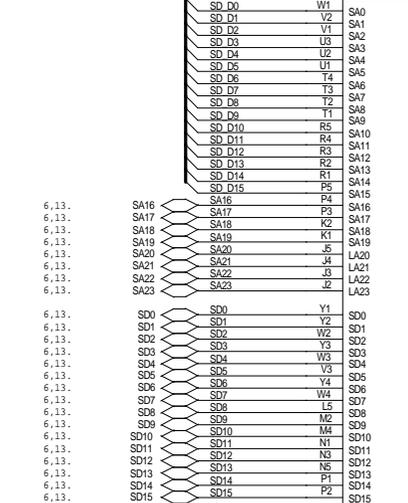
Selective Group: 5  
SSA----For 510 pin 694X;  
SSB----For 502 pin 693A.

<b>Gemlight Computer Ltd.</b>		
Title NORTH BRIDGE VT82C694-A		
Size B	Document Number GMB-6292	Rev C
Date: Monday, October 25, 1999	Sheet 3	of 18



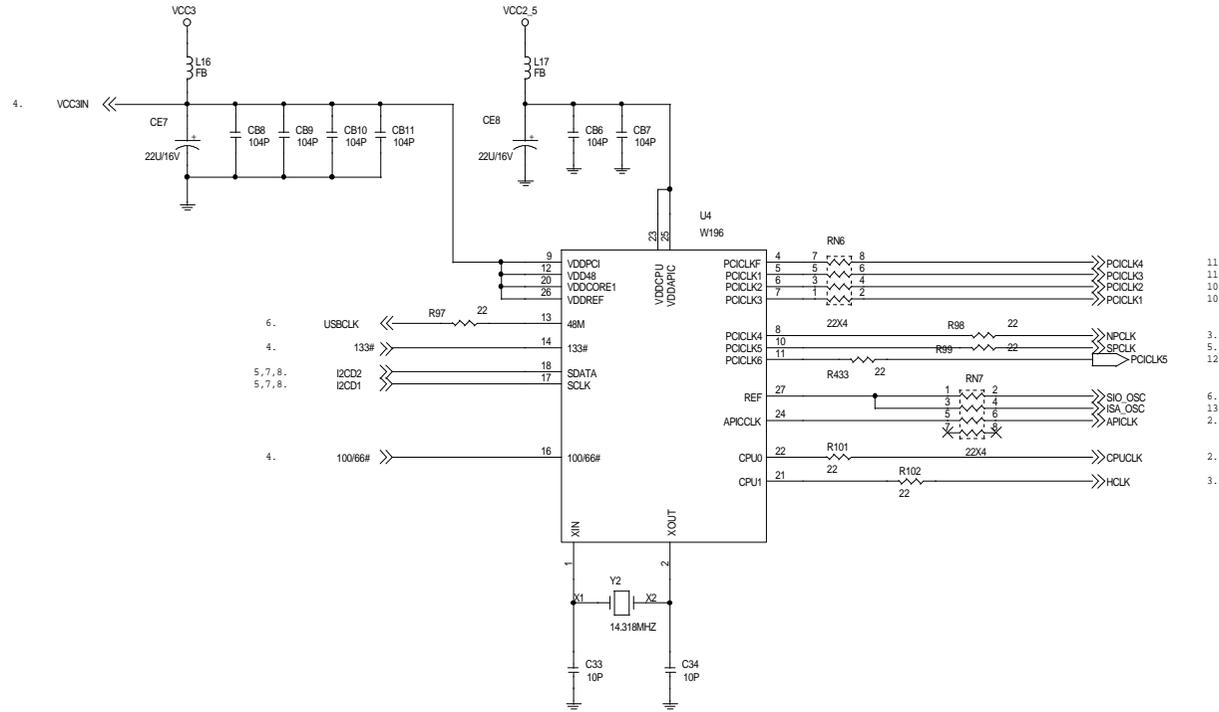


13,14. SD\_D[0..15]

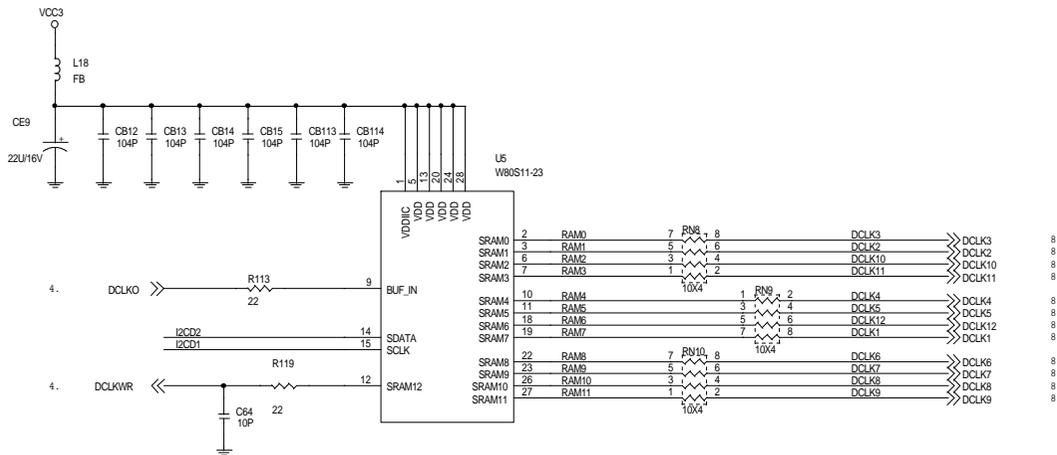


<b>Gemlight Computer Ltd.</b>		
Title SOUTH BRIDGE VT82C686-B, USB, KB & FDD CONNECTORS		
Size B	Document Number GMB-6292	Rev C
Date: Monday, October 25, 1999	Sheet 6	of 18

# CLOCK GENERATOR & BUFFER



CPULCK	C36	10P NS
HCLK	C38	10P NS
DCLK0	C40	10P NS
SPCLK	C42	10P NS
NPCLK	C43	10P NS
PCICLK1	C44	10P NS
PCICLK2	C46	10P NS
PCICLK3	C48	10P NS
PCICLK4	C50	10P NS
PCICLK5	C175	10P NS
APICLK	C54	10P NS
SIO_OSC	C56	10P NS
USBCLK	C58	10P NS
ISA_OSC	C60	10P NS



DCLK4	C35	10P NS
DCLK3	C37	10P NS
DCLK2	C39	10P NS
DCLK1	C41	10P NS
DCLK8	C45	10P NS
DCLK7	C47	10P NS
DCLK6	C49	10P NS
DCLK5	C51	10P NS
DCLK9	C53	10P NS
DCLK10	C55	10P NS
DCLK11	C57	10P NS
DCLK12	C59	10P NS

**Gemlight Computer Ltd.**

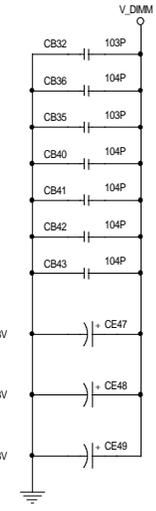
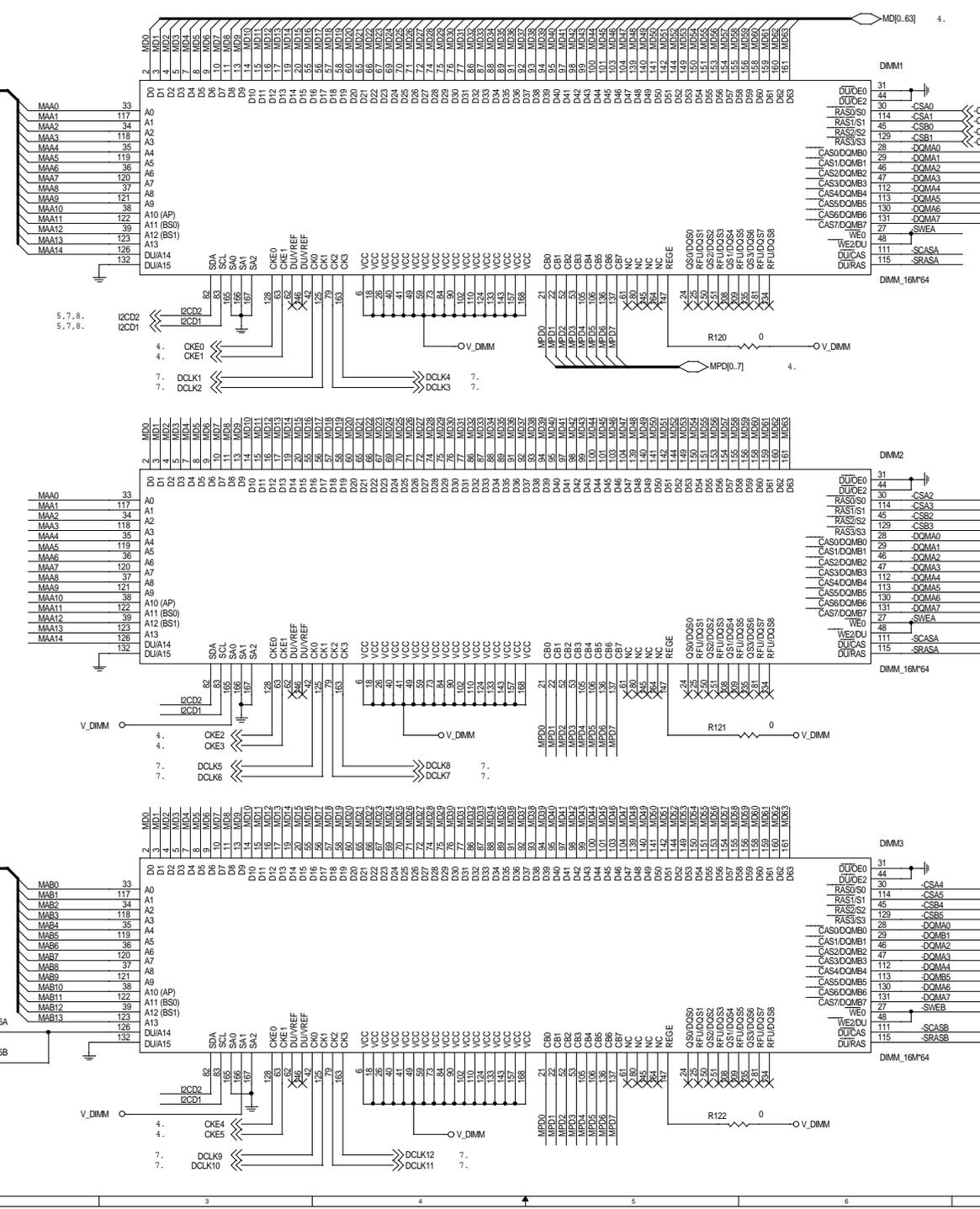
Title		
CLOCK SYNTHESIZER		
Size	Document Number	Rev
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**DIMM1**  
ADDRESS: 1010000B

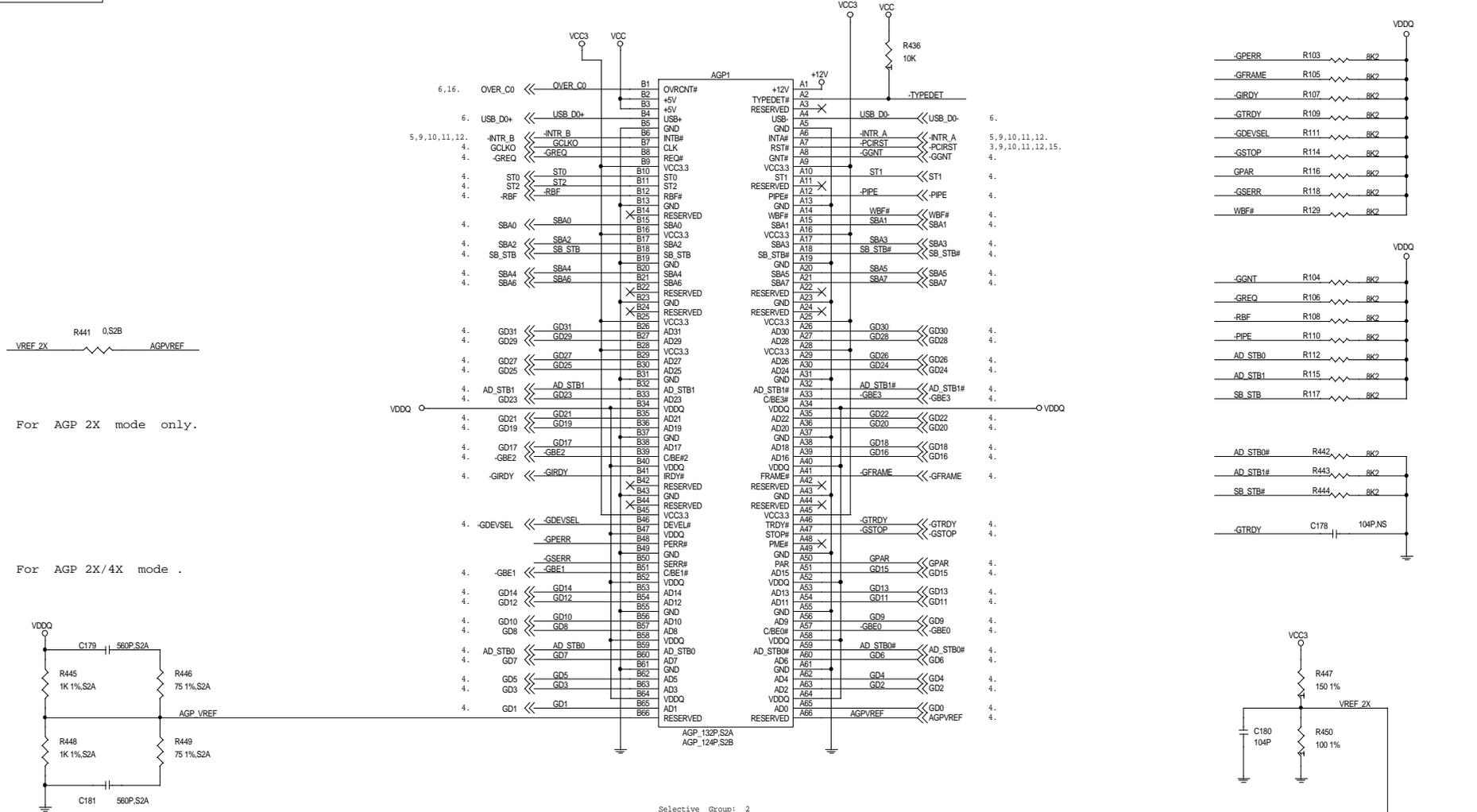
**DIMM2**  
ADDRESS: 1010001B

**DIMM3**  
ADDRESS: 1010010B

Selective Group: 5  
S5A----For 510 pin 694X;  
S5B----For 502 pin 693A.



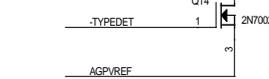
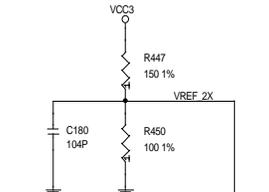
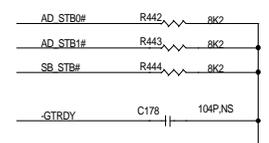
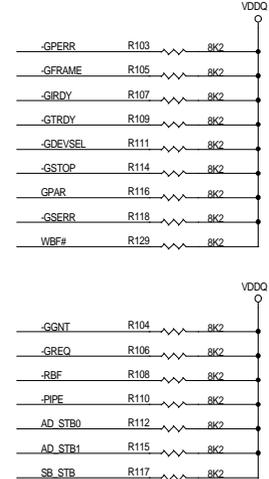
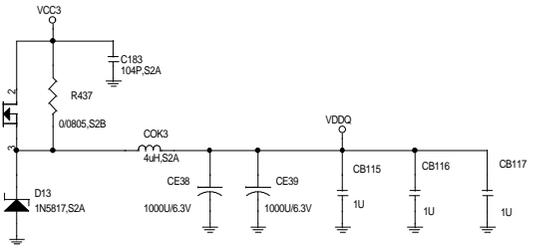
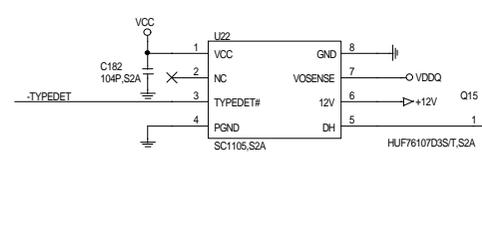
# AGP SLOT



For AGP 2X mode only.

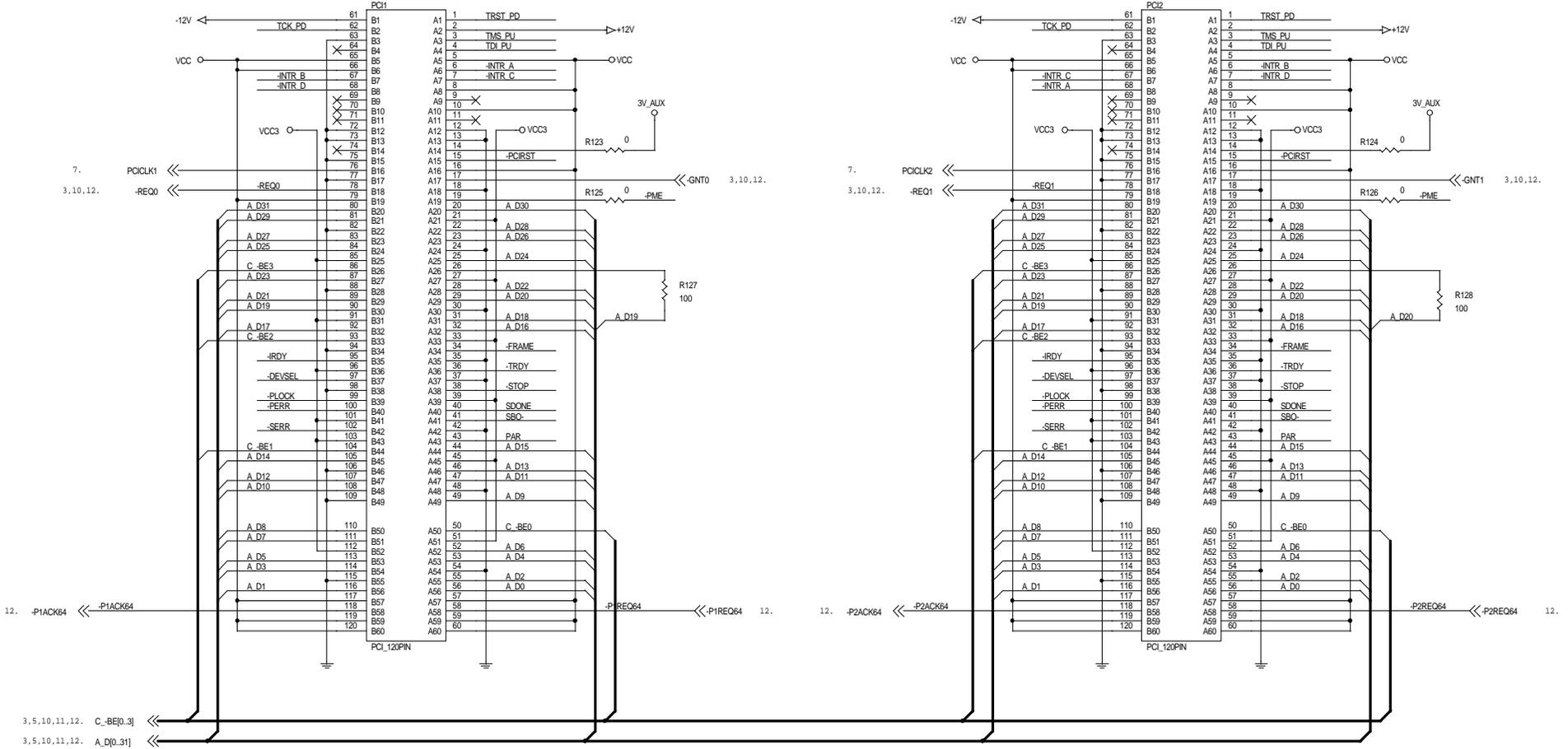
For AGP 2X/4X mode .

Selective Group: 2  
 S2A----For Support AGP2X/4X;  
 S2B----For Only Support AGP2X.



PCI#1  
ID=AD19  
DEV=8H

PCI#2  
ID=AD20  
DEV=9H

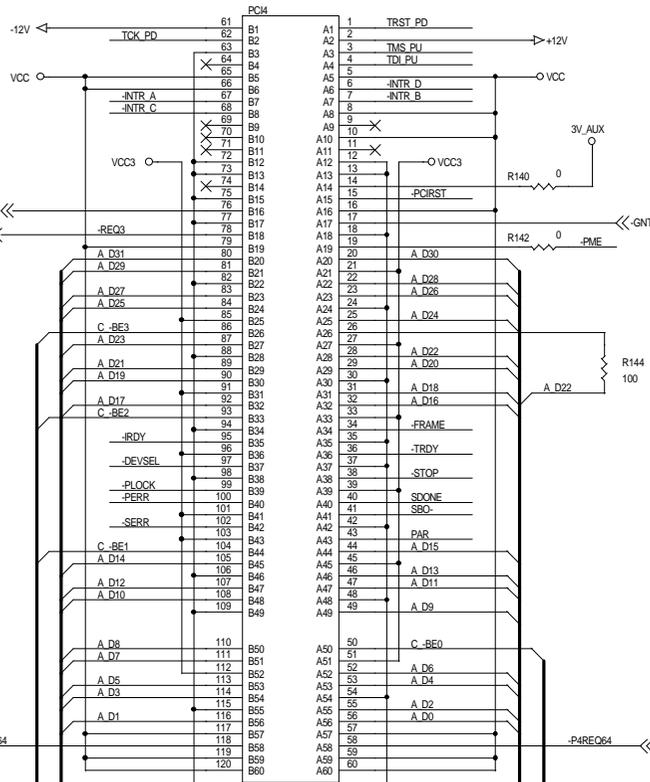
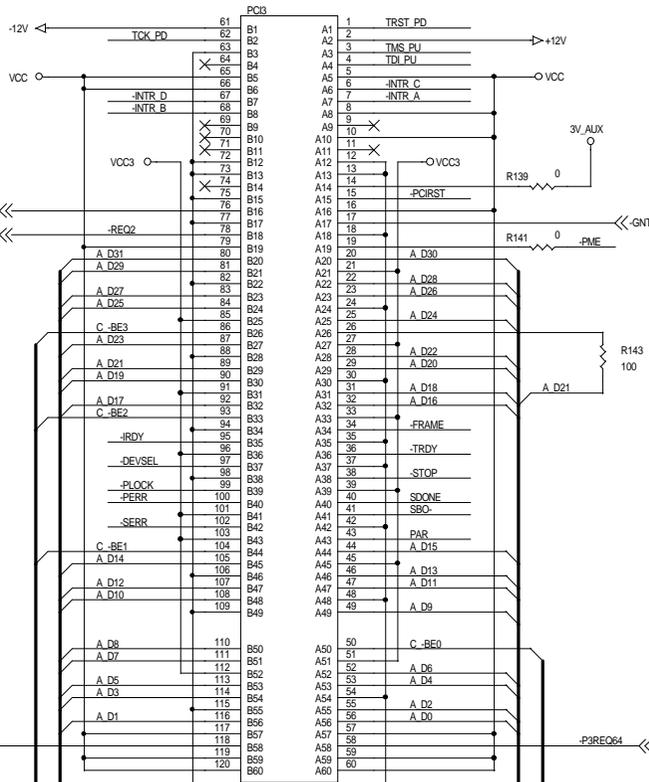


- |               |         |    |         |               |         |    |         |         |    |        |                  |
|---------------|---------|----|---------|---------------|---------|----|---------|---------|----|--------|------------------|
| 10,11,12.     | TMS_PU  | << | TMS_PU  | 3,10,11,12.   | -IRDY   | << | -IRDY   | -PCIRST | << | PCIRST | 3,9,10,11,12,15. |
| 10,11,12.     | TDI_PU  | << | TDI_PU  | 3,10,11,12.   | -DEVSEL | << | -DEVSEL | -PME    | << | PME    | 5,10,11,12.      |
| 10,11,12.     | TRST_PD | << | TRST_PD | 3,10,11,12.   | -PLOCK  | << | -PLOCK  | -FRAME  | << | FRAME  | 3,10,11,12.      |
| 10,11,12.     | TCK_PD  | << | TCK_PD  | 10,11,12.     | -PERR   | << | -PERR   | -TRDY   | << | TRDY   | 3,10,11,12.      |
| 5,9,10,11,12. | -INTR_A | << | -INTR_A | 3,5,10,11,12. | -SERR   | << | -SERR   | -STOP   | << | STOP   | 3,10,11,12.      |
| 5,9,10,11,12. | -INTR_B | << | -INTR_B |               |         |    |         | SDONE   | << | SDONE  | 10,11,12.        |
| 5,9,10,11,12. | -INTR_C | << | -INTR_C |               |         |    |         | SBO     | << | SBO    | 10,11,12.        |
| 5,9,10,11,12. | -INTR_D | << | -INTR_D |               |         |    |         | PAR     | << | PAR    | 3,5,10,11,12.    |

<b>Gemlight Computer Ltd.</b>			
Title	PCI 1 & 2		
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PCI#3  
ID=AD21  
DEV=AH

PCI#4  
ID=AD22  
DEV=BH



10,11,12. TMS\_PU << TMS\_PU  
10,11,12. TDI\_PU << TDI\_PU  
10,11,12. TRST\_PD << TRST\_PD  
10,11,12. TCK\_PD << TCK\_PD  
5,9,10,11,12. -INTR\_A << -INTR\_A  
5,9,10,11,12. -INTR\_B << -INTR\_B  
5,9,10,11,12. -INTR\_C << -INTR\_C  
5,9,10,11,12. -INTR\_D << -INTR\_D

3,10,11,12. -IRDY << -IRDY  
3,10,11,12. -DEVSEL << -DEVSEL  
3,10,11,12. -PLOCK << -PLOCK  
10,11,12. -PERR << -PERR  
3,5,10,11,12. -SERR << -SERR

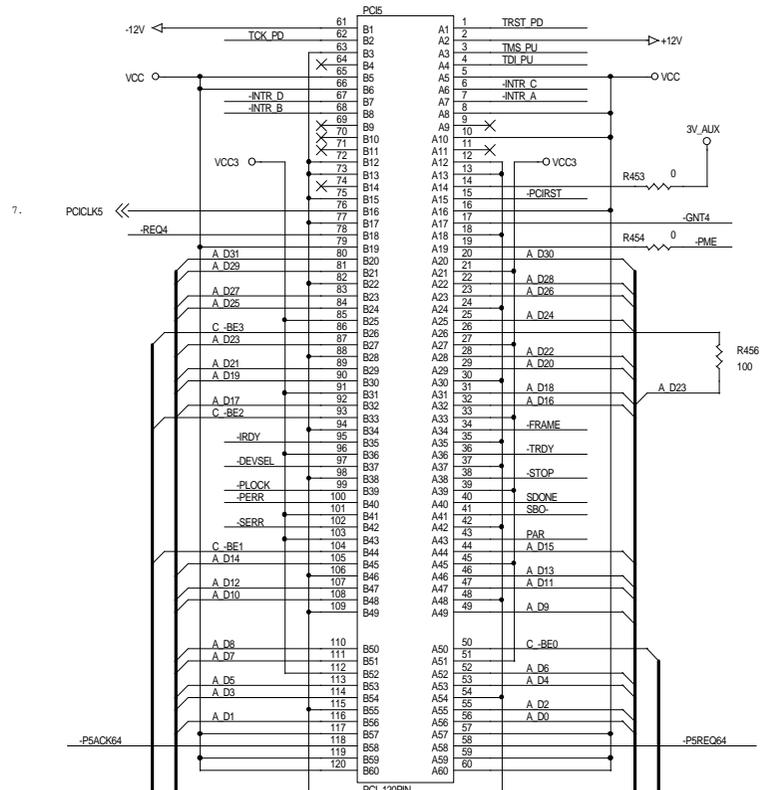
-PCIRST << -PCIRST 3,9,10,11,12,15.  
-PME << -PME 5,10,11,12.  
-FRAME << -FRAME 3,10,11,12.  
-TRDY << -TRDY 3,10,11,12.  
-STOP << -STOP 3,10,11,12.  
SDCONE << SDCONE 10,11,12.  
SBO << SBO 10,11,12.  
PAR << PAR 3,5,10,11,12.

3,5,10,11,12. C\_BE[0..3] <<  
3,5,10,11,12. A\_D[0..31] <<

12. -P3REQ64 << -P3REQ64  
12. -P4ACK64 << -P4ACK64

12. -P4REQ64 << -P4REQ64  
12. -P4ACK64 << -P4ACK64

PCI#5  
ID=AD23  
DEV=CH

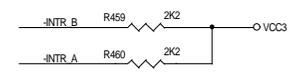
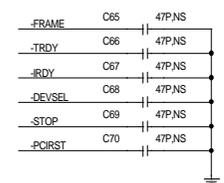
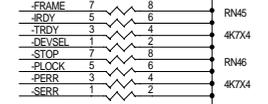
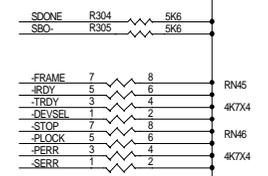
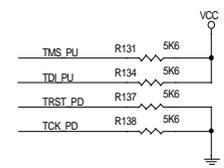
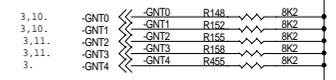
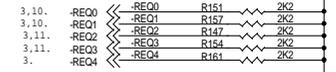
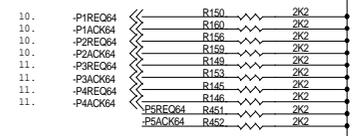
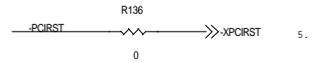


3, 5, 10, 11, 12. C\_BE[0..3] <<<  
3, 5, 10, 11, 12. A\_D[0..31] <<<

10, 11, 12. TMS\_PU <<< TMS\_PU  
10, 11, 12. TDI\_PU <<< TDI\_PU  
10, 11, 12. TRST\_PD <<< TRST\_PD  
10, 11, 12. TCK\_PD <<< TCK\_PD  
5, 9, 10, 11, 12. -INTR\_A <<< -INTR\_A  
5, 9, 10, 11, 12. -INTR\_B <<< -INTR\_B  
5, 9, 10, 11, 12. -INTR\_C <<< -INTR\_C  
5, 9, 10, 11, 12. -INTR\_D <<< -INTR\_D

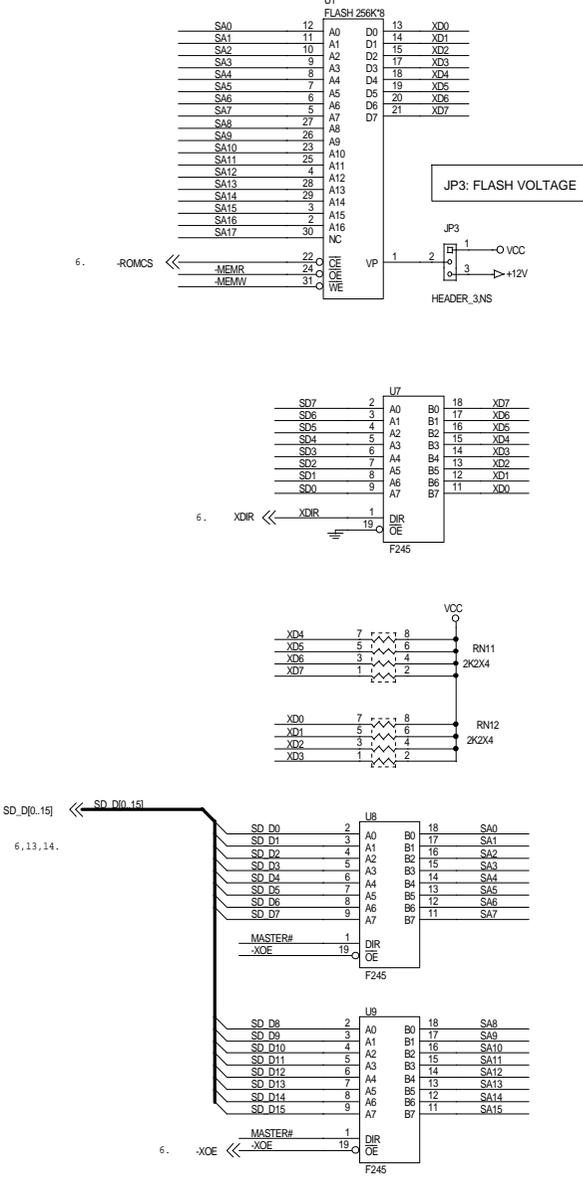
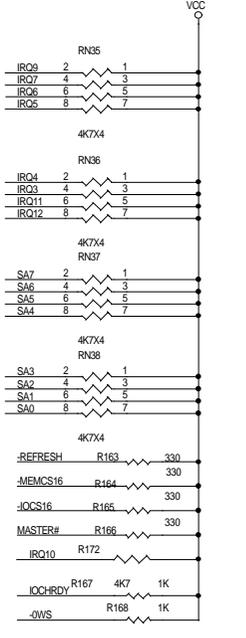
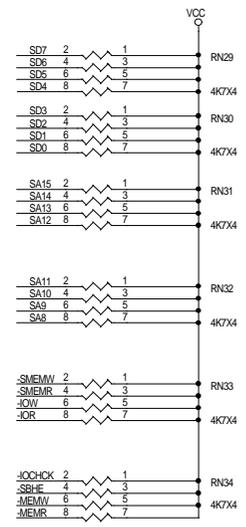
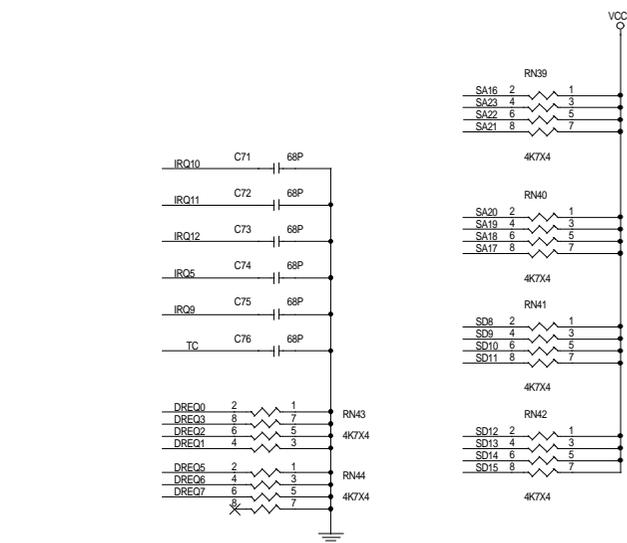
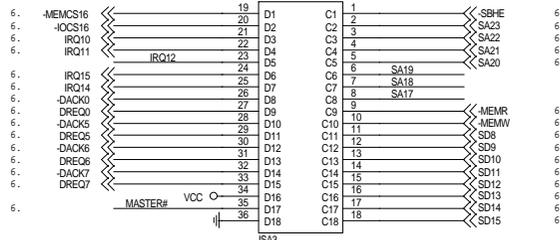
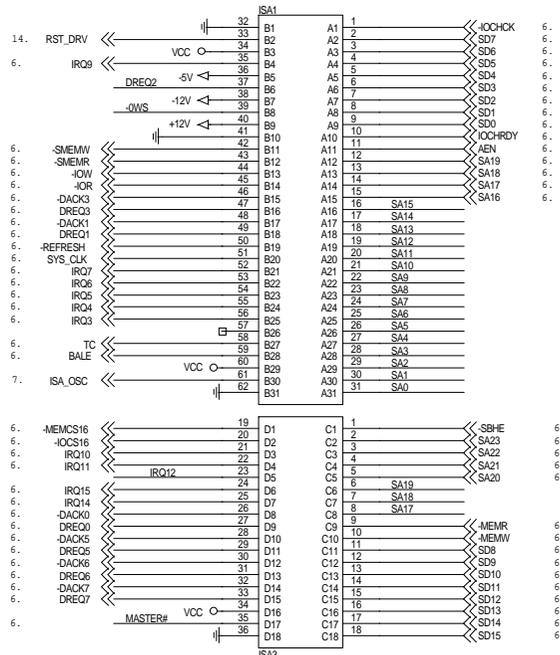
3, 5, 10, 11, 12. -IRDY <<< -IRDY  
3, 5, 10, 11, 12. -DEVSEL <<< -DEVSEL  
3, 10, 11, 12. -PLOCK <<< -PLOCK  
10, 11, 12. -PERR <<< -PERR  
3, 5, 10, 11, 12. -SERR <<< -SERR

-PCIRST <<< -PCIRST 2, 3, 9, 10, 11, 12.  
-PME <<< -PME 5, 10, 11, 12.  
-FRAME <<< -FRAME 3, 5, 10, 11, 12.  
-TRDY <<< -TRDY 3, 5, 10, 11, 12.  
-STOP <<< -STOP 3, 5, 10, 11, 12.  
-SDONE <<< -SDONE 10, 11, 12.  
-SBO <<< -SBO 10, 11, 12.  
-PAR <<< -PAR 3, 5, 10, 11, 12.



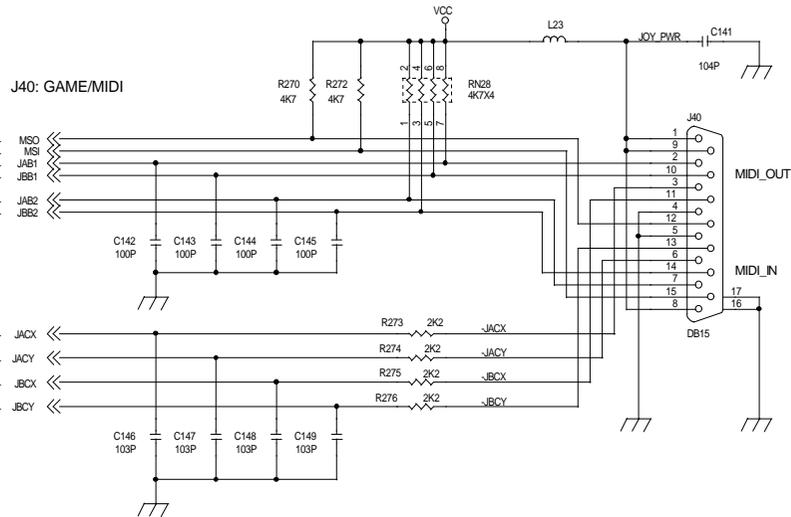
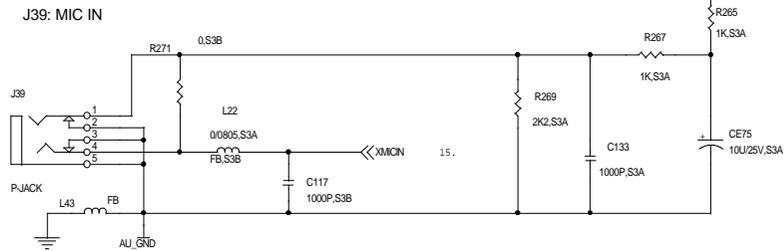
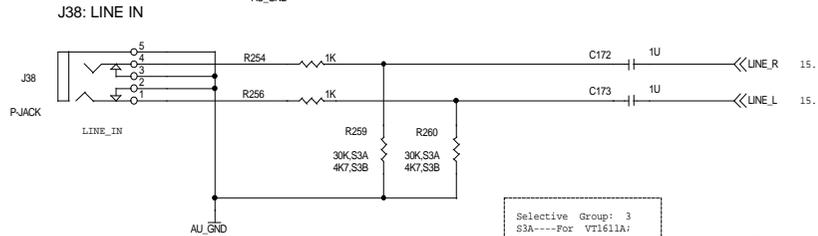
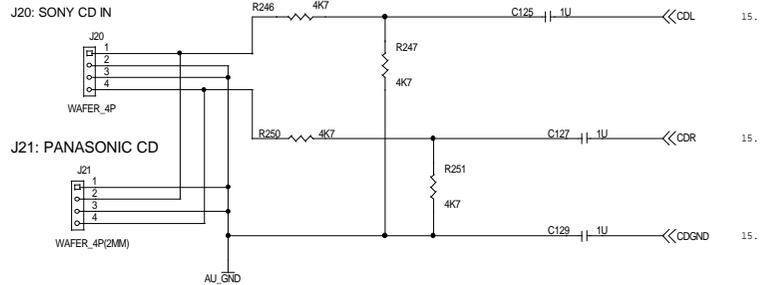
**Gemlight Computer Ltd.**

Title		PCI 5	
Size	Document Number	GMB-6292	Rev C
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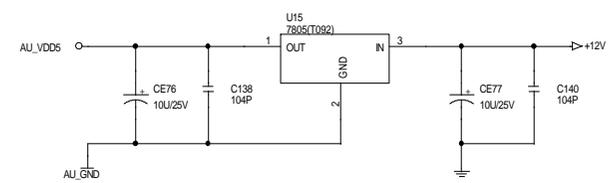
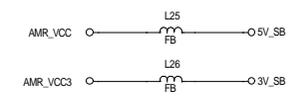
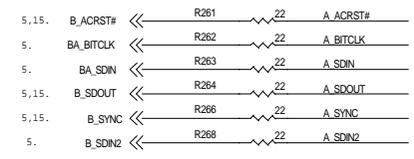
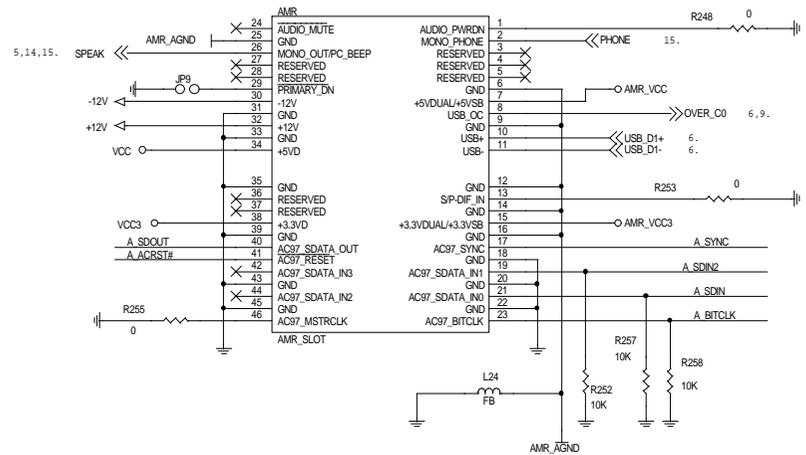


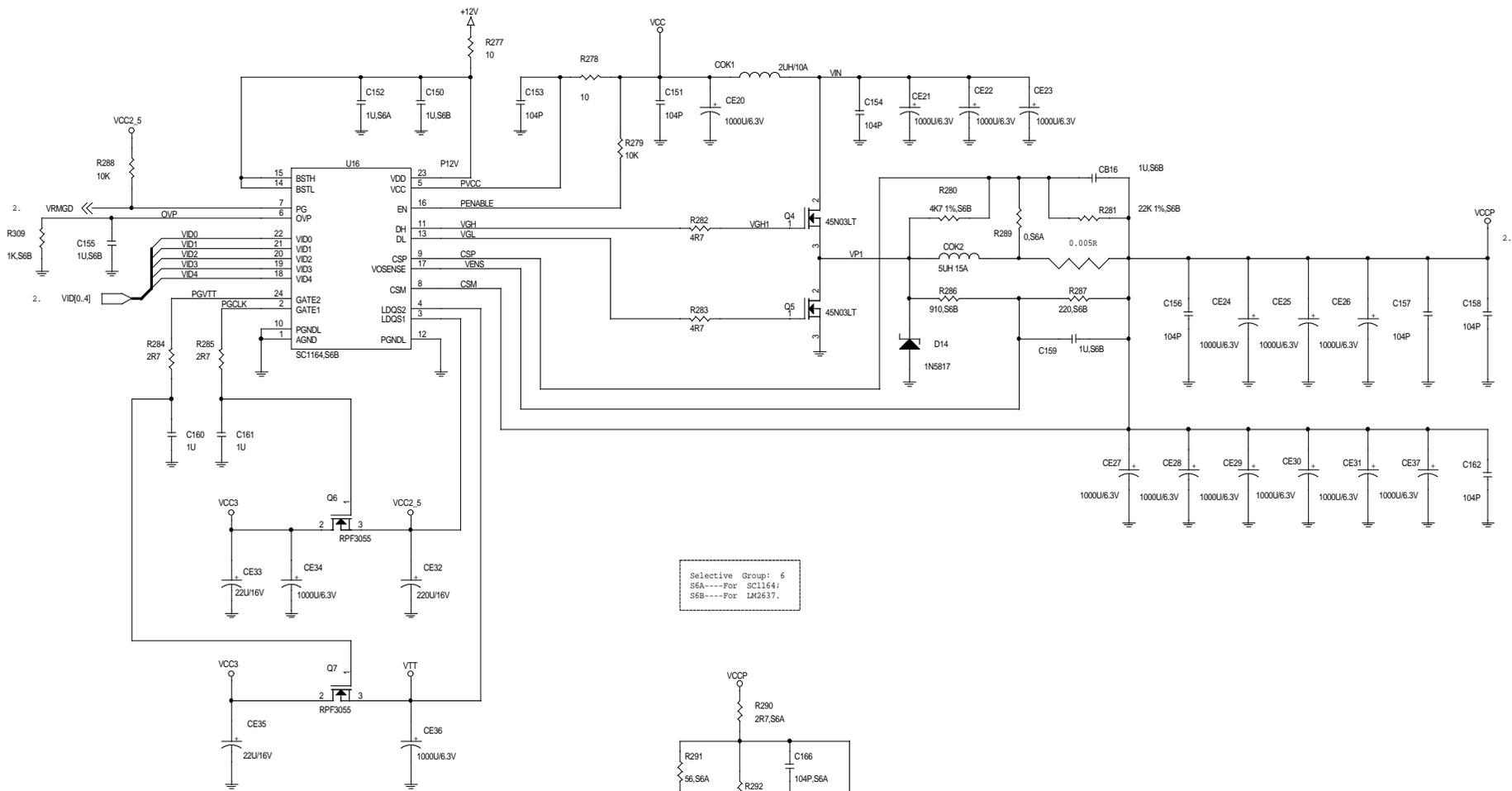






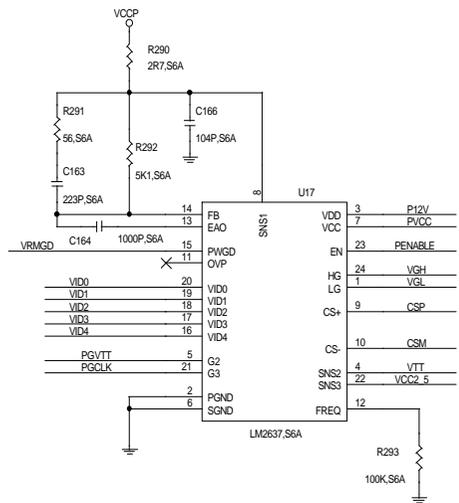
**AUDIOMODEM RISER SLOT**





Selective Group 6  
 S6A---For SC11641  
 S6B---For LM2637.

VID4	VID3	VID2	VID1	VID0	Vcore
0	1	0	0	1	1.60
0	1	0	1	1	1.65
0	0	1	1	1	1.70
0	0	1	1	0	1.75
0	0	1	0	1	1.80
0	0	1	0	0	1.85
0	0	0	1	1	1.90
0	0	0	1	0	1.95
0	0	0	0	1	2.00
0	0	0	0	0	2.05
1	1	1	1	1	2.00
1	1	1	1	0	2.10
1	1	1	0	1	2.20
1	1	1	0	0	2.30
1	1	0	1	1	2.40
1	1	0	1	0	2.50
1	1	0	0	1	2.60
1	1	0	0	0	2.70
1	0	1	1	1	2.80

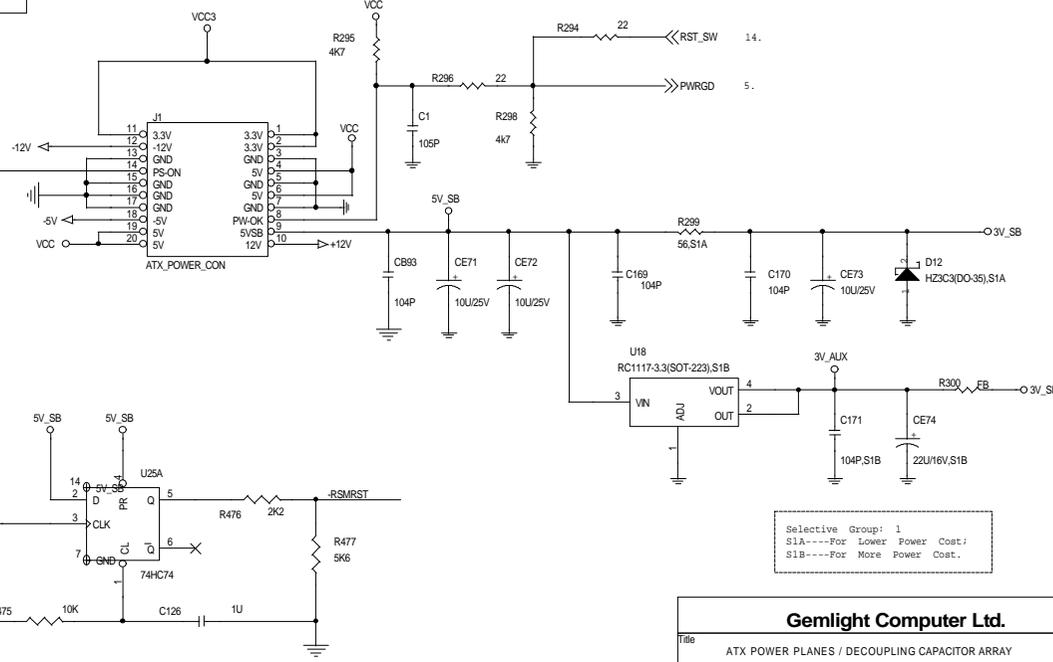
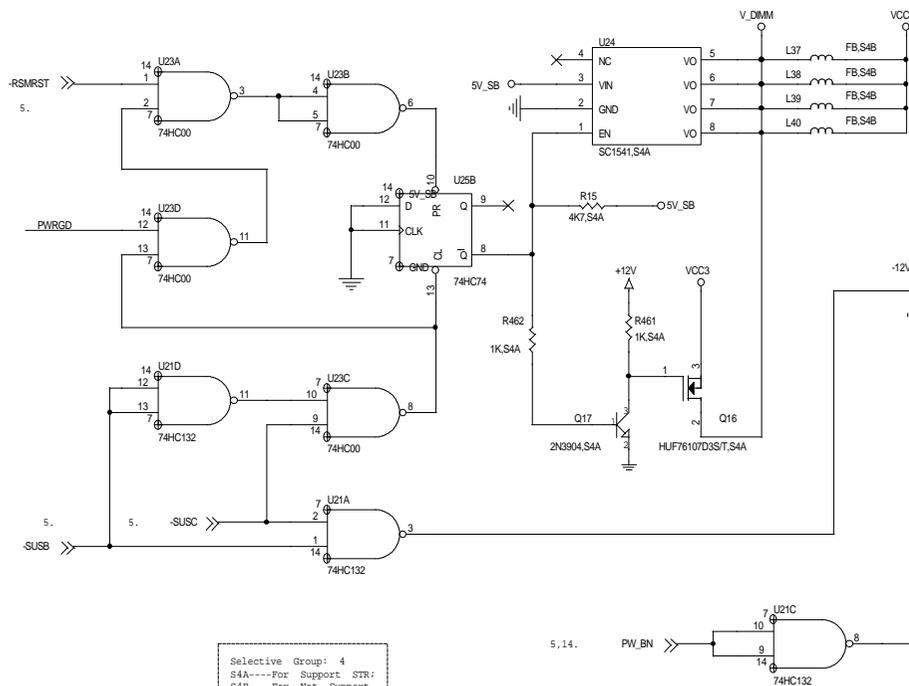
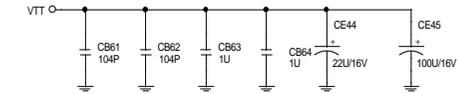
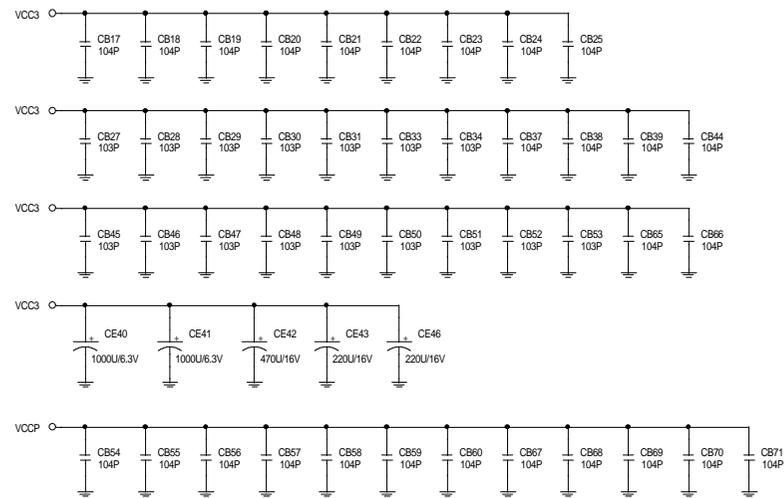
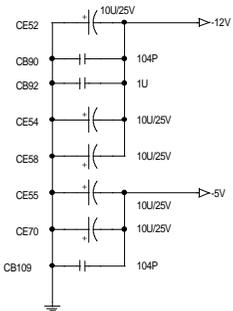
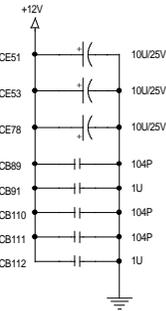
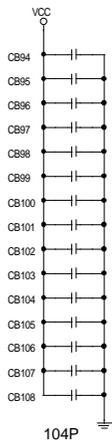
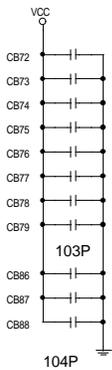
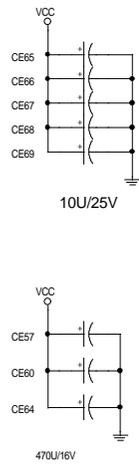


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Title: DC to DC CONVERTER CIRCUITS

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Selective Group: 4  
 S4A----For Support STR;  
 S4B----For Not Support.

Selective Group: 1  
 S1A----For Lower Power Cost;  
 S1B----For More Power Cost.

<b>Gemlight Computer Ltd.</b>			
Title: ATX POWER PLANES / DECOUPLING CAPACITOR ARRAY			
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