

THE JUMPER SETTING FOR 5IHXA

INSTALL 586 CPU QUICK TO SETTING FOR INTEL MMX/AMD K6 MMX CYRIX M2 MMX CLK & VOLTAGE

A. INTEL PENTIUM MMX

Product Name	CPU Speed (MHZ)	BUS CLK	JP2 Connect	JP10,J11 Connect	Multiplier					
PENTIUM-MMX-166	166	66	3	2	1	<table border="1"> <tr><td>2</td><td>1</td></tr> <tr><td>2</td><td>1</td></tr> </table> 2.5X	2	1	2	1
			2	1						
			2	1						
			6	5	4					
9	8	7								
PENTIUM-MMX-200	200	66	3	2	1	<table border="1"> <tr><td>2</td><td>1</td></tr> <tr><td>2</td><td>1</td></tr> </table> 3X	2	1	2	1
			2	1						
			2	1						
			6	5	4					
9	8	7								

SET CPU VOLTAGE

CPU Power Type/jumper	JP12	JP8	Regulator Installed
DUAL 2.9V & 3.3V	1	2	Q1,Q2
	3	4	
	5	6	
	7	8	
	9	10	

DUAL 2.8V & 3.3V

AMD K6 MMX

CPU CLK

Product Name	CPU Speed (MHZ)	BUS CLK	JP2 Connect			JP10,J11 Connect	Multiplier				
AMD-K6-166	166	66	3	2	1	<table border="1"> <tr> <td>2</td> <td>1</td> </tr> <tr> <td>2</td> <td>1</td> </tr> </table>	2	1	2	1	2.5X
			2	1							
			2	1							
			6	5	4						
9	8	7									
AMD-K6-200	200	66	3	2	1	<table border="1"> <tr> <td>2</td> <td>1</td> </tr> <tr> <td>2</td> <td>1</td> </tr> </table>	2	1	2	1	3X
			2	1							
			2	1							
			6	5	4						
9	8	7									

AMD-K6-233	233	66	3	2	1	<table border="1"> <tr> <td>2</td> <td>1</td> </tr> <tr> <td>2</td> <td>1</td> </tr> </table>	2	1	2	1	3.5X
			2	1							
			2	1							
			6	5	4						
9	8	7									

SET CPU VOLTAGE

CPU TYPE	CPU Power Type/jumper iumber	JP12	JP8	Regulator Installed					
AMD K6 166/200	DUAL 2.9V & 3.3V DUAL 2.8V & 3.3V	1	2	<table border="1"> <tr> <td>3</td> <td>4</td> </tr> <tr> <td>1</td> <td>2</td> </tr> </table>	3	4	1	2	Q1.Q2
		3	4						
		1	2						
		3	4						
		5	6						
		7	8						
9	10								

AMD -K6-233	DUAL 3.3V & 3.3V DUAL 3.2V & 3.3V	1	2	<table border="1"> <tr> <td>3</td> <td>4</td> </tr> <tr> <td>1</td> <td>2</td> </tr> </table>	3	4	1	2	Q1.Q2
		3	4						
		1	2						
		3	4						
		5	6						
		7	8						
9	10								

CYRIX M2 MMX
CPU CLK

Product Name	CPU Speed (MHZ)	BUS CLK	JP2 Connect			JP10,J11 Connect	Multiplier				
CYRIX-M2-PR166	150	60	3	2	1	<table border="1"> <tr> <td>2</td> <td>1</td> </tr> <tr> <td>2</td> <td>1</td> </tr> </table>	2	1	2	1	2.5X
			2	1							
			2	1							
			6	5	4						
9	8	7									

CYRIX-M2-PR200	166	66	3	2	1	<table border="1"> <tr><td>2</td><td>1</td></tr> <tr><td>2</td><td>1</td></tr> </table>	2	1	2	1	2.5X
			2	1							
			2	1							
			6	5	4						
9	8	7									
CYRIX-M2-PR233	188	75	3	2	1	<table border="1"> <tr><td>2</td><td>1</td></tr> <tr><td>2</td><td>1</td></tr> </table>	2	1	2	1	2.5X
			2	1							
			2	1							
			6	5	4						
9	8	7									

SET CPU VOLTAGE

CPU TYPE	CPU Power Type/jumper number	JP12		JP8	Regulator Installed				
		1	2						
		3	4						
		5	6						
		7	8						
		9	10	<table border="1"> <tr><td>3</td><td>4</td></tr> <tr><td>1</td><td>2</td></tr> </table>	3	4	1	2	
3	4								
1	2								

CYRIX M2 PR166/PR200/ PR233	DUAL 2.9V & 3.3V DUAL 2.8V & 3.3V			Q1.Q2
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**QUICK TO SETTING FOR INTEL PENTIUM & AMD K5 CPU
CLK & VOLTAGE
CPU CLK**

Product Name	CPU Speed (MHZ)	BUS CLK	JP2 Connect	JP10,J11 Connect	Multiplier													
PENTIUM-75 AMD-K5-PR75	75	50	<table border="1"> <tr><td>3</td><td>2</td><td>1</td></tr> <tr><td>6</td><td>5</td><td>4</td></tr> <tr><td>9</td><td>8</td><td>7</td></tr> </table>	3	2	1	6	5	4	9	8	7	<table border="1"> <tr><td>2</td><td>1</td></tr> <tr><td>2</td><td>1</td></tr> </table>	2	1	2	1	1.5X
3	2	1																
6	5	4																
9	8	7																
2	1																	
2	1																	
PENTIUM-90 AMD K5 PR90 AMD K5 PR120	90	60	<table border="1"> <tr><td>3</td><td>2</td><td>1</td></tr> <tr><td>6</td><td>5</td><td>4</td></tr> <tr><td>9</td><td>8</td><td>7</td></tr> </table>	3	2	1	6	5	4	9	8	7	<table border="1"> <tr><td>2</td><td>1</td></tr> <tr><td>2</td><td>1</td></tr> </table>	2	1	2	1	1.5X
3	2	1																
6	5	4																
9	8	7																
2	1																	
2	1																	
PENTIUM-100 AMD K5 PR100 AMD K5-PR133	100	66	<table border="1"> <tr><td>3</td><td>2</td><td>1</td></tr> <tr><td>6</td><td>5</td><td>4</td></tr> <tr><td>9</td><td>8</td><td>7</td></tr> </table>	3	2	1	6	5	4	9	8	7	<table border="1"> <tr><td>2</td><td>1</td></tr> <tr><td>2</td><td>1</td></tr> </table>	2	1	2	1	1.5X
3	2	1																
6	5	4																
9	8	7																
2	1																	
2	1																	

PENTIUM-120 AMD K5 PR150	120	60	3	2	1	<table border="1"> <tbody> <tr> <td>2</td> <td>1</td> </tr> <tr> <td>2</td> <td>1</td> </tr> </tbody> </table>	2	1	2	1	2X
			2	1							
			2	1							
			6	5	4						
9	8	7									
PENTIUM-133	133	66	3	2	1	<table border="1"> <tbody> <tr> <td>2</td> <td>1</td> </tr> <tr> <td>2</td> <td>1</td> </tr> </tbody> </table>	2	1	2	1	2X
			2	1							
			2	1							
			6	5	4						
9	8	7									
PENTIUM-150	150	60	3	2	1	<table border="1"> <tbody> <tr> <td>2</td> <td>1</td> </tr> <tr> <td>2</td> <td>1</td> </tr> </tbody> </table>	2	1	2	1	2.5X
			2	1							
			2	1							
			6	5	4						
9	8	7									
			3	2	1	<table border="1"> <tbody> <tr> <td>2</td> <td>1</td> </tr> <tr> <td>2</td> <td>1</td> </tr> </tbody> </table>	2	1	2	1	
			2	1							
			2	1							
			6	5	4						
9	8	7									

PENTIUM-166 AMD K5 PR166	166	66			2.5X
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PENTIUM-180	180	60	<table border="1"> <tr><td>3</td><td>2</td><td>1</td></tr> <tr><td>6</td><td>5</td><td>4</td></tr> <tr><td>9</td><td>8</td><td>7</td></tr> </table>	3	2	1	6	5	4	9	8	7	<table border="1"> <tr><td>2</td><td>1</td></tr> <tr><td>2</td><td>1</td></tr> </table>	2	1	2	1	3X
			3	2	1													
6	5	4																
9	8	7																
2	1																	
2	1																	
PENTIUM-200	200	66	<table border="1"> <tr><td>3</td><td>2</td><td>1</td></tr> <tr><td>6</td><td>5</td><td>4</td></tr> <tr><td>9</td><td>8</td><td>7</td></tr> </table>	3	2	1	6	5	4	9	8	7	<table border="1"> <tr><td>2</td><td>1</td></tr> <tr><td>2</td><td>1</td></tr> </table>	2	1	2	1	3X
			3	2	1													
6	5	4																
9	8	7																
2	1																	
2	1																	

CPU Power Type/jumper number	JP12	JP8	Regulator Installed														
ONLY 3.3V	<table border="1"> <tr><td>1</td><td>2</td></tr> <tr><td>3</td><td>4</td></tr> <tr><td>5</td><td>6</td></tr> <tr><td>7</td><td>8</td></tr> <tr><td>9</td><td>10</td></tr> </table>	1	2	3	4	5	6	7	8	9	10	<table border="1"> <tr><td>3</td><td>4</td></tr> <tr><td>1</td><td>2</td></tr> </table>	3	4	1	2	Q2
1	2																
3	4																
5	6																
7	8																
9	10																
3	4																
1	2																
ONLY 3.52V	<table border="1"> <tr><td>1</td><td>2</td></tr> <tr><td>3</td><td>4</td></tr> <tr><td>5</td><td>6</td></tr> <tr><td>7</td><td>8</td></tr> <tr><td>9</td><td>10</td></tr> </table>	1	2	3	4	5	6	7	8	9	10	<table border="1"> <tr><td>3</td><td>4</td></tr> <tr><td>1</td><td>2</td></tr> </table>	3	4	1	2	Q2
1	2																
3	4																
5	6																
7	8																
9	10																
3	4																
1	2																
DUAL 3.3V & 3.3V DUAL 3.2V & 3.3V	<table border="1"> <tr><td>1</td><td>2</td></tr> <tr><td>3</td><td>4</td></tr> <tr><td>5</td><td>6</td></tr> <tr><td>7</td><td>8</td></tr> <tr><td>9</td><td>10</td></tr> </table>	1	2	3	4	5	6	7	8	9	10	<table border="1"> <tr><td>3</td><td>4</td></tr> <tr><td>1</td><td>2</td></tr> </table>	3	4	1	2	Q1,Q2
	1	2															
3	4																
5	6																
7	8																
9	10																
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<table border="1"> <tr><td>1</td><td>2</td></tr> <tr><td>3</td><td>4</td></tr> <tr><td>5</td><td>6</td></tr> <tr><td>7</td><td>8</td></tr> <tr><td>9</td><td>10</td></tr> </table>	1	2	3	4	5	6	7	8	9	10	<table border="1"> <tr><td>3</td><td>4</td></tr> <tr><td>1</td><td>2</td></tr> </table>	3	4	1	2		
1	2																
3	4																
5	6																
7	8																
9	10																
3	4																
1	2																

CPU Power Type/jumper number	JP12	JP8	Regulator Installed														
DUAL 3.52V&3.52V	<table border="1"> <tr><td>1</td><td>2</td></tr> <tr><td>3</td><td>4</td></tr> <tr><td>5</td><td>6</td></tr> <tr><td>7</td><td>8</td></tr> <tr><td>9</td><td>10</td></tr> </table>	1	2	3	4	5	6	7	8	9	10	<table border="1"> <tr><td>3</td><td>4</td></tr> <tr><td>1</td><td>2</td></tr> </table>	3	4	1	2	Q1, Q2
1	2																
3	4																
5	6																
7	8																
9	10																
3	4																
1	2																
DUAL 2.9V & 3.3V DUAL 2.8V & 3.3V	<table border="1"> <tr><td>1</td><td>2</td></tr> <tr><td>3</td><td>4</td></tr> <tr><td>5</td><td>6</td></tr> <tr><td>7</td><td>8</td></tr> <tr><td>9</td><td>10</td></tr> </table>	1	2	3	4	5	6	7	8	9	10	<table border="1"> <tr><td>3</td><td>4</td></tr> <tr><td>1</td><td>2</td></tr> </table>	3	4	1	2	Q1,Q2
1	2																
3	4																
5	6																
7	8																
9	10																
3	4																
1	2																
DUAL 32.9V & 3.52V DUAL 2.8V & 3.52V	<table border="1"> <tr><td>1</td><td>2</td></tr> <tr><td>3</td><td>4</td></tr> <tr><td>5</td><td>6</td></tr> <tr><td>7</td><td>8</td></tr> <tr><td>9</td><td>10</td></tr> </table>	1	2	3	4	5	6	7	8	9	10	<table border="1"> <tr><td>3</td><td>4</td></tr> <tr><td>1</td><td>2</td></tr> </table>	3	4	1	2	Q1,Q2
1	2																
3	4																
5	6																
7	8																
9	10																
3	4																
1	2																

CPU Power Type/jumper number	JP12	JP8	Regulator Installed														
DUAL 2.7V & 3.3V	<table border="1"> <tr><td>1</td><td>2</td></tr> <tr><td>3</td><td>4</td></tr> <tr><td>5</td><td>6</td></tr> <tr><td>7</td><td>8</td></tr> <tr><td>9</td><td>10</td></tr> </table>	1	2	3	4	5	6	7	8	9	10	<table border="1"> <tr><td>3</td><td>4</td></tr> <tr><td>1</td><td>2</td></tr> </table>	3	4	1	2	Q1,Q2
1	2																
3	4																
5	6																
7	8																
9	10																
3	4																
1	2																
DUAL 2.7V & 3.52V	<table border="1"> <tr><td>1</td><td>2</td></tr> <tr><td>3</td><td>4</td></tr> <tr><td>5</td><td>6</td></tr> <tr><td>7</td><td>8</td></tr> <tr><td>9</td><td>10</td></tr> </table>	1	2	3	4	5	6	7	8	9	10	<table border="1"> <tr><td>3</td><td>4</td></tr> <tr><td>1</td><td>2</td></tr> </table>	3	4	1	2	Q1,Q2
1	2																
3	4																
5	6																
7	8																
9	10																
3	4																
1	2																
DUAL 2.5V & 3.3V	<table border="1"> <tr><td>1</td><td>2</td></tr> <tr><td>3</td><td>4</td></tr> <tr><td>5</td><td>6</td></tr> <tr><td>7</td><td>8</td></tr> <tr><td>9</td><td>10</td></tr> </table>	1	2	3	4	5	6	7	8	9	10	<table border="1"> <tr><td>3</td><td>4</td></tr> <tr><td>1</td><td>2</td></tr> </table>	3	4	1	2	Q1,Q2
1	2																
3	4																
5	6																
7	8																
9	10																
3	4																
1	2																

CPU Power Type/jumper jumper	JP12	JP8	Regulator Installed														
<p>DUAL 2.5V & 3.52V</p>	<table border="1"> <tr> <td>1</td> <td>2</td> </tr> <tr> <td>3</td> <td>4</td> </tr> <tr> <td>5</td> <td>6</td> </tr> <tr> <td>7</td> <td>8</td> </tr> <tr> <td>9</td> <td>10</td> </tr> </table>	1	2	3	4	5	6	7	8	9	10	<table border="1"> <tr> <td>3</td> <td>4</td> </tr> <tr> <td>1</td> <td>2</td> </tr> </table>	3	4	1	2	<p>Q1,Q2</p>
1	2																
3	4																
5	6																
7	8																
9	10																
3	4																
1	2																

JP10,JP11:SET BUS CLK & FREQUENCY/RATIO

JP10,JP11 SET FOR CPU FREQUENCY/RATIO		JP2(1-9) They are used to set BUS CLK							
JP10,JP11 CONNECT	Frequency/Ratio	JP2 CONNECT			BUS CLK(MHZ)				
<table border="1"> <tr><td>2</td><td>1</td></tr> <tr><td>2</td><td>1</td></tr> </table>	2	1	2	1	1.5X	3	2	1	50
2	1								
2	1								
		6	5	4					
		9	8	7					
		3	2	1	55				
		6	5	4					
		9	8	7					
<table border="1"> <tr><td>2</td><td>1</td></tr> <tr><td>2</td><td>1</td></tr> </table>	2	1	2	1	2X	3	2	1	60
2	1								
2	1								
		6	5	4					
		9	8	7					
<table border="1"> <tr><td>2</td><td>1</td></tr> <tr><td>2</td><td>1</td></tr> </table>	2	1	2	1	2.5X	3	2	1	66
2	1								
2	1								
		6	5	4					
		9	8	7					
<table border="1"> <tr><td>2</td><td>1</td></tr> <tr><td>2</td><td>1</td></tr> </table>	2	1	2	1	3X	3	2	1	
2	1								
2	1								
		6	5	4					
		9	8	7					

<p>P.S 83MHZ BUS CLK RESERVED FOR SYSTEM</p>				<p>75</p>
	3	2	1	
	6	5	4	
	3	2	1	
	6	5	4	
9	8	7		

QUICK TO SETTING FOR CYRIX 6X86 CPU CLK & VOLTAGE

Product Name	CPU Speed (MHZ)	BUS CLK	JP2 Connect	JP10,J11 Connect	Multiplier	
6X86-P120+GP	100	50	3	2	1	2X
			6	5	4	
			9	8	7	
			<table border="1" style="margin: auto; border-collapse: collapse;"> <tr> <td style="text-align: center;">2</td> <td style="text-align: center;">1</td> </tr> <tr style="background-color: #cccccc;"> <td style="text-align: center;">2</td> <td style="text-align: center;">1</td> </tr> </table>			
2	1					
2	1					
6X86-P133+GP	110	55	3	2	1	2X
			6	5	4	
			9	8	7	
			<table border="1" style="margin: auto; border-collapse: collapse;"> <tr> <td style="text-align: center;">2</td> <td style="text-align: center;">1</td> </tr> <tr style="background-color: #cccccc;"> <td style="text-align: center;">2</td> <td style="text-align: center;">1</td> </tr> </table>			
2	1					
2	1					
6X86-P150+GP	120	60	3	2	1	2X
			6	5	4	
			9	8	7	
			<table border="1" style="margin: auto; border-collapse: collapse;"> <tr> <td style="text-align: center;">2</td> <td style="text-align: center;">1</td> </tr> <tr style="background-color: #cccccc;"> <td style="text-align: center;">2</td> <td style="text-align: center;">1</td> </tr> </table>			
2	1					
2	1					

6X86-P166+GP	133	66	<table border="1"> <tr><td>3</td><td>2</td><td>1</td></tr> <tr><td>6</td><td>5</td><td>4</td></tr> <tr><td>9</td><td>8</td><td>7</td></tr> </table>	3	2	1	6	5	4	9	8	7	<table border="1"> <tr><td>2</td><td>1</td></tr> <tr><td>2</td><td>1</td></tr> </table>	2	1	2	1	2X
3	2	1																
6	5	4																
9	8	7																
2	1																	
2	1																	
6X86-P200+GP	150	75	<table border="1"> <tr><td>3</td><td>2</td><td>1</td></tr> <tr><td>6</td><td>5</td><td>4</td></tr> <tr><td>9</td><td>8</td><td>7</td></tr> </table>	3	2	1	6	5	4	9	8	7	<table border="1"> <tr><td>2</td><td>1</td></tr> <tr><td>2</td><td>1</td></tr> </table>	2	1	2	1	2X
3	2	1																
6	5	4																
9	8	7																
2	1																	
2	1																	

SET CPU VOLTAGE

CPU Power Type/jumper iumber	JP12	JP8	Regulator Installed														
ONLY 3.3V	<table border="1"> <tr><td>1</td><td>2</td></tr> <tr><td>3</td><td>4</td></tr> <tr><td>5</td><td>6</td></tr> <tr><td>7</td><td>8</td></tr> <tr><td>9</td><td>10</td></tr> </table>	1	2	3	4	5	6	7	8	9	10	<table border="1"> <tr><td>3</td><td>4</td></tr> <tr><td>1</td><td>2</td></tr> </table>	3	4	1	2	Q2
	1	2															
	3	4															
	5	6															
	7	8															
9	10																
3	4																
1	2																
			Q2														

2

ONLY 3.52V

1	
3	4
5	6
7	8
9	10

3	4
1	2

DUAL 3.3V & 3.3V

1	2
3	4
5	6
7	8

3	4
1	2

Q1,Q2

CPU Power Type/jumper iumber	JP12	JP8	Regulator Installed
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DUAL 3.52V&3.52V

1	2
3	4
5	6
7	8
9	10

3	4
1	2

Q1,Q2

DUAL 2.9V&3.3V

1	2
3	4
5	6
7	8

3	4
1	2

Q1,Q2

9	10
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DUAL 2.9V & 3.52V	1	2	<table border="1"> <tr> <td>3</td> <td>4</td> </tr> <tr> <td>1</td> <td>2</td> </tr> </table>	3	4	1	2	Q1,Q2
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CPU Power Type/jumper iumber	JP12	JP8	Regulator Installed														
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CPU Power Type/jumper number	JP12	JP8	Regulator Installed														
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CACHE MEMORY INSTALLATION AND JUMPER SETTING

(1) ON BOARD 256K OR 512K CACHE LOCATION AT U1,U3,U4

TYPE	TAG SRAM(U1)	PIPEL SRAM	JP1 CONNECT

256K	32K*8	U3,U4	<table border="1"> <tbody> <tr> <td>1</td> <td>2</td> </tr> <tr> <td>3</td> <td>4</td> </tr> <tr> <td>5</td> <td>6</td> </tr> </tbody> </table>	1	2	3	4	5	6
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3	4								
5	6								
512K	32K*8	U3,U4	<table border="1"> <tbody> <tr> <td>1</td> <td>2</td> </tr> <tr> <td>3</td> <td>4</td> </tr> <tr> <td>5</td> <td>6</td> </tr> </tbody> </table>	1	2	3	4	5	6
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3	4								
5	6								
TYPE	TAG SRAM(U1)	PIPEL SRAM	JP1 CONNECT						
512K	32K*8	U3,U4,J10	<table border="1"> <tbody> <tr> <td>1</td> <td>2</td> </tr> <tr> <td>3</td> <td>4</td> </tr> <tr> <td>5</td> <td>6</td> </tr> </tbody> </table>	1	2	3	4	5	6
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**ON BOARD 256K+CACHE MODULE 256K UPDATE TO 512K
CACHE LOCATION AT U1 ,U3,U4,J10.**