- The undocumented jumper JP1 (next to keyboard connector) has to be closed or the keyboard won't work (not the same as keylock)
- You get 3.5x multiplier (for 233MHz) by bridging pin 1 of JP20 with pin 1 of JP21 (vertically)
- Board only supports 256KB COAST (Cache On A STick) modules, 512KB modules get detected as 256KB
- Enabling 512KB L2 cache (256KB onboard + 256KB COAST module) doesn't seem to work, leading to no L2 cache at all when jumpered this way
- The voltage regulators are undersized for Pentium MMX faster than 166MHz. Needs active cooling. Forget about K6es and Cyrixes
- The voltage regulator heatsinks are "live" at core voltage. Be really careful not to short anything to these while powered on
- 2.8-2.9V (~2.84-2.87V) core voltage by leaving JP12 unpopulated and JP13 open, closed, open, closed closed. WILL NEED FAN ON VRM
- All electrolytic caps were bad (measuring way out of tolerances). Best to replace all of them
- USB header can be soldered in and works if southbridge is sspec SU092. If it's SU056 it's buggy and USB won't work.
- Pin 1 (5V) is marked with white triangle, 5V/D-/D+/GND/NC. Standard/"modern" USBslot brackets will work