

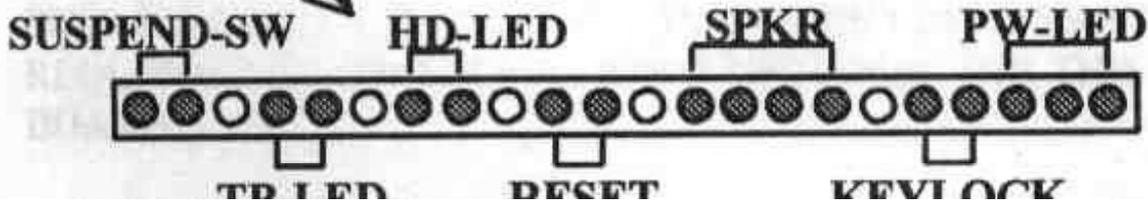
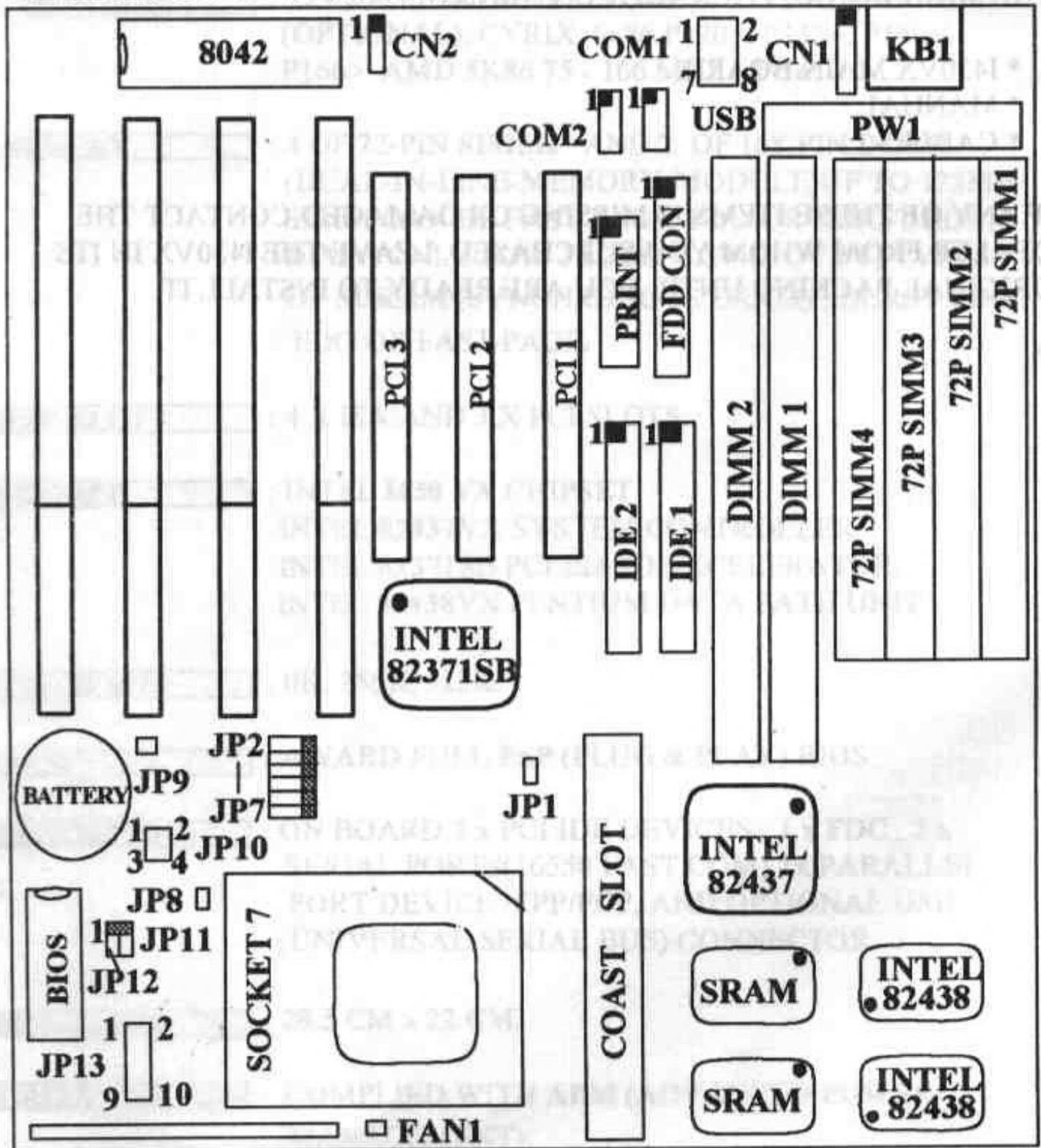
-2 SPECIFICATIONS

- CPU** : 75 - 200 MHZ INTEL PENTIUM P54C /P55C (OPTIONAL), CYRIX 6x86 P120+, P133+, P150+, P166+ AMD 5K86 75 - 166 MHZ CPUs.
- MEMORY** : 4 OF 72-PIN SIMMs AND 2 OF 168-PIN DIMM (DUAL-IN-LINE-MEMORY-MODULE) UP TO 128MB. SIMM CAN BE FAST PAGE, EDO OR BURST EDO, DIMM ONLY FOR JEDEC 3.3V (NOT 5V) TYPES OF SDRAM (SYNCHRONOUS DRAM), BURST EDO, EDO OR FAST PAGE.
- EXP. SLOT** : 4 X ISA AND 3 X PCI SLOTS.
- CHIPSET** : INTEL I430 VX CHIPSET
INTEL 82437VX SYSTEM CONTROLLER
INTEL 82371SB PCI ISA IDE XCELERATOR.
INTEL 82438VX PENTIUM DATA PATH UNIT.
- CACHE SIZE** : 0K, 256K, 512K.
- BIOS** : AWARD FULL PnP (PLUG & PLAY) BIOS.
- I/O FUNCTION** : ON BOARD 2 x PCI IDE DEVICES , 1 x FDC , 2 x SERIAL PORTS(16550 FAST COM), 1x PARALLEL PORT DEVICE /EPP/ECP, AND OPTIONAL USB (UNIVERSAL SERIAL BUS) CONNECTOR.
- BOARD SIZE** : 28.5 CM x 22 CM.
- GREEN FUNCTION** : COMPLIED WITH APM (ADVANCED POWER MANAGEMENT).

* P55C IS INTEL'S "MMX" (MULTI-MEDIA-EXTENDED INSTRUCTION) SETS TYPE CPU, CPU CORE IS 2.5V-2.9V BUT I/O OPERATION REQUIRES 3.3V, THIS FUNCTION IS OPTIONAL FOR THIS MAIN BOARD.

CHAPTER 2. INSTALLATION

2-1 LAYOUT REFERENCE



2-2 JUMPER SETTINGS

1.JP2-JP4,JP6,JP7 : INTEL/CYRIX /AMD CPU TYPE SELECTOR

INTEL	CPU	CLOCK SPEED	RATIO	JP2	JP3	JP4	JP6	JP7	JP8	JP10	JP11 JP12	JP13
	P54C 75 3.3V	(50MHZ)	1.5	1-2	1-2	1-2	1-2	1-2	ON	3-4	OFF	3-4
	P54C 90 3.3V	(60MHZ)	1.5	1-2	1-2	2-3	1-2	1-2	ON	3-4	OFF	3-4
	P54C 100 3.3V	(66MHZ)	1.5	1-2	2-3	1-2	1-2	1-2	ON	3-4	OFF	3-4
	P54C 120 3.3V	(60MHZ)	2	1-2	1-2	2-3	2-3	1-2	ON	3-4	OFF	3-4
	P54C 133 3.3V	(66MHZ)	2	1-2	2-3	1-2	2-3	1-2	ON	3-4	OFF	3-4
	P54C 150 3.3V	(60MHZ)	2.5	1-2	1-2	2-3	2-3	2-3	ON	3-4	OFF	3-4
	P54C 166 3.3V	(66MHZ)	2.5	1-2	2-3	1-2	2-3	2-3	ON	3-4	OFF	3-4
	P55C 166 2.8/3.3V	(66MHZ)	2.5	1-2	2-3	1-2	2-3	2-3	OFF	3-4	ON	9-10
	P54C 180 3.3V	(60MHZ)	3	1-2	1-2	2-3	1-2	2-3	ON	3-4	OFF	3-4
	P54C 200 3.3V	(66MHZ)	3	1-2	2-3	1-2	1-2	2-3	ON	3-4	OFF	3-4
	P55C 200 2.8/3.3V	(66MHZ)	3	1-2	2-3	1-2	1-2	2-3	OFF	3-4	ON	9-10
	P55C 233 2.8/3.3V	(66MHZ)	3.5	1-2	2-3	1-2	1-2	1-2	OFF	3-4	ON	9-10
AMD	CPU FOR (PRxxxAB)	CLOCK SPEED	RATIO	JP2	JP3	JP4	JP6	JP7	JP8	JP10	JP11 JP12	JP13
	PR75 3.52V	(50MHZ)	1.5	1-2	1-2	1-2	1-2	1-2	ON	1-2	OFF	1-2
	PR90/120 3.52V	(60MHZ)	1.5	1-2	1-2	2-3	1-2	1-2	ON	1-2	OFF	1-2
	PR100/133 3.52V	(66MHZ)	1.5	1-2	2-3	1-2	1-2	1-2	ON	1-2	OFF	1-2
	PR166 3.52V	(66MHZ)	2.5	1-2	2-3	1-2	2-3	2-3	ON	1-2	OFF	1-2
	K6 PR166 2.9/3.3V	(66MHZ)	2.5	1-2	2-3	1-2	2-3	2-3	OFF	3-4	ON	5-6
	K6 PR200 2.9/3.3V	(66MHZ)	3	1-2	2-3	1-2	1-2	2-3	OFF	3-4	ON	5-6
	*K6 PR233 3.3/3.3V	(66MHZ)	3.5	1-2	2-3	1-2	1-2	1-2	OFF	3-4	ON	3-4
CYRIX	CPU	CLOCK SPEED	RATIO	JP2	JP3	JP4	JP6	JP7	JP8	JP10	JP11 JP12	JP13
	P120+ 3.52V	(50MHZ)	2	1-2	1-2	1-2	2-3	1-2	ON	1-2	OFF	1-2
	P133+ 3.52V	(55MHZ)	2	2-3	1-2	1-2	2-3	1-2	ON	1-2	OFF	1-2
	P150+ 3.52V	(60MHZ)	2	1-2	1-2	2-3	2-3	1-2	ON	1-2	OFF	1-2
	P166+ 3.52V	(66MHZ)	2	1-2	2-3	1-2	2-3	1-2	ON	1-2	OFF	1-2
	P200+ 3.52V	(75MHZ)	2	2-3	1-2	2-3	2-3	1-2	ON	1-2	OFF	1-2
	P133+ 2.8V/3.3V 6X86L	(55MHZ)	2	2-3	1-2	1-2	2-3	1-2	OFF	3-4	ON	9-10
	P150+ 2.8V /3.3V 6X86L	(60MHZ)	2	1-2	1-2	2-3	2-3	1-2	OFF	3-4	ON	9-10
	P166+ 2.8V/3.3V 6X86L	(66MHZ)	2	1-2	2-3	1-2	2-3	1-2	OFF	3-4	ON	9-10
	P200+ 2.8/3.3V 6X86L	(75MHZ)	2	2-3	1-2	2-3	2-3	1-2	OFF	3-4	ON	9-10

NOTE : "*" THIS CPU HAD NOT BEEN TESTED WHEN THIS WAS PRINTED

NOTE : INTEL P55C MMX, AMD K6 MMX AND CYRIX 6X86L ARE "DUAL VOLTAGE CPUs" "6X86L" IS CYRIX NEW LOW POWER CPU.

EXPLANATION OF JUMPER SETTING

JP2-JP4 CPU & PCI BUS FREQUENCY SELECTOR

	50 MHZ	55 MHZ	60 MHZ	66.6 MHZ	75MHZ
JP2	1-2	2-3	1-2	1-2	2-3
JP3	1-2	1-2	1-2	2-3	1-2
JP4	1-2	1-2	2-3	1-2	2-3

JP6,JP7 CPU CLOCK SELECTOR

	1.5 X	2.0 X	2.5 X	3.0 X	3.5 X
JP6	1-2	2-3	2-3	1-2	1-2
JP7	1-2	1-2	2-3	2-3	1-2

JP10 : SECOND REGULATOR VOLTAGE SELECTOR (OPTIONAL)

	3.52V	3.3V
JP10	1-2	3-4

NOTE : INTEL P55C, AMD K6 AND CYRIX 6X86L SET "3-4"

JP13 : CPU V CORE VOLTAGE SELECTOR (BLACK JUMPER CAP)

	3.52 V	3.3V	2.9V	2.7V	2.8V
JP13	1-2	3-4	5-6	7-8	9-10

NOTE : INTEL P55C MMX CPU AND CYRIX 6X86L SET 2.8V

JP8,JP11,JP12 : SINGLE /DUAL VOLTAGE CPU SELECTOR

	SINGLE VOLTAGE CPU	DUAL VOLTAGE CPU
JP8	ON	OFF
JP11,JP12	OFF	ON

2.JP1 : ONBOARD TAG SELECTOR (BLACK JUMPER CAP)

	TAG ONBOARD	NONE
JP1	ON	OFF

3.JP9 : BATTERY SELECTOR (BLACK JUMPER CAP)

	NORMAL	CLEAR CMOS
JP9	OFF (DEFAULT)	ON

- ☞ CUSTOMER NEEDS TO CLEAR CMOS, THEN RECONFIGURE IT IF FORGETS PASSWORD FOR BIOS SETUP.
- ☞ PS/2 MOUSE CABLE IS OPTIONAL, THE PIN #1 OF CABLE IS YELLOW COLOR.

4.JP5 PCI BUS FREQUENCY SELECTION

CPU FREQUENCY	PCI BUS FREQUENCY	
50 MHZ	JP5 : 1-2 32MHZ (DEFAULT)	JP5 : 2-3 25MHZ
55 MHZ	JP5 : 1-2 32MHZ (DEFAULT)	JP5 : 2-3 27.5MHZ
60 MHZ	JP5 : 1-2 32MHZ (DEFAULT)	JP5 : 2-3 30MHZ
66.6 MHZ	JP5 : 1-2 32MHZ (DEFAULT)	JP5 : 2-3 33.3MHZ
75 MHZ	JP5 : 1-2 32MHZ (DEFAULT)	JP5 : 2-3 37.5MHZ

5. OTHER JUMPER SETTINGS AND CONNECTORS :

- CN1 : PS/2 MOUSE CONNECTOR.
- CN2 : Infra Red (IR) CONNECTOR .
- IDE1 ::PRIMARY IDE CONNECTOR.
- IDE2 : SECONDARY IDE CONNECTOR
- FDC1 : FLOPPY DISK CONNECTOR .
- COM1 : SERIAL PORT 1 CONNECTOR.
- COM2 : SERIAL PORT 2 CONNECTOR.
- LPT1 : PARALLEL PORT CONNECTOR.
- USB : USB (UNIVERSAL SERIAL BUS) CONNECTOR .
- FAN1 : FOR 12V CPU FAN CONNECTOR.

NOTE : USB (UNIVERSAL SERIAL BUS) AND IRCON CONNECTOR ARE OPTIONAL.

6. CN2 : IRCON (INFRARED) CONNECTOR

IRCON PIN OUT			
PIN 1	RX	PIN 2	GND
PIN 3	TX	PIN 4	+5V

NOTE:IRCON USES SAME I/O PORT AS COM2. THERE IS NO ANY HARDWARE JUMPER SETTING FOR IRCON/COM2 ON THIS MAIN BOARD BUT CUSTOMER NEEDS TO SET PROPER BIOS SETTING FOR "HPSIR", "ASKIR" OR "DISABLED"(DEFAULT) UNDER "Infra Red (IR) Function" OF "INTEGRATED PERIPHERAL" ACCORDING TO THE FOLLOWING TABLE:

	HPSIR	ASKIR	DISABLED
(UNDER INTEGRATED PERIPHERALS) " Infra Red (IR) Function "	USE HEWLETT PACKARD IR ON IRCON.	USE AMPLITUDE SHIFT KEYED IR ON IRCON .	USE COM2 (DEFAULT)

7.USB : USB (UNIVERSAL SERIAL BUS) CONNECTOR

USB PIN OUT	
USB1	USB2
PIN1 +5V	PIN2 +5V
PIN3 USBP0-	PIN4 USBP1-
PIN5 USBP0+	PIN6 USBP1+
PIN7 GND	PIN8 GND

8. FAN1 (FOR 12V CPU FAN) CONNECT

FAN PIN OUT	
PIN1 +12V	PIN2 GND

2-3 MEMORY INSTALLATION

NO JUMPER SETTING IS NECESSARY FOR DRAM SETTING, BIOS WILL CHECK DRAM TYPE AND SIZE AUTOMATICALLY. **I430VX** MAIN BOARD CONTAINS 4 BY 72-PIN SIMM SOCKETS (SIMM1, SIMM2, SIMM3, SIMM4) OR 2 BY 168-PIN DIMM SOCKET (DIMM). SIMM MODULE SOCKETS ARE DIVIDED IN TWO BANKS : SIMM1, SIMM2 IN ONE BANK AND SIMM3, SIMM4 IN ANOTHER BANK, MINIMUN USER HAS TO INSTALL TWO DRAM SIMMs OF THE SAME KIND INTO ONE BANK OR INSTALL ONE DIMM INTO DIMM SOCKET. **I430VX** MAIN BOARD HAS TABLE-FREE (OR AUTO-BANK) FEATURE AND USER CAN INSTALL SIMMs INTO ANY BANK. BUT CANNOT MIX UP SIMM WITH DIMM SINCE CHIPS LIMITATION.

NOTE : CANNOT MIX SIMM WITH DIMM

SIMM4	SIMM3	SIMM2	SIMM1	DIM2	DIM1	TOTAL
4MB	4MB	---	---	---	---	8MBytes
---	---	---	---	8MB	---	8MBytes
4MB	4MB	4MB	4MB	---	---	16MBytes
8MB	8MB	---	---	---	---	16MBytes
---	---	---	---	8MB	8MB	16MBytes
---	---	---	---	16MB	---	16MBytes
4MB	4MB	8MB	8MB	---	---	24MBytes
8MB	8MB	4MB	4MB	---	---	24MBytes
8MB	8MB	8MB	8MB	---	---	32MBytes
16MB	16MB	---	---	---	---	32MBytes
---	---	---	---	16MB	16MB	32MBytes
---	---	---	---	32MB	---	32MBytes
16MB	16MB	4MB	4MB	---	---	40MBytes
8MB	8MB	16MB	16MB	---	---	48MBytes
16MB	16MB	8MB	8MB	---	---	48MBytes
16MB	16MB	16MB	16MB	---	---	64MBytes
32MB	32MB	---	---	---	---	64MBytes
---	---	---	---	32MB	32MB	64MBytes
8MB	8MB	32MB	32MB	---	---	80MBytes
32MB	32MB	8MB	8MB	---	---	80MBytes
16MB	16MB	32MB	32MB	---	---	96MBytes
32MB	32MB	16MB	16MB	---	---	96MBytes
32MB	32MB	32MB	32MB	---	---	128MBytes