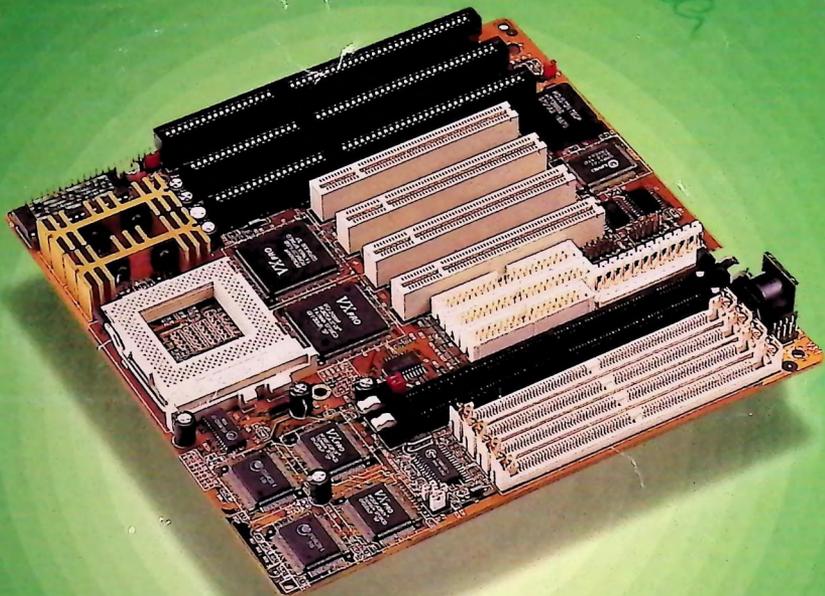


# 586 Mainboard

*New Generation VX<sub>PRO</sub> Chipset  
PCI Bus and ISA Bus*

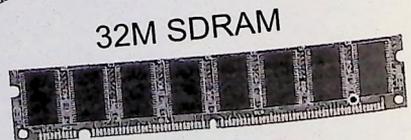
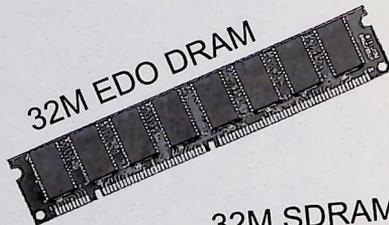


## INTRODUCTION & USER'S MANUAL

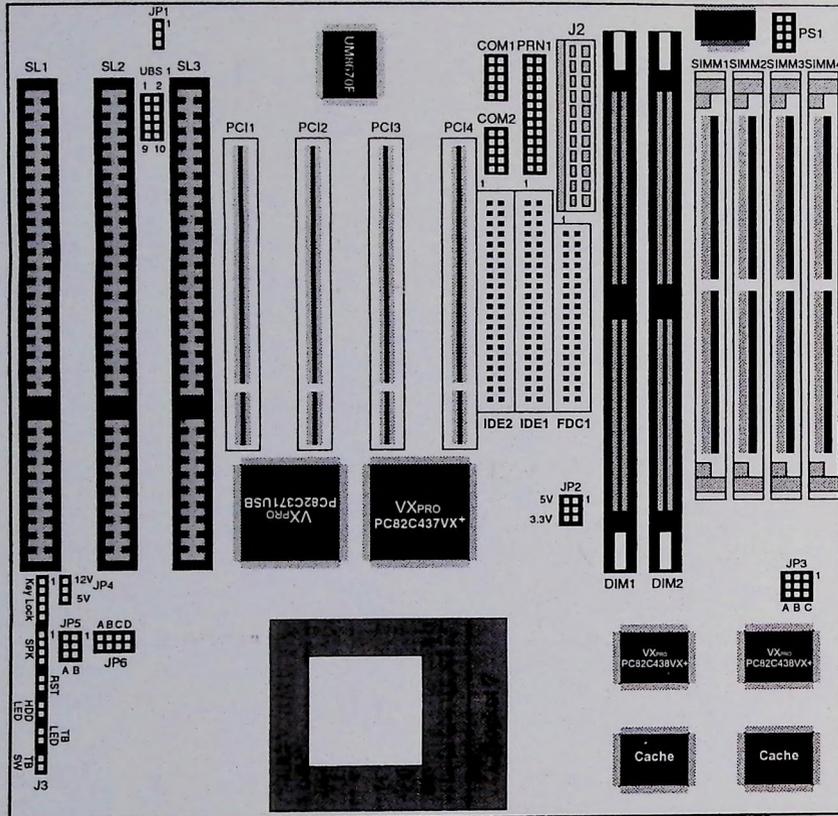
- With PCI IDE & Multi I/O
- Fully Compatible with Intel **MMX™** Technology with VX<sub>PRO</sub> Chipset
- Features SDRAM Modules
- Offers High Degree of Flexibility with Revolutionary Combined DIMM and SIMM Configurations

# Quick Installation Guide

1. Set JP1 to CMOS RAM discharge jumper (pin 1 - 2)
2. Set JP3 to select CPU speed
3. Set JP5 to select CPU Internal Clock Speed
4. Set JP6 - A, B, C, D to select CPU Voltage Regulator Output
5. Insert CPU to CPU socket
6. Insert 72-pin SIMM modules into SIMM1 - 4 and/or insert 168-pin DIMM modules into DIM1 - 2, notice that DIM2 and SIMM1,2 can not be installed at the same time
7. Install mainboard into system chassis
8. Connect keyboard to J1
9. Insert the display card and other peripheral cards (if required) onto the mainboard
10. Connect hard disk(s) to IDE primary/secondary connector(s)
11. Connect floppy drive(s) to FDC1 connector
12. Connect serial ports to COM1 and COM2 connectors
13. Connect parallel port to PRN1 connector
14. Connect J3-HDD LED to "Hard Disk Busy" LED on the system chassis
15. Connect J3-TB LED to Turbo LED on the system chassis
16. Connect J3-RST to Reset Switch on the system chassis
17. Connect J3-SPK to Speaker on the system chassis
18. Connect J3-KEYLOCK to keylock and power LED on the system chassis
19. Connect power cord to J2 Power Supply Connector
20. Close system chassis, connect all external cables to your computer.



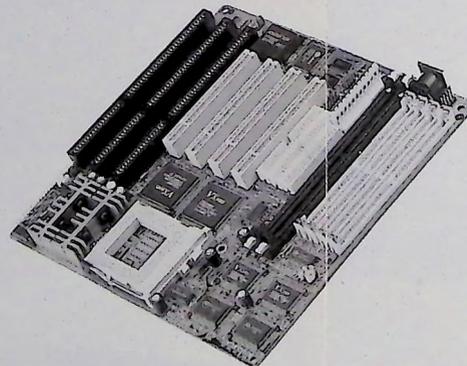
# Jumper Settings



## J1 Keyboard Connector

A standard five-pin female DIN keyboard connector is located at the rear of the board J1.

Pin	Description
1	Keyboard Clock
2	Keyboard Data
3	N.C.
4	Ground
5	+5VDC



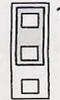
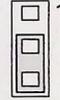
# Jumper Settings

## J2 Power Supply Connector

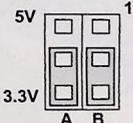
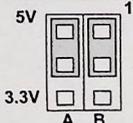
Pin	Description	Pin	Description
1	Power Good	7	Ground
2	+5VDC	8	Ground
3	+12VDC	9	- 5VDC
4	- 12VDC	10	+5VDC
5	Ground	11	+5VDC
6	Ground	12	+5VDC

## JP1: CMOS RAM Discharge Jumper

Pin	Description
1	Internal Battery Positive
2	Connector to CMOS
3	Ground

Description	JP1
Internal Battery Mode	
Discharge CMOS	

## JP2: DIMM Module Voltage Selector

Description	JP2
For SDRAM DIMM Module (3.3V)	
For EDO DRAM / Fast Page DRAM DIMM Module (5V)	

# Jumper Settings

## J3: RST (Reset Switch Connector)

Setting	Description
Open	Normal Mode
Short	Reset System

## J3: KEYLOCK(Keylock & Power LED Connector)

Pin	Description
1	LED Output
2	N.C.
3	Ground
4	Keylock
5	Ground

## J3: TB LED (Turbo LED Connector)

Pin	Description
1	Anode(+)
2	Cathode(-), Ground

## J3 SPK (Speaker Connector)

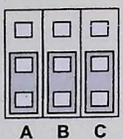
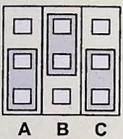
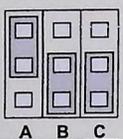
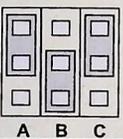
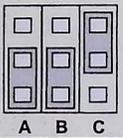
Pin	Description
1	DATA Out
2	N.C.
3	Ground
4	+5V

## J3 HDD LED (Hard Disk LED Connector)

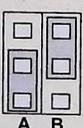
Pin	Description
1	+5V
2	Active Low,

# Jumper Settings

## JP3: CPU Speed Jumpers

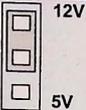
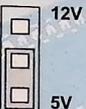
CPU Clock	JP3A~C	CPU Clock	JP3A~C
50MHz	 A B C	66MHz	 A B C
55MHz	 A B C	75MHz	 A B C
60MHz	 A B C		

## JP5: CPU Internal Clock Speed Jumpers

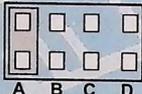
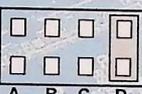
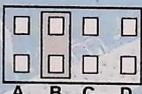
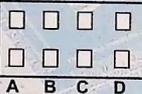
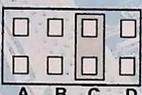
Intel	Cyrix	AMD	JP5
1.5X	Reserved	1.5X	 A B
2.0X	2.0X	Reserved	 A B
2.5X	Reserved	2.5X	 A B
3.0X	Reserved	3.0X	 A B

# Jumper Settings

## JP4: Flash ROM Voltage Jumper

Description	JP4
12 Volt Flash programming	 12V 5V
5 Volt Flash programming	 12V 5V

## JP6-A, B, C, D: CPU Voltage Regulator Output

	Setting		Setting
3.5V	 A B C D	2.7V	 A B C D
2.9V	 A B C D	2.5V	 A B C D
2.8V	 A B C D		

## PS1: PS/2 Mouse Connector

Pin	Description
1	Mouse Data
2	N.C.
3	Ground
4	+5V
5	Mouse CLK
6	N.C.

## USB1: 2 Sets of Universal Serial Bus Connector

Pin	Description
1,2	+5VDC
3,4	Data -
5,6	Data +
7,8	Ground
9,10	Ground

# SPECIFICATION

- VXPRO 586 chipset: new technology for MMX™ CPUs (including Intel MMX™ P55C, Cyrix M2, and AMD K6)
- Supports 75/90/100/120/133/166/180/200/233 MHz Pentium™ CPUs with 321pin ZIF socket
- Supports Pentium™ P54C and P55C
- Supports Cyrix 6x86, M2, IBM 6x86, and AMD K5/K6 CPUs
- Uses four 72-pin EDO/Page Mode SIMM modules auto banking in multiple configuration up to 256MB
- Provides 2x168-pin DIMM to support SDRAM/EDO DRAM/Page Mode DRAM
- Supports "Table Free" configuration so that DIMM and SIMM can be installed in any combination, except that SIMM 1, 2 and DIMM 2 can not be installed at the same time
- Supports up to 384MB of system memory through SIMM and DIMM Sockets
- Supports 512K on board Pipelined Burst synchronous cache
- Four PCI Local Bus slots and three 16 bits ISA Bus slots
- All four PCI slots support Master Mode
- System BIOS supports four IDE hard disk drives without device driver for *S/W* application and the capacity of each hard disk can be larger than 528MB up to 8.4GB
- Onboard PCI Bus Master IDE interface with two connectors supports four IDE devices in 2 channels and the PCI IDE Controller supports PIO Mode 0 to Mode 4 at maximum transfer rate of 16.67MB/s and Bus Master IDE DMA Mode 2
- Onboard super Multi-I/O chip that supports two serial ports with 16550 Fast UART compatible, one parallel port with EPP and ECP capabilities, and one floppy disk drive interface
- Supports PS/2™ mouse
- Supports the Universal Serial Bus (USB)
- System BIOS supports NCR810 SCSI BIOS firmware and Green feature function
- "Plug and Play" Flash ROM .

