

RP12, RP13, RP15 : Cache Size Selection

Cache	128K	512K	512K
RP SEL	RP12	RP13	RP15

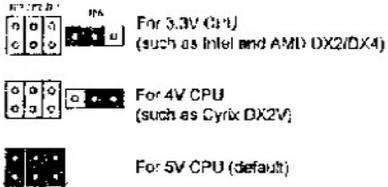
JP11 - JP13 : CPU Clock Selection

System Clock	If MAX318 is ON			If MAX318 is OFF		
	JP13	JP12	JP11	JP13	JP12	JP11
25MHz	O	S	O	O	O	O
33MHz	S	S	S	S	O	S
40MHz	S	S	O	S	O	C
50MHz	O	O	S	O	O	S

JP1 - JP3, JP6, JP20 & JP21, JP9 & JP10, JP16, JP15, JP25, RP5 - RP10 : CPU Type Jumper Setting

CPU Type	DPI Power Voltage	JP1-JP3	JP6	JP20	JP21	JP9 & JP10	JP16	JP15	JP25	RP5	RP6	RP7	RP8	RP9	RP10
Intel 486DX	5	S	X	2-3	2-3	1-2	1-2	O	O	S	O	O	S	C	O
UMC U58															
Intel 486DXDX2	5	S	X												
AMD 486DXDX2															
Intel DX4 (write through)	3.45	O		1-2	1-2	1-2	1-2	O	O	S	O	O	S	O	O
AMD DX4-NV8T															
Intel CPDP	5	S	X	2-3	2-3	1-2	2-3	O	O	O	S	O	S	O	O
AMD DX4-60	3.45	O		1-2	2-3	1-2	1-2	O	1-2	O	S	O	S	O	O
Cyrix DX4-DX2	5	S	X												
Cyrix DX2V	4	O		2-3	1-2	1-2	1-2	1-2	O	O	S	O	O	S	O
Cyrix DX4	3.45	O		1-2	1-2	1-2	1-2	2-3	O	O	S	O	O	S	O
Intel P24D (486DX2 write back)	5	S	X												
Cyrix M15C															
Intel DX4 (write back)	3.45	O		1-2	2-3	1-2	1-2	O	2-3	O	O	S	O	O	S
AMD DX4-SV8B															
Cyrix DX4 (DX4 P/D)	3.45	O		1-2	1-2	1-2	1-2	O	2-3	O	O	S	O	O	S

*** JP1 - JP3, JP6: CPU power select**



- J1: Speaker Connector
- J2: Keyboard lock Connector
- J3: Reset Switch
- J4: Turbo LED
- J5: Turbo Switch
- J6: Peripheral Green Control Connector
- J7: External battery Connector

- D1: Green LED
- TR2: Regulator socket

- JP4: VL-Bus wait state setup
1-2: 0 wait state
2-3: 1 wait state (default)
- JP5: VL-Bus wait state setup
1-2: <= 33MHz
2-3: > 33MHz (default)

- JP17: Power saving toggle switch connector
- JP22: CMOS data clear
1-2 short: default
2-3 short: clear CMOS data

Remark:

1-2: Pin1 & Pin2 short; "X": don't care; "O": Open; "S": Short.
 The AMD's AM486DX4-100 NV8T is a non-green and 8K internal write through cache CPU.
 The AMD's AM486DX4-100 SV8B (Enhanced AM486DX4) is a green and 8K internal write back cache CPU.

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 P1
 N6P
 N3
 N5