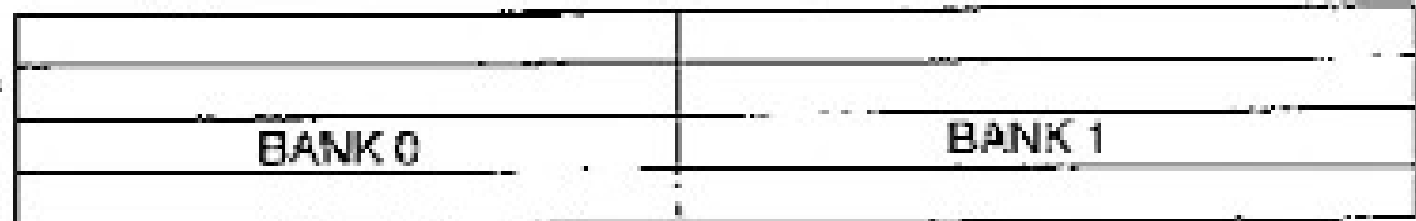


Power-
Connector



OSC

WEITEK
4167



JP3



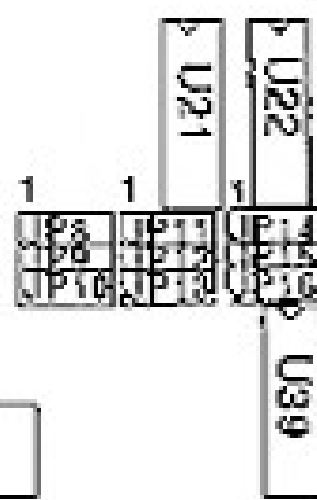
JP4 JP5



JP6



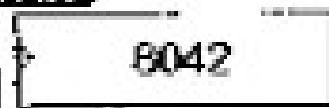
CACHE BANK 1



JP17

JP18

JP8



CACHE BANK 0

List for different jumpers' setting for
8.

64K	128K	256K
2,3	2,3	2,3
1,2	2,3	2,3
1,2	1,2	2,3
2,3	2,3	2,3
1,2	2,3	2,3
1,2	1,2	2,3
2,3	1,2	2,3
2,3	1,2	2,3
1,2	2,3	2,3

1-6
CPU
CONFIG-
URATION

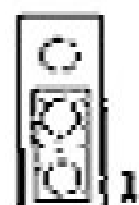
	486DX
JP4	1,2
JP5	SHORT
JP6	1,2

JP3

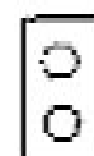
AT Bus Clock Selector

J2

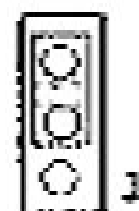
Turbo Sw



1,2 Short : for 486DX above 25MHz
OSC uses single frequency



Open



2,3 short : for 486SX 16/20 MHz
OSC use double frequency



{C1

{C1

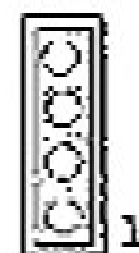
Short

J7

External Battery Connector

JP17

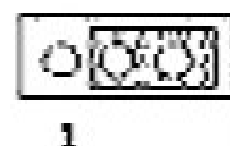
Battery D



1. +VDD (External battery power input)
4. GND



1,2 Short



2,3 Short

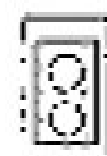
J3

Turbo LED

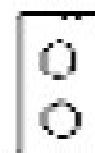
On : high speed
Off : low speed

J6

Mono/Ca



Open



Short

