

- |   |  |
|---|--|
| 1. i486SX PQFP CPU (optional)                 | 10. Video display mode selection jumper JP2                |
| 2. PGA CPU socket                             | 11. Battery selection jumper BJP1                          |
| 3. CPU selector jumpers, CP1, 2&3             | 12. <33MHz jumper JP1                                      |
| 4. Oscillator (some models)                   | 13. Expansion slots  |
| 5. Clock Speed jumpers CS1, 2&3 (some models) | 14. VL-Bus slots (some models)                             |
| 6. DRAM module sockets                        | 15. Case function connectors                               |
| 7. Power supply connector                     | 16. External cache(some models)                            |
| 8. Keyboard connector                         | 17. Tag RAM (some models)                                  |
| 9. On board battery (some models)             | 18. Cache size selection jumpers, CT1, 2 & 3 (some models) |

## ADDENDUM: JUMPER SETTING CHANGE

FOR VL/ISA-486CTV2 MAIN BOARD REV. 2.5 or ABOVE  
PLEASE UPDATE THIS ADDENDUM PAGE OVER PAGE 2-2

### Manufacturer's Jumpers

open	short		33MHz	50MHz
0 wait	1 wait	TP5	short	short 1D2
< 33MHz	> 33MHz	TP6	open	short 1D3
	■	TP7	open	short VESA MASTER
	↓	TP8	1-2	2-3
↑ [ ] ←	↓ [ ] ↓	TP9	short	open 2 VESA MASTER
	■	TP10	2-3	2-3
		TP15	2-3	2-3
		TP16	2-3	2-3
		TP17	open	open
		TP18	1-2	1-2
		TP19	open	open

JP3



**JP3: short**

**[Weitek Power 9000 VESA VGA card users:  
pull off this jumper cap and stick it on *one*  
of the two pins *before* you install the card.  
The card must be installed in the left-hand  
local bus slot. ]**

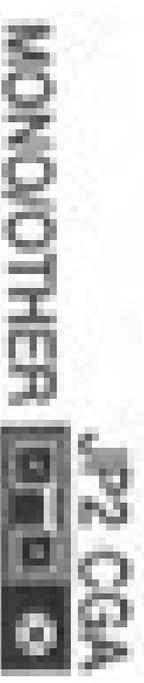
## Battery selection jumper BJP1 setting

Setting for external battery

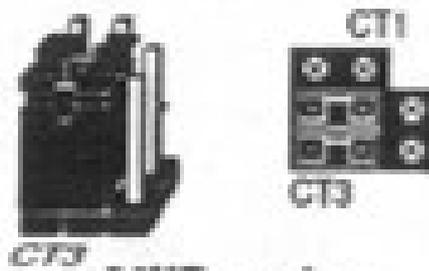


## Video jumper JP2 default setting

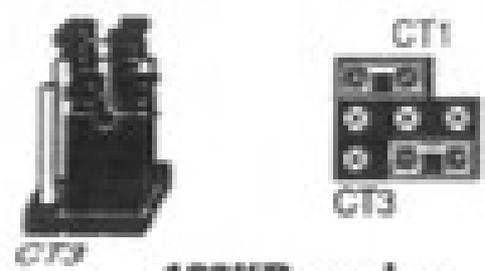
**Remember: The MONO/OTHER setting is for all color or monochrome display cards except CGA.**



## Cache size selection jumper settings



64KB cache

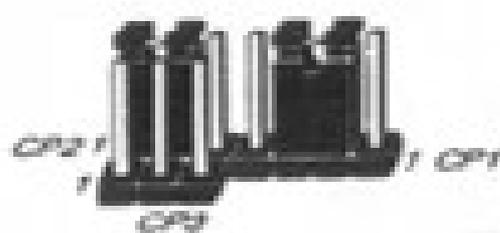


128KB cache



256KB cache

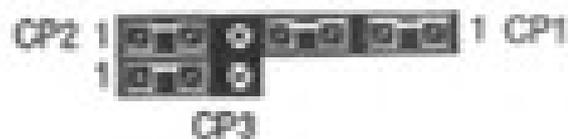
## PGA CPU type selection jumper settings for CP1, 2 & 3



486SX



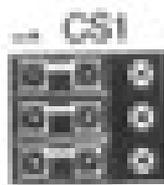
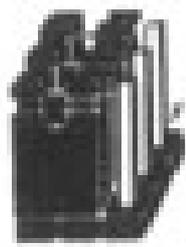
486DX/486DX2



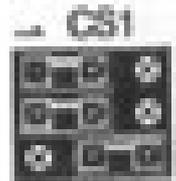
487SX/OverDrive  
POFD ONLY



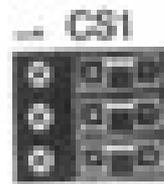
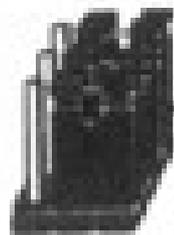
**Clock speed selection jumper settings CS1, 2 & 3 for those mainboards which use a clock generator.**



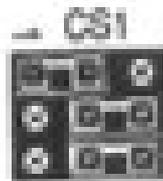
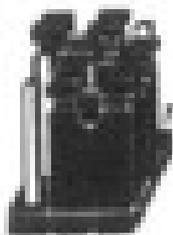
**20MHz**



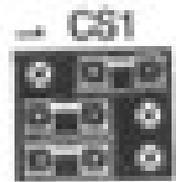
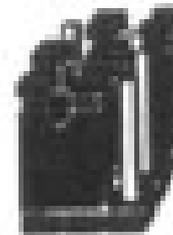
**25MHz**



**33MHz**

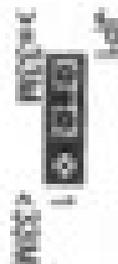


**40MHz**

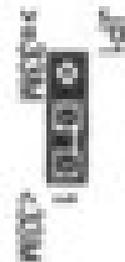


**50MHz**

### **Oscillator jumper JP1 setting**

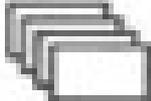
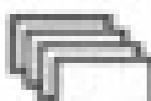
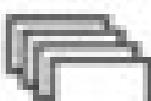
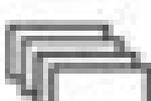
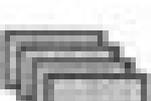


**External CPU speed  
33MHz or more  
short pins 2 & 3.**

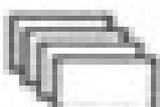


**All other CPU speeds  
short pins 1 & 2,**

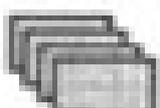
# VL/ISA-486CTV2 memory configuration options

Total Memory	Bank 0	Bank 1
1MB		
2MB		
5MB*		
4MB		
8MB		
20MB*		
16MB		
32MB		

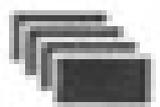
\* SIMM bank arrangement can be reversed



Four 256KB SIMMs = 1MB



Four 1MB SIMMs = 4MB

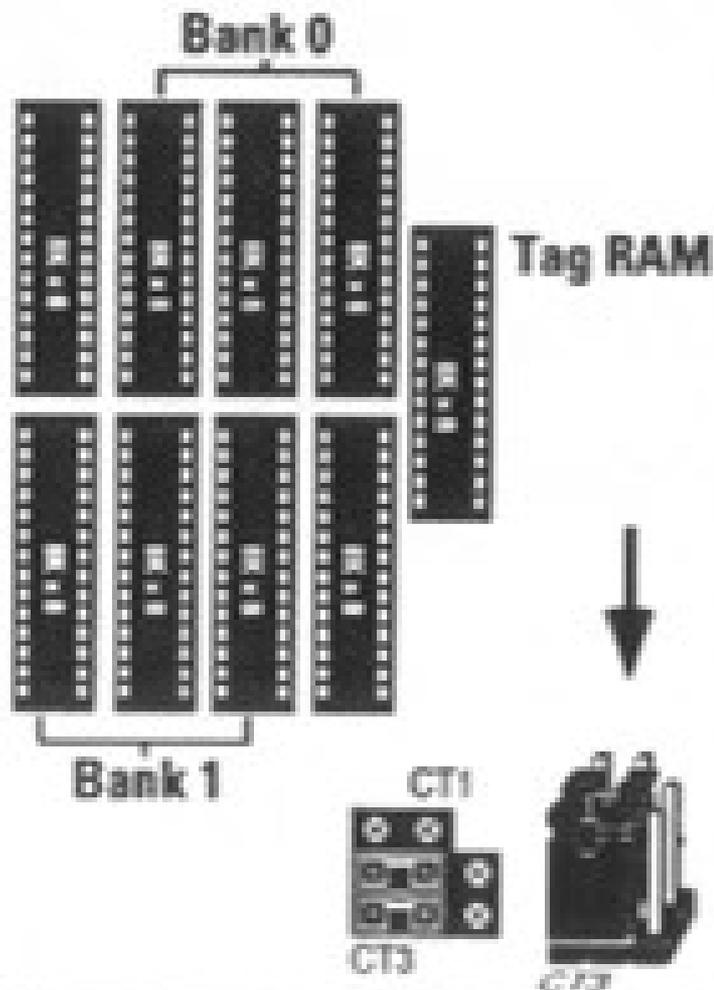


Four 4MB SIMMs = 16MB

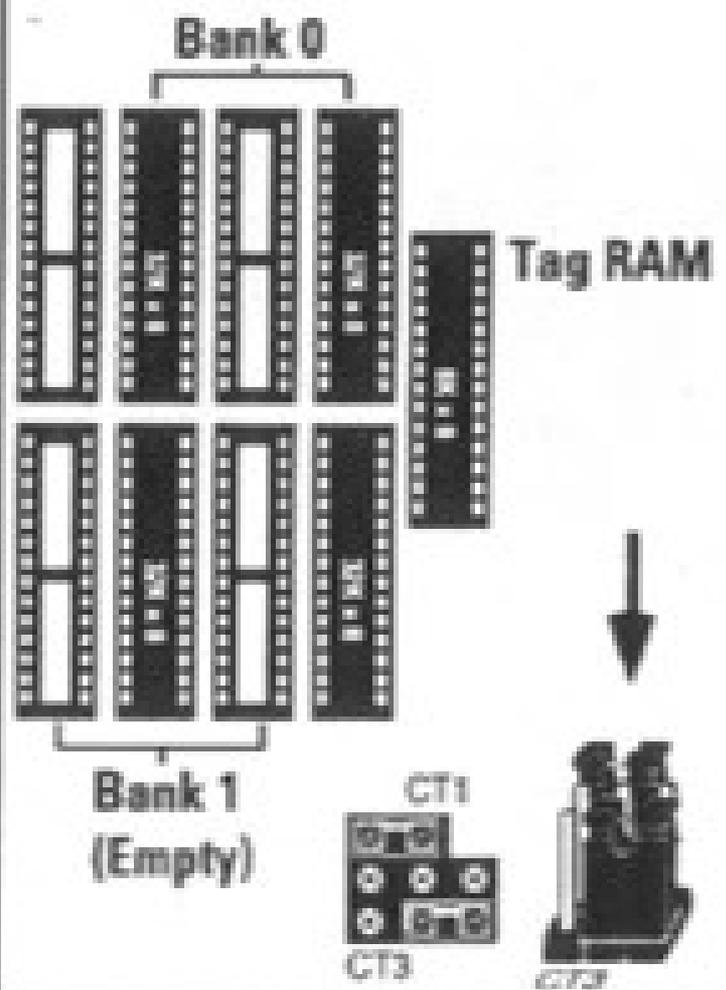
<b>Cache Size</b>	<b>Tag RAM</b>	<b>Data RAM</b>
<b>64KB</b>	<b>One 8Kx8 SRAM</b>	<b>Eight 8Kx8 SRAMs</b>
<b>128KB</b>	<b>One 8Kx8 SRAM</b>	<b>Four 32Kx8 SRAMs*</b>
<b>256KB</b>	<b>One 32Kx8 SRAM</b>	<b>Eight 32Kx8 SRAMs</b>

\* Four SRAMs by Bank 0

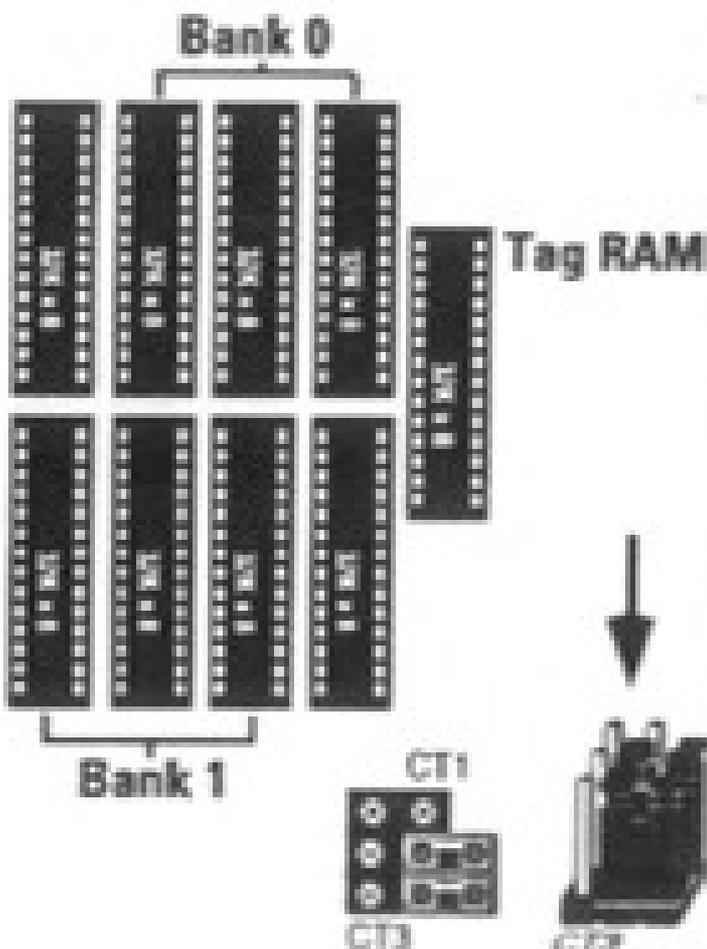
### 64KB cache



### 128KB cache



### 256KB cache



**SRAM speed requirements:**  
≤ 25ns chips for Data RAM  
≤ 25ns chips for Tag RAM or  
if the CPU is a 486DX-50, use  
a 20ns chip for Tag RAM

↓  
= Required jumper  
positions for each  
cache option