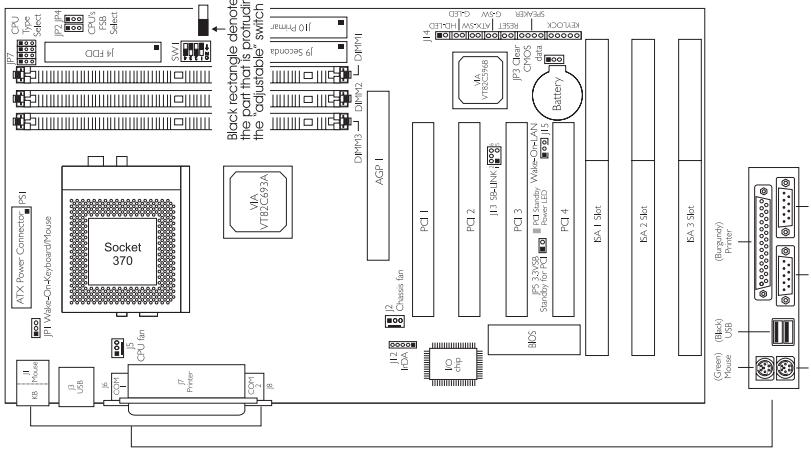
CA61

rev. A+



Processor			Freq. Ratio	SW1
66MHz	100MHz	133MHz		
--------	Future processor	Future processor	3x	
--------	Future processor	Future processor	3.5x	
--------	Future processor	Future processor	4x	
300MHz (300A)	Future processor	Future processor	4.5x	
333MHz	Future processor	Future processor	5x	
366MHz	Future processor	Future processor	5.5x	
400MHz	Future processor	Future processor	6x	
433MHz	Future processor	Future processor	6.5x	
466MHz	Future processor	Future processor	7x	
500MHz	Future processor	Future processor	7.5x	
533MHz	Future processor	Future processor	8x	

JP5 (3.3VSB Standby for PCI)
On: Provides 3.3VSB standby power to the PCI slots. (default)
Off: For PCI modem cards that does not comply to PCI 2.2 specification.

PCI Standby Power LED will light when the system is in the power-on, Soft-Off or Suspend mode.

JP2 & JP4 (CPU's FSB Select)

For Intel CPUs

	Auto*	66MHz	100MHz	133MHz
JP2	1-2 On	2-3 On	All off	1-2 On
JP4	1-2 On	2-3 On	1-2 On	All off

"*" denotes default setting

For Cyrix CPUs

	66MHz	100MHz	133MHz
JP2	2-3 On	1-2 On	1-2 On
JP4	2-3 On	2-3 On	1-2 On

JP7 (CPU Type Select)

Intel CPUs (default)

Cyrix CPUs

JP1 (Wake-On-Keyboard/Mouse) : 2-3 On: Enable
1-2 On: Disable (default)

By default, JP1 is disabled. Make sure "Keyboard/Mouse Power On" in the Integrated Peripherals setup of the Award BIOS is also disabled. If JP1 was previously enabled with a password set in the "KB Power On Password" field, and now you wish to disable the password(Wake-On-Keyboard) function, make sure to disable the password in this field prior to setting JP1 to disabled. You will not be able to boot up the system if you fail to do so.

JP3 (Clear CMOS Data) : 1-2 On: Normal (default)
2-3 On: Clear CMOS Data

Before clearing the CMOS data, make sure to power-off your system and unplug the power cord.

Note
- The voltage regulator will automatically be set according to the voltage of the processor.
- You can not overclock some processors because their frequency ration has been fixed by the manufacturer.