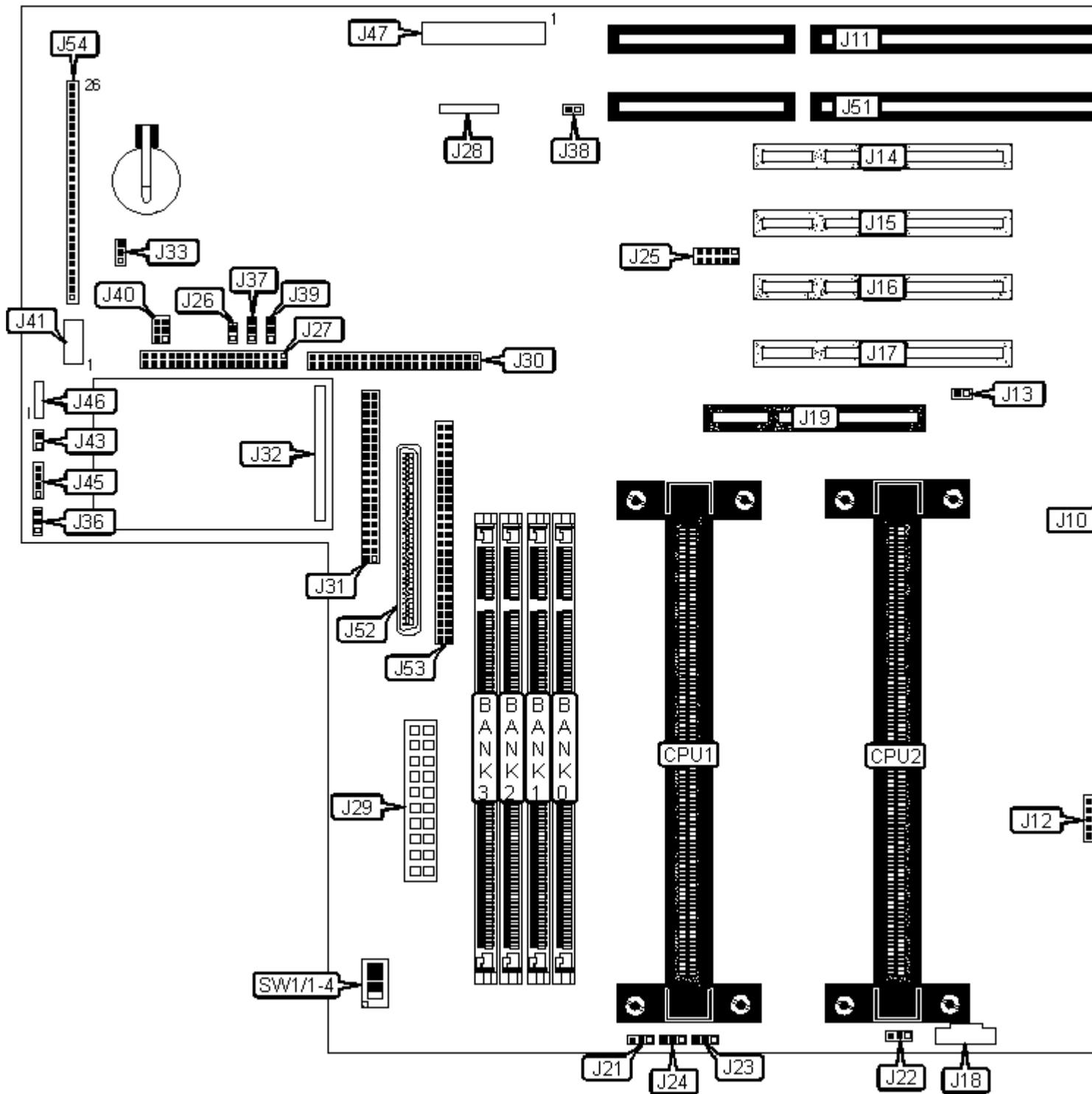


MOTOROLA, INC.

ATXP11 PLUS

Device Type	Mainboard
Processor	Pentium II (2)
Processor Speed	233/266/300/333MHz
Chip Set	Intel 440LX
Audio Chip Set	Unidentified
Maximum Onboard Memory	512MB (EDO & SDRAM supported)
Maximum Audio Memory	Unidentified
Cache	256/512KB (located on the CPU)
BIOS	Unidentified
Dimensions	327mm x 305mm
I/O Options	16-bit ISA slots (2), 32-bit PCI slots (4), AGP slot, ATX power connector, audio in - CD ROM, floppy drive interface, IDE interfaces (2), IR connector, parallel port, line in (2), line out, microphone in, PS/2 keyboard port, PS/2 mouse port, RJ-45 Ethernet connector, SanDisk IDE FlashDrive connector, SCSI interfaces (2), serial interface, serial ports (2), USB interface, USB ports (2), Watchdog timer interface



CONNECTIONS

Purpose	Location	Purpose	Location
Line in	J1	Power supply fan power	J24
Microphone in	J2	Floppy drive interface	J27
Line out	J3	Watchdog timer programming interface	J28

Audio in - CD ROM	J4	ATX power connector	J29
PS/2 keyboard port	J5A	IDE interface 1	J30
PS/2 mouse port	J5	IDE interface 2	J31
Serial port 2	J6	SanDisk IDE FlashDrive connector	J32
Serial port 1	J6A	Chassis intrusion connector	J36
USB port 2	J7	IR connector	J41
USB port 1	J7A	Reset connector	J43
RJ-45 Ethernet connector	J8	USB interface	J46
Parallel port	J9	ROM emulator board interface (reserved)	J47
Line in interface	J10	16-bit ISA slot 2	J51
16-bit ISA slot 1	J11	Wide SCSI interface	J52
Serial interface	J12	Narrow SCSI interface	J53
Ethernet LED connector	J13	Green PC LED	J54/Pins 1 - 3
32-bit PCI slots	J14 - J17	Power switch	J54/Pins 8 & 9
Intel test port (reserved)	J18	IDE interface LED	J54/Pins 12 - 15
AGP slot	J19	Power LED	J54/Pins 17 & 19
System fan power	J21	Keylock	J54/Pins 21 & 22
CPU2 fan power	J22	External speaker	J54/Pins 23 & 26
CPU1 fan power	J23		

USER CONFIGURABLE SETTINGS

Function		Label	Position
»	USB port 2 (J7) selected	J25	Pins 1 & 3, 2 & 4, 9 & 10 closed
	USB interface (J46) selected	J25	Pins 3 & 5, 4 & 6, 7 & 8 closed
»	Watchdog timer disabled	J26	Open
	Watchdog timer enabled	J26	Closed
»	CMOS memory normal operation	J37	Pins 2 & 3 closed
	CMOS memory clear	J37	Pins 1 & 2 closed

»	Speaker output is alarm beeps	J40	Pins 3 & 5, 4 & 6 closed
	Speaker output is mono audio out	J40	Pins 1 & 3, 2 & 4 closed
»	Onboard battery selected	J45	Pins 1 & 2 closed
	External battery selected	J45	Open
»	Onboard speaker selected	J54	Pins 25 & 26 closed
	External speaker selected	J54	Pins 25 & 26 open

DIMM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2	Bank 3
8MB	(1) 1M x 64	None	None	None
16MB	(1) 1M x 64	(1) 1M x 64	None	None
16MB	(1) 2M x 64	None	None	None
24MB	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64	None
32MB	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64
32MB	(1) 2M x 64	(1) 2M x 64	None	None
32MB	(1) 4M x 64	None	None	None
48MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64	None
64MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64
64MB	(1) 4M x 64	(1) 4M x 64	None	None
64MB	(1) 8M x 64	None	None	None
80MB	(1) 4M x 64	(1) 4M x 64	(1) 1M x 64	(1) 1M x 64
96MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64	None
96MB	(1) 4M x 64	(1) 4M x 64	(1) 2M x 64	(1) 2M x 64
128MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64
128MB	(1) 8M x 64	(1) 8M x 64	None	None
128MB	(1) 16M x 64	None	None	None
144MB	(1) 8M x 64	(1) 8M x 64	(1) 1M x 64	(1) 1M x 64
160MB	(1) 8M x 64	(1) 8M x 64	(1) 2M x 64	(1) 2M x 64

192MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64	None
192MB	(1) 8M x 64	(1) 8M x 64	(1) 4M x 64	(1) 4M x 64
256MB	(1) 8M x 64			
256MB	(1) 16M x 64	(1) 16M x 64	None	None
256MB	(1) 32M x 64	None	None	None
272MB	(1) 16M x 64	(1) 16M x 64	(1) 1M x 64	(1) 1M x 64
288MB	(1) 16M x 64	(1) 16M x 64	(1) 2M x 64	(1) 2M x 64
320MB	(1) 16M x 64	(1) 16M x 64	(1) 4M x 64	(1) 4M x 64
384MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64	None
384MB	(1) 16M x 64	(1) 16M x 64	(1) 8M x 64	(1) 8M x 64
512MB	(1) 16M x 64			

Note: Board supports EDO & SDRAM memory.

CACHE CONFIGURATION

Note: 256/512KB cache is located on the Pentium II CPU.

GREEN LED/POWER LED SELECTION

LEDs		J33
»	Common cathode	Pins 2 & 3 closed
	Common anode	Pins 1 & 2 closed
	Dual function	Open

FLASH BOOT BLOCK SELECTION

Setting		J38	J39
»	Write to boot block disabled	Closed	Pins 1 & 2 closed
	Write to boot block enabled	Open	Open

Note: J38 write protects the boot block. J39 informs BIOS whether boot block can be written to.

CPU SPEED SELECTION

CPU Speed		Multiplier	SW1/1	SW1/2	SW1/3	SW1/4
	233MHz	3.5x	Off	Off	On	On
	266MHz	4.0x	On	On	Off	On
»	300MHz	4.5x	Off	On	Off	On
	333MHz	5.0x	On	Off	Off	On

MISCELLANEOUS TECHNICAL NOTES

When only 1 processor is used, a termination card should be installed into the CPU2 slot.