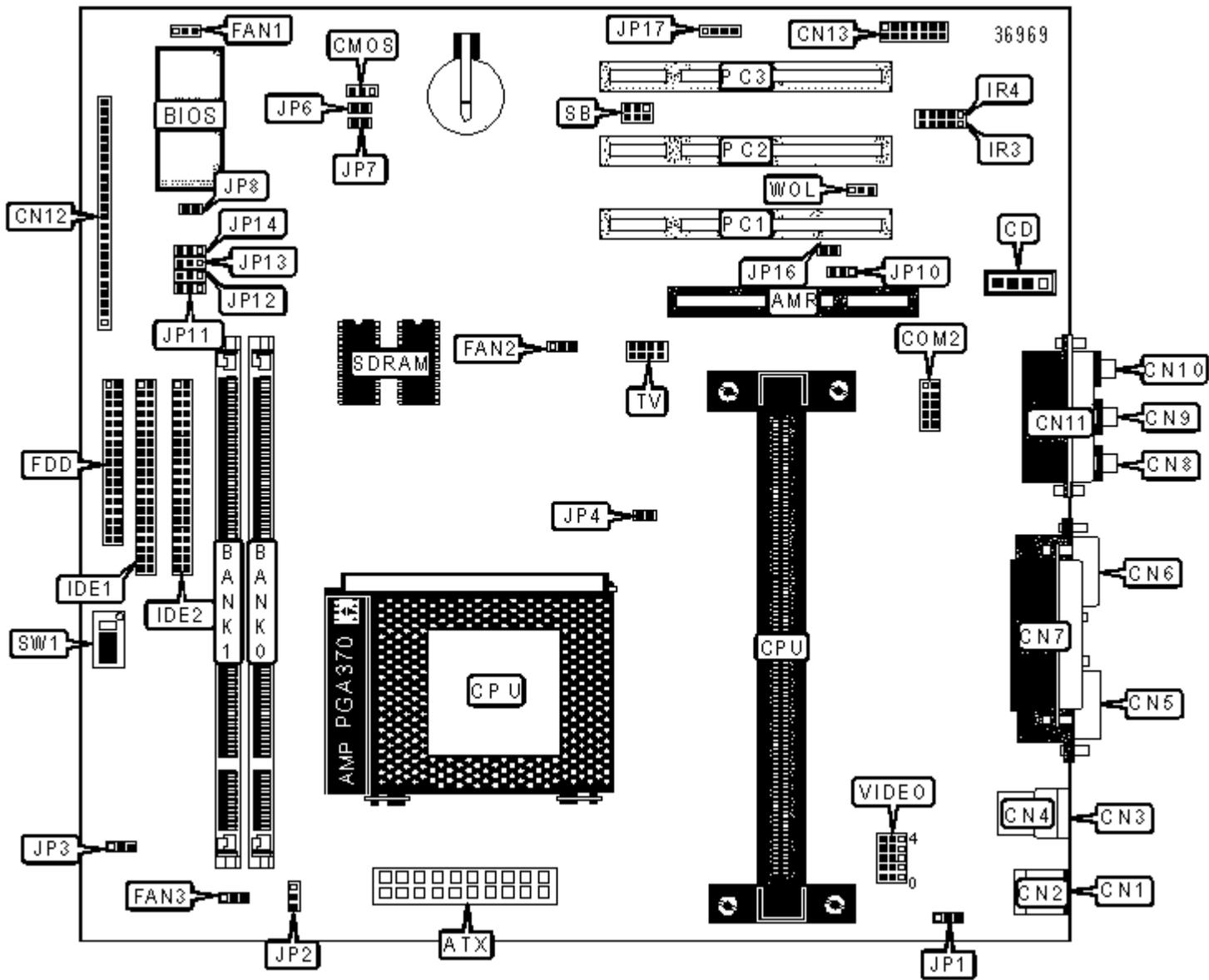


BOARDRUNNER

6P2M810

Device Type	Mainboard
Processor	Celeron
Processor Speed	300/333/366/400/433/466MHz
Chip Set	Intel
Video Chip Set	Unidentified
Audio Chip Set	Unidentified
Maximum Onboard Memory	512MB (SDRAM supported)
Maximum Video Memory	Unidentified
Maximum Audio Memory	Unidentified
Cache	0/128KB (located on the Celeron CPU)
BIOS	Award
Dimensions	244mm x 220mm
I/O Options (backplane)	32-bit PCI slots (3), floppy drive interface, game/MIDI port, IDE interfaces (2), parallel port, PS/2 mouse port, PS/2 keyboard port, serial port, serial interface, VGA port, IR connector, USB connectors (2), ATX power connector, Audio/Modem Riser slot, line in, line out, microphone in, audio in - CD-ROM, SB-Link connector, TV-Out connector, Wake-on-LAN connector



CONNECTIONS

Purpose	Location	Purpose	Location
Audio/Modem Riser slot	AMR	IDE interface LED	CN12/Pins 15 & 16
ATX power connector	ATX	Turbo LED	CN12/Pins 18 & 19
Audio in - CD-ROM	CD	Power switch	CN12/Pins 21 & 22
PS/2 mouse port	CN1	Unidentified	CN13
PS/2 keyboard port	CN2	Serial interface	COM2
USB port 1	CN3	Fan power 1	FAN1
USB port 2	CN4	Fan power 2	FAN2

Serial port	CN5	Fan power 3	FAN3
VGA port	CN6	Floppy drive interface	FDD
Parallel port	CN7	IDE interface 1	IDE1
Line out	CN8	IDE interface 2	IDE2
Line in	CN9	IR connector 1	IR3
Microphone in	CN10	IR connector 2	IR4
Game/MIDI port	CN11	32-bit PCI slots	PC1 - PC3
Power LED & keylock	CN12/Pins 1 - 5	SB-Link connector	SB
Speaker	CN12/Pins 7 - 10	TV-Out connector	TV
Reset switch	CN12/Pins 12 & 13	Wake-on-LAN connector	WOL

USER CONFIGURABLE SETTINGS

Function		Label	Position
»	CMOS memory normal operation	CMOS	Pins 1 & 2 closed
	CMOS memory clear	CMOS	Pins 2 & 3 closed
»	Factory configured - do not alter	JP1	Unidentified
	BSEL at 100MHz	JP2	Pins 1 & 2 closed
	BSEL at 133MHz	JP2	Pins 2 & 3 closed
	Vcore at 3v	JP3	Closed
	Vcore at 2v	JP3	Open
»	On board AC-97 CODEC enabled	JP10	Pins 2 & 3 closed
	On board AC-97 CODEC disabled	JP10	Pins 1 & 2 closed

DIMM CONFIGURATION

Size	Bank 0	Bank 1
8MB	(1) 1M x 64	None
16MB	(1) 1M x 64	(1) 1M x 64
16MB	(1) 2M x 64	None

24MB	(1) 2M x 64	(1) 1M x 64
32MB	(1) 2M x 64	(1) 2M x 64
32MB	(1) 4M x 64	None
40MB	(1) 4M x 64	(1) 1M x 64
48MB	(1) 4M x 64	(1) 2M x 64
64MB	(1) 4M x 64	(1) 4M x 64
64MB	(1) 8M x 64	None
72MB	(1) 8M x 64	(1) 1M x 64
80MB	(1) 8M x 64	(1) 2M x 64
96MB	(1) 8M x 64	(1) 4M x 64
128MB	(1) 8M x 64	(1) 8M x 64
128MB	(1) 16M x 64	None
136MB	(1) 16M x 64	(1) 1M x 64
144MB	(1) 16M x 64	(1) 2M x 64
160MB	(1) 16M x 64	(1) 4M x 64
192MB	(1) 16M x 64	(1) 8M x 64
256MB	(1) 16M x 64	(1) 16M x 64
256MB	(1) 32M x 64	None
264MB	(1) 32M x 64	(1) 1M x 64
272MB	(1) 32M x 64	(1) 2M x 64
288MB	(1) 32M x 64	(1) 4M x 64
320MB	(1) 32M x 64	(1) 8M x 64
384MB	(1) 32M x 64	(1) 16M x 64
512MB	(1) 32M x 64	(1) 32M x 64
Note: Board supports SDRAM memory.		

CACHE CONFIGURATION

Note: 128KB cache is located on the Celeron 300A and greater CPUs.

CPU FREQUENCY SELECTION

Speed	SW1/1	SW1/2	SW1/3	SW1/4
66MHz	On	On	Off	Off
75MHz	Off	Off	On	On
83MHz	Off	Off	On	Off
90MHz	Off	Off	Off	On
95MHz	Off	Off	Off	Off
100MHz	On	On	On	On

CPU VOLTAGE SELECTION

Voltage	VIDEO0	VIDEO1	VIDEO2	VIDEO3	VIDEO4
1.3v	Closed	Closed	Closed	Closed	Open
1.35v	Open	Closed	Closed	Closed	Open
1.4v	Closed	Open	Closed	Closed	Open
1.45v	Open	Open	Closed	Closed	Open
1.5v	Closed	Closed	Open	Closed	Open
1.55v	Open	Closed	Open	Closed	Open
1.6v	Closed	Open	Open	Closed	Open
1.65v	Open	Open	Open	Closed	Open
1.7v	Closed	Closed	Closed	Open	Open
1.75v	Open	Closed	Closed	Open	Open
1.8v	Closed	Open	Closed	Open	Open
1.85v	Open	Open	Closed	Open	Open
1.9v	Closed	Closed	Open	Open	Open
1.95v	Open	Closed	Open	Open	Open
2v	Closed	Open	Open	Open	Open
2.05	Open	Open	Open	Open	Open
2v	Closed	Closed	Closed	Closed	Closed
2.1v	Open	Closed	Closed	Closed	Closed

2.2v	Closed	Open	Closed	Closed	Closed
2.3v	Open	Open	Closed	Closed	Closed
2.4v	Closed	Closed	Open	Closed	Closed
2.5v	Open	Closed	Open	Closed	Closed
2.6v	Closed	Open	Open	Closed	Closed
2.7v	Open	Open	Open	Closed	Closed
2.8v	Closed	Closed	Closed	Open	Closed
2.9v	Open	Closed	Closed	Open	Closed
3v	Closed	Open	Closed	Open	Closed
3.1v	Open	Open	Closed	Open	Closed
3.2v	Closed	Closed	Open	Open	Closed
3.3v	Open	Closed	Open	Open	Closed
3.4v	Closed	Open	Open	Open	Closed
3.5	Open	Open	Open	Open	Closed