# BOARDRUNNER

6M810

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|---|---|
| **Device Type** | Mainboard |
| **Processor** | Celeron |
| **Processor Speed** | 266/300/333/366/400/433/466/500MHz |
| **Chip Set** | Intel |
| **Video Chip Set** | Intel |
| **Audio Chip Set** | Unidentified |
| **Maximum Onboard Memory** | 512MB (SDRAM supported) |
| **Maximum Video Memory** | Unidentified |
| **Maximum Audio Memory** | Unidentified |
| **Cache** | 0/128KB (located on the Celeron CPU) |
| **BIOS** | Award |
| **Dimensions** | 244mm x 170mm |
| **I/O Options** | 32-bit PCI slots (3), floppy drive interface, game/MIDI port, IDE interfaces (2), parallel port, PS/2 mouse port, PS/2 keyboard port, serial interface, serial port, VGA port, IR connector, USB ports (2), UTP slot, ATX power connector, Audio/Modem Riser slot, line in, line out, microphone in, audio in - CD-ROM, audio in - Auxilliary, audio in - TAD, Wake-on-LAN connector |
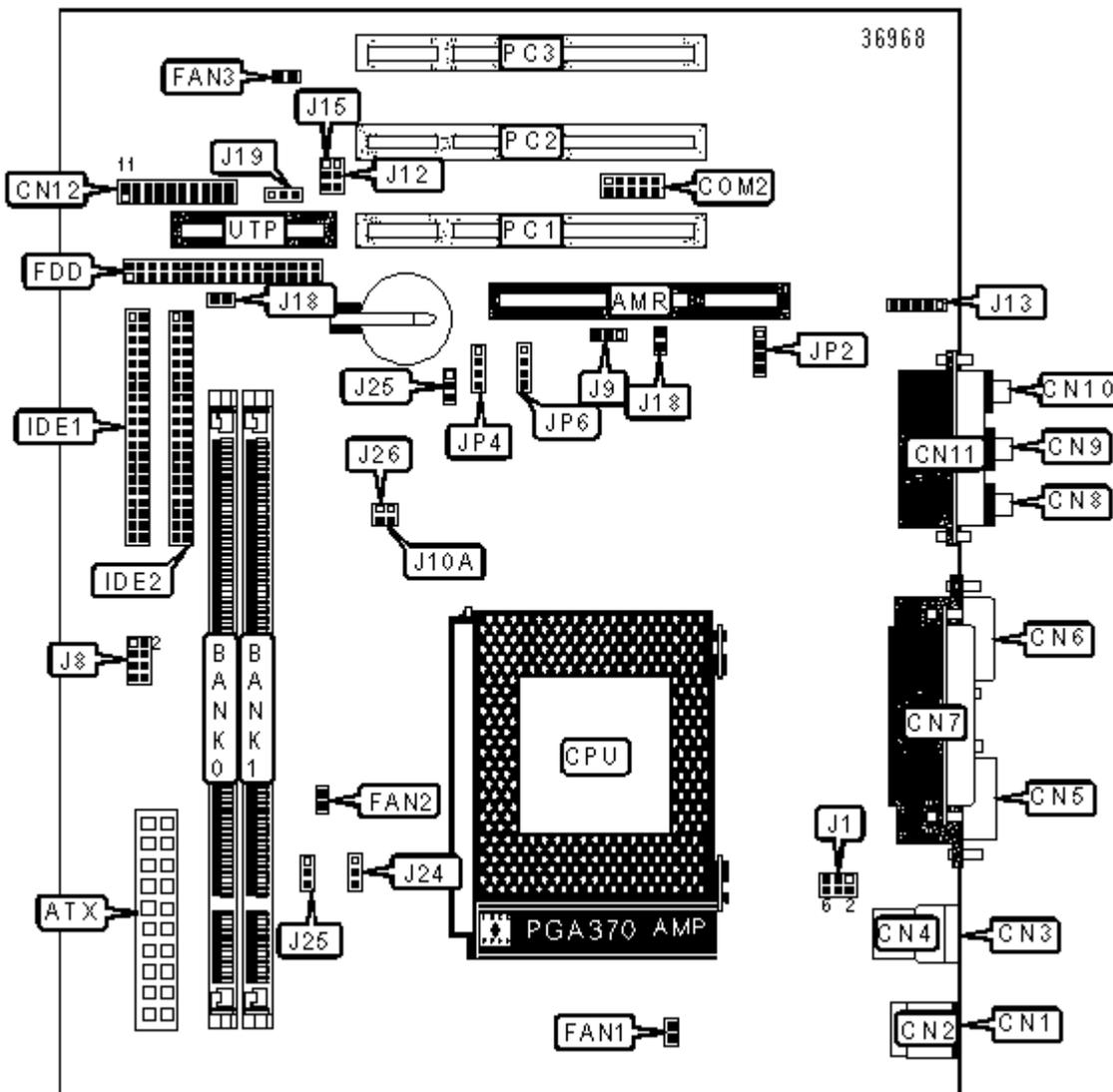
## CONNECTIONS

| Purpose | Location | Purpose | Location |
|---|---|---|---|
| Audio/Modem Riser slot | AMR | Green PC LED | CN12/Pins 7 & 8 |
| ATX power connector | ATX | Power switch | CN12/Pins 9 & 10 |
| PS/2 mouse port | CN1 | Power LED | CN12/Pins 11 - 13 |
| PS/2 keyboard port | CN2 | Speaker | CN12/Pins 17 - 20 |
| USB port 1 | CN3 | CPU fan power | FAN1 |
| USB port 2 | CN4 | Chassis fan power | FAN2 |
| Serial port 1 | CN5 | AMR fan power | FAN3 |
| VGA port | CN6 | Floppy drive interface | FDD |
| Parallel port | CN7 | IDE interface 1 | IDE1 |
| Line in | CN8 | IDE interface 2 | IDE2 |
| Line out | CN9 | IR connector | J13 |
| Microphone in | CN10 | Wake-on-LAN connector | J19 |
| Game/MIDI port | CN11 | Audio in - Auilliary | JP2 |
| Serial port 2 | COM2 | Audio in - TAD | JP4 |
| Reset switch | CN12/Pins 1 & 2 | Audio in - CD-ROM | JP6 |
| IDE interface LED | CN12/Pins 3 & 4 | 32-bit PCI slots | PC1 - PC3 |
| EPMI interface | CN12/Pins 5 & 6 | USB/PanelLink/TV-Out slot | UTP |

## USER CONFIGURABLE SETTINGS

| | Function | Label | Position |
|---|---|---|---|
| | AMR secondary slot setting | J9 | Pins 1 & 2 closed |
| | AMR primary slot setting | J9 | Pins 2 & 3 closed |
| » | Normal bus operation for 66MHz | J10A | Closed |
| | Overclocking bus operation for 66MHz CPU (100MHz) | J10A | Open |
| » | CMOS memory normal operation | J12 | Pins 1 & 2 closed |

| | | | |
|---|---|---|---|
| | CMOS memory clear | J12 | Pins 2 & 3 closed |
| » | Password normal operation | J15 | Pins 1 & 2 closed |
| | Password clear | J15 | Pins 2 & 3 closed |
| » | Factory configured - do not alter | J16 | Open |
| » | Factory configured - do not alter | J18 | Open |
| | Suspend to RAM disabled | J23 | Pins 1 & 2 closed |
| | Suspend to RAM enabled | J23 | Pins 2 & 3 closed |
| | CPU voltage increase by 2% | J24 | Pins 1 & 2 closed |
| | CPU voltage increase by 5% | J24 | Pins 2 & 3 closed |
| » | ICH volatage setting at 3.3v | J25 | Pins 1 & 2 closed |
| | ICH volatage setting at 3.9v | J25 | Pins 2 & 3 closed |
| » | Normal bus operation for 100MHz | J26 | Closed |
| | Overclocking bus operation for 100MHz CPU (133MHz) | J26 | Open |

| DIMM CONFIGURATION | | |
|---|---|---|
| **Size** | **Bank 0** | **Bank 1** |
| 8MB | (1) 1M x 64 | None |
| 16MB | (1) 1M x 64 | (1) 1M x 64 |
| 16MB | (1) 2M x 64 | None |
| 24MB | (1) 2M x 64 | (1) 1M x 64 |
| 32MB | (1) 2M x 64 | (1) 2M x 64 |
| 32MB | (1) 4M x 64 | None |
| 40MB | (1) 4M x 64 | (1) 1M x 64 |
| 48MB | (1) 4M x 64 | (1) 2M x 64 |
| 64MB | (1) 4M x 64 | (1) 4M x 64 |
| 64MB | (1) 8M x 64 | None |
| 72MB | (1) 8M x 64 | (1) 1M x 64 |
| 80MB | (1) 8M x 64 | (1) 2M x 64 |

| | | |
|---|---|---|
| 96MB | (1) 8M x 64 | (1) 4M x 64 |
| 128MB | (1) 8M x 64 | (1) 8M x 64 |
| 128MB | (1) 16M x 64 | None |
| 136MB | (1) 16M x 64 | (1) 1M x 64 |
| 144MB | (1) 16M x 64 | (1) 2M x 64 |
| 160MB | (1) 16M x 64 | (1) 4M x 64 |
| 192MB | (1) 16M x 64 | (1) 8M x 64 |
| 256MB | (1) 16M x 64 | (1) 16M x 64 |
| 256MB | (1) 32M x 64 | None |
| 264MB | (1) 32M x 64 | (1) 1M x 64 |
| 272MB | (1) 32M x 64 | (1) 2M x 64 |
| 288MB | (1) 32M x 64 | (1) 4M x 64 |
| 320MB | (1) 32M x 64 | (1) 8M x 64 |
| 384MB | (1) 32M x 64 | (1) 16M x 64 |
| 512MB | (1) 32M x 64 | (1) 32M x 64 |

Note:   Board supports SDRAM memory.

## CACHE CONFIGURATION

Note:  128KB cache is located on the Celeron 300A and greater CPUs.

## CPU SPEED SELECTION

| | Speed | J8/Pins 1 & 2 | J8/Pins 3 & 4 | J8/Pins 5 & 6 | J8/Pins 7 & 8 |
|---|---|---|---|---|---|
| » | 66MHz | Open | Open | Open | Open |
| | 70MHz | Closed | Open | Open | Open |
| | 75MHz | Closed | Closed | Closed | Closed |
| | 83MHz | Closed | Closed | Closed | Open |
| | 90MHz | Closed | Closed | Open | Closed |
| | 95MHz | Closed | Closed | Open | Open |

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|---|---|---|---|---|---|
| | 100MHz | Open | Open | Closed | Open |
| | 105MHz | Open | Open | Open | Closed |
| | 114MHz | Closed | Open | Open | Closed |
| | 124MHz | Open | Closed | Closed | Open |
| | 133MHz | Open | Closed | Open | Closed |

| POWER-ON SELECTION | | |
|---|---|---|
| **Speed** | | **J1** |
| | Mouse/Keyboard power-on | Pins 1 & 3, 2 & 4 closed |
| » | Keyboard power-on | Pins 2 & 4, 3 & 5 closed |
| | Mouse power-on | Pins 1 & 3, 4 & 6 closed |