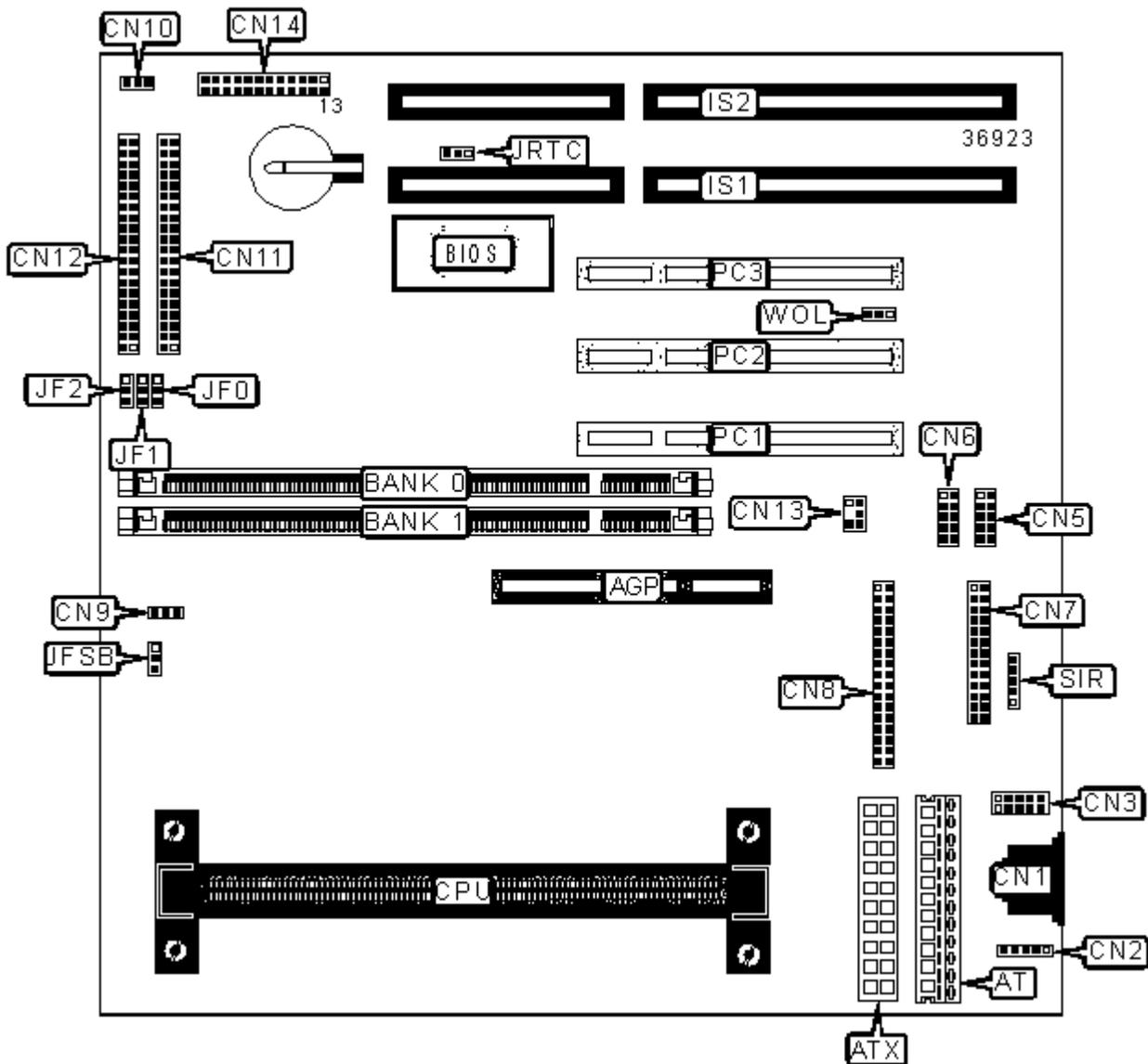


ACER, INC.

P2ZXB

| | |
|-------------------------------|---|
| Device Type | Mainboard |
| Processor | Celeron/Pentium II |
| Processor Speed | 233/266/300/333/350/366/400/450/500MHz |
| Chip Set | Intel 440ZX |
| Maximum Onboard Memory | 512MB (EDO & SDRAM supported) |
| Cache | 0/128/256/512KB (located on the CPU) |
| BIOS | Award |
| Dimensions | 220mm x 210mm |
| I/O Options | 16-bit ISA slots (2), 32-bit PCI slots (3), floppy drive interface, green PC connector, IDE interfaces (2), parallel interface, PS/2 mouse interface, AT keyboard port, serial interfaces (2), USB interface, ATX power connector, AT power connector, AGP slot, Wake-on-LAN connector, SB-Link connector, IR connector |



| CONNECTIONS | | | |
|-------------|----------|---------|----------|
| Purpose | Location | Purpose | Location |
| | | | |

| | | | |
|------------------------|------|-----------------------|-------------------|
| AGP slot | AGP | IDE interface 2 | CN12 |
| AT power connector | AT | SB-Link connector | CN13 |
| ATX power connector | ATX | Green PC LED | CN14/Pins 1 & 2 |
| AT keyboard port | CN1 | Reset switch | CN14/Pins 3 & 4 |
| PS/2 mouse interface | CN2 | IDE interface LED | CN14/Pins 6 & 7 |
| USB interface | CN3 | Power switch | CN14/Pins 9 & 10 |
| Serial interface 2 | CN5 | Green PC connector | CN14/Pins 11 & 12 |
| Serial interface 1 | CN6 | Power LED & keylock | CN14/Pins 14-18 |
| Parallel interface | CN7 | Speaker | CN14/Pins 20-23 |
| Floppy drive interface | CN8 | 16-bit ISA slots | IS1-IS2 |
| CPU fan power | CN9 | 32-bit PCI slots | PC1 - PC3 |
| Chassis fan power | CN10 | IR connector | SIR |
| IDE interface 1 | CN11 | Wake-on-LAN connector | WOL |

USER CONFIGURABLE SETTINGS

| Function | | Label | Position |
|----------|------------------------------|-------|-------------------|
| » | CMOS memory normal operation | JRTC | Pins 2 & 3 closed |
| | CMOS memory clear | JRTC | Pins 1 & 2 closed |

DIMM CONFIGURATION

| Size | Bank 0 | Bank 1 |
|------|-------------|-------------|
| 8MB | (1) 1M x 64 | None |
| 16MB | (1) 1M x 64 | (1) 1M x 64 |
| 16MB | (1) 2M x 64 | None |
| 24MB | (1) 2M x 64 | (1) 1M x 64 |
| 32MB | (1) 2M x 64 | (1) 2M x 64 |
| 32MB | (1) 4M x 64 | None |
| 40MB | (1) 4M x 64 | (1) 1M x 64 |
| 48MB | (1) 4M x 64 | (1) 2M x 64 |

| | | |
|-------|--------------|--------------|
| 64MB | (1) 4M x 64 | (1) 4M x 64 |
| 64MB | (1) 8M x 64 | None |
| 72MB | (1) 8M x 64 | (1) 1M x 64 |
| 80MB | (1) 8M x 64 | (1) 2M x 64 |
| 96MB | (1) 8M x 64 | (1) 4M x 64 |
| 128MB | (1) 8M x 64 | (1) 8M x 64 |
| 128MB | (1) 16M x 64 | None |
| 136MB | (1) 16M x 64 | (1) 1M x 64 |
| 144MB | (1) 16M x 64 | (1) 2M x 64 |
| 160MB | (1) 16M x 64 | (1) 4M x 64 |
| 192MB | (1) 16M x 64 | (1) 8M x 64 |
| 256MB | (1) 16M x 64 | (1) 16M x 64 |
| 256MB | (1) 32M x 64 | None |
| 264MB | (1) 32M x 64 | (1) 1M x 64 |
| 272MB | (1) 32M x 64 | (1) 2M x 64 |
| 288MB | (1) 32M x 64 | (1) 4M x 64 |
| 320MB | (1) 32M x 64 | (1) 8M x 64 |
| 384MB | (1) 32M x 64 | (1) 16M x 64 |
| 512MB | (1) 32M x 64 | (1) 32M x 64 |

Note: Board supports EDO & SDRAM memory.

CACHE CONFIGURATION

Note: 256KB/512KB cache is located on the Pentium II CPUs. 128KB cache is located on the Celeron 300A and greater CPUs.

CPU SPEED SELECTION (CELERON)

| CPU speed | Clock speed | Multiplier | JF0 | JF1 | JF2 | JFSB |
|-----------|-------------|------------|-------|-------|-------|-------|
| 300MHz | 66MHz | 4.5x | 2 & 3 | 1 & 2 | 2 & 3 | 2 & 3 |
| 333MHz | 66MHz | 5x | 2 & 3 | 2 & 3 | 1 & 2 | 2 & 3 |

Note: Pins designated should be in the closed position.

Note: Clock speed may be set to auto-detect by leaving JFSB open.

CPU SPEED SELECTION (PENTIUM II)

| CPU speed | Clock speed | Multiplier | JF0 | JF1 | JF2 | JFSB |
|-----------|-------------|------------|-------|-------|-------|-------|
| 233MHz | 66MHz | 3.5x | 1 & 2 | 2 & 3 | 2 & 3 | 2 & 3 |
| 266MHz | 66MHz | 4x | 2 & 3 | 1 & 2 | 1 & 2 | 2 & 3 |
| 300MHz | 66MHz | 4.5x | 2 & 3 | 1 & 2 | 2 & 3 | 2 & 3 |
| 333MHz | 66MHz | 5x | 2 & 3 | 2 & 3 | 1 & 2 | 2 & 3 |
| 350MHz | 100MHz | 3.5x | 1 & 2 | 2 & 3 | 2 & 3 | 1 & 2 |
| 366MHz | 66MHz | 5.5x | 2 & 3 | 2 & 3 | 2 & 3 | 2 & 3 |
| 400MHz | 100MHz | 4x | 2 & 3 | 1 & 2 | 1 & 2 | 1 & 2 |
| 450MHz | 100MHz | 4.5x | 2 & 3 | 1 & 2 | 2 & 3 | 1 & 2 |

Note: Pins designated should be in the closed position.

Note: Clock speed may be set to auto-detect by leaving JFSB open.