FAMOUS TECHNOLOGY CO., LTD.

MP-5VIP3 (REV. 1.00)

Device Type	Mainboard
Processor	CX 6x86/CX 6x86L/CX 6x86MX/AM K5/AM K6/Pentium/Pentium MMX
Processor Speed	75/90/100/120/133/150/166/180/200/233/266/300MHz
Chip Set	VIA
Maximum Onboard Memory	384MB (EDO & SDRAM supported)
Cache	512KB/1MB
BIOS	Award
Dimensions	Unidentified
I/O Options	32-bit PCI slots (5), AGP slot, ATX power connector, floppy drive interface, IDE interfaces (2), IR connector, parallel port, PS/2 mouse port, serial ports (2), USB ports (2), Wake-on- LAN connector
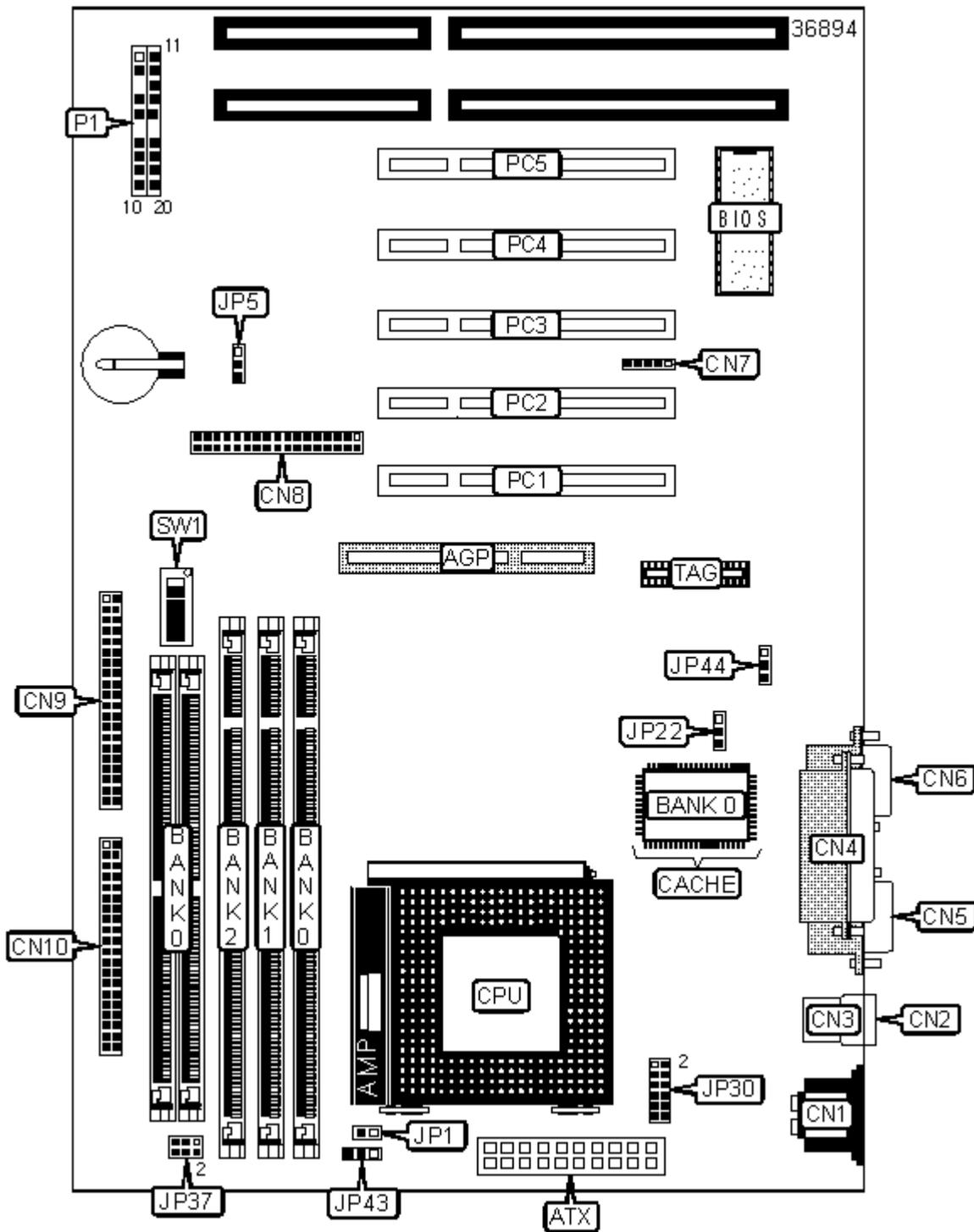
CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**
AGP slot	AGP	IDE interface 2	CN10
ATX power connector	ATX	CPU fan power	JP43
PS/2 mouse port	CN1	Wake-on-LAN connector	JP44
USB port 1	CN2	Reset switch	P1/Pins 1 & 2
USB port 2	CN3	Power switch	P1/Pins 4 & 5

Parallel port	CN4	Turbo LED	P1/Pins 7 & 8
Serial port 1	CN5	IDE interface LED	P1/Pins 9 & 10
Serial port 2	CN6	Power LED & keylock	P1/Pins 11 - 15
IR connector	CN7	Speaker	P1/Pins 17 - 20
Floppy drive interface	CN8	32-bit PCI slots	PC1 - PC5
IDE interface 1	CN9		

USER CONFIGURABLE SETTINGS			
Function		**Label**	**Position**
»	CPU voltage set automatically	JP1	Closed
	CPU voltage set manually	JP1	Open
»	CMOS memory normal operation	JP5	Pins 1 & 2 closed
	CMOS memory clear	JP5	Pins 2 & 3 closed
»	DIMM voltage is 3.3V	JP37	Pins 1 & 3, 2 & 4 closed
	DIMM voltage is 5V	JP37	Pins 3 & 5, 4 & 6 closed

SIMM CONFIGURATION	
Size	**Bank 0**
8MB	(2) 1M x 32
16MB	(2) 2M x 32
32MB	(2) 4M x 32
64MB	(2) 8M x 32
Note: SIMM Bank supports EDO memory. Note: SIMM Bank 0 and DIMM Bank 0 can not be used at the same time.	

DIMM CONFIGURATION			
Size	**Bank 0**	**Bank 1**	**Bank 2**
8MB	(1) 1M x 64	None	None
16MB	(1) 1M x 64	(1) 1M x 64	None

16MB	(1) 2M x 64	None	None
24MB	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64
32MB	(1) 2M x 64	(1) 2M x 64	None
32MB	(1) 4M x 64	None	None
32MB	(1) 2M x 64	(1) 1M x 64	(1) 1M x 64
48MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64
48MB	(1) 4M x 64	(1) 1M x 64	(1) 1M x 64
64MB	(1) 4M x 64	(1) 4M x 64	None
64MB	(1) 8M x 64	None	None
64MB	(1) 4M x 64	(1) 2M x 64	(1) 2M x 64
80MB	(1) 8M x 64	(1) 1M x 64	(1) 1M x 64
96MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64
96MB	(1) 8M x 64	(1) 2M x 64	(1) 2M x 64
128MB	(1) 8M x 64	(1) 8M x 64	None
128MB	(1) 16M x 64	None	None
128MB	(1) 8M x 64	(1) 4M x 64	(1) 4M x 64
144MB	(1) 16M x 64	(1) 1M x 64	(1) 1M x 64
160MB	(1) 16M x 64	(1) 2M x 64	(1) 2M x 64
192MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64
192MB	(1) 16M x 64	(1) 4M x 64	(1) 4M x 64
256MB	(1) 16M x 64	(1) 16M x 64	None
256MB	(1) 16M x 64	(1) 8M x 64	(1) 8M x 64
384MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64

Note: Board supports EDO & SDRAM memory.
Note: EDO & SDRAM can not be mixed.
Note: SIMM Bank 0 and DIMM Bank 0 can not be used at the same time.

CACHE CONFIGURATION		
Size	Cache RAM	TAG RAM
512KB	(1) 64K x 64	(1) 16K x 8

1MB	(2) 64K x 64	Not installed

CPU BURST MODE SELECTION			
	Burst Mode	**CPU**	**JP22**
»	Interleave CPU Burst mode	AM K5, AM K6 & Pentium	Pins 1 & 2 closed
	Linear CPU Burst mode	CX 6x86, CX 6x86L CX, 6x86MX	Pins 2 & 3 closed

CPU SPEED SELECTION (CX 6X86)								
CPU speed	**Clock speed**	**Multiplier**	**SW1/1**	**SW1/2**	**SW1/3**	**SW1/4**	**SW1/5**	**SW1/6**
150MHz	60MHz	2.0x	On	Off	Off	On	Off	Off
166MHz	66MHz	2.0x	On	Off	Off	Off	Off	Off
200MHz	75MHz	2.0x	On	Off	Off	Off	On	Off
Note: Pins designated should be in the closed position.								

CPU SPEED SELECTION (CX 6X86L)								
CPU speed	**Clock speed**	**Multiplier**	**SW1/1**	**SW1/2**	**SW1/3**	**SW1/4**	**SW1/5**	**SW1/6**
150MHz	60MHz	2.0x	On	Off	Off	On	Off	Off
166MHz	66MHz	2.0x	On	Off	Off	Off	Off	Off
200MHz	75MHz	2.0x	On	Off	Off	Off	On	Off
Note: Pins designated should be in the closed position.								

CPU SPEED SELECTION (CX 6X86MX)								
CPU speed	**Clock speed**	**Multiplier**	**SW1/1**	**SW1/2**	**SW1/3**	**SW1/4**	**SW1/5**	**SW1/6**
166MHz	60MHz	2.5x	On	On	Off	On	Off	Off
200MHz	66MHz	2.5x	On	On	Off	Off	Off	Off
233MHz	66MHz	3.0x	Off	On	Off	Off	Off	Off
233MHz	75MHz	2.5x	On	On	Off	Off	On	Off
266MHz	66MHz	3.5x	Off	Off	Off	Off	Off	Off
266MHz	75MHz	3.0x	Off	On	Off	Off	On	Off

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (AM K5)								
CPU speed	Clock speed	Multiplier	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6
75MHz	50MHz	1.5x	Off	Off	Off	On	On	On
90MHz	60MHz	1.5x	Off	Off	Off	On	Off	Off
100MHz	66MHz	1.5x	Off	Off	Off	Off	Off	Off
120MHz	60MHz	1.5x	Off	Off	Off	On	Off	Off
133MHz	66MHz	1.5x	Off	Off	Off	Off	Off	Off
166MHz	66MHz	2.5x	On	On	Off	Off	Off	Off

CPU SPEED SELECTION (AM K6)								
CPU speed	Clock speed	Multiplier	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6
166MHz	66MHz	2.5x	On	On	Off	Off	Off	Off
180MHz	60MHz	3.0x	Off	On	Off	On	Off	Off
200MHz	66MHz	3.0x	Off	On	Off	Off	Off	Off
233MHz	66MHz	3.5x	Off	Off	Off	Off	Off	Off
266MHz	66MHz	4.0x	On	Off	On	Off	Off	Off
300MHz	66MHz	4.5x	On	On	On	Off	Off	Off

CPU SPEED SELECTION (PENTIUM)									
	CPU speed	Clock speed	Multiplier	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6
	75MHz	50MHz	1.5x	Off	Off	Off	On	On	On
	90MHz	60MHz	1.5x	Off	Off	Off	On	Off	Off
	100MHz	66MHz	1.5x	Off	Off	Off	Off	Off	Off
	120MHz	60MHz	2.0x	On	Off	Off	On	Off	Off
»	133MHz	66MHz	2.0x	On	Off	Off	Off	Off	Off

	CPU speed	Clock speed	Multiplier						
	150MHz	60MHz	2.5x	On	On	Off	On	Off	Off
	166MHz	66MHz	2.5x	On	On	Off	Off	Off	Off
	180MHz	60MHz	3.0x	Off	On	Off	On	Off	Off
	200MHz	66MHz	3.0x	Off	On	Off	Off	Off	Off

CPU SPEED SELECTION (PENTIUM MMX)								
CPU speed	Clock speed	Multiplier	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6
166MHz	66MHz	2.5x	On	On	Off	Off	Off	Off
180MHz	60MHz	3.0x	Off	On	Off	Off	Off	Off
200MHz	66MHz	3.0x	Off	On	Off	Off	Off	Off
233MHz	66MHz	3.5x	Off	Off	Off	Off	Off	Off

VCORE SELECTION			
	Voltage	CPU Type	JP30
	2.2V	AM K6 266/300MHz	Pins 5 & 6 closed
»	2.8V	CX 6x86L, Pentium MMX	Pins 1 & 2, 11 & 12 closed
	2.9V	CX 6x86MX, AM K6 166/180/200MHz	Pins 1 & 2, 5 & 6 closed
	3.2V	AM K6 233MHz	Pins 1 & 2, 7 & 8 closed
»	3.52V	CX 6x86, AM K5, Pentium	Pins 1 & 2, 11 & 12 closed