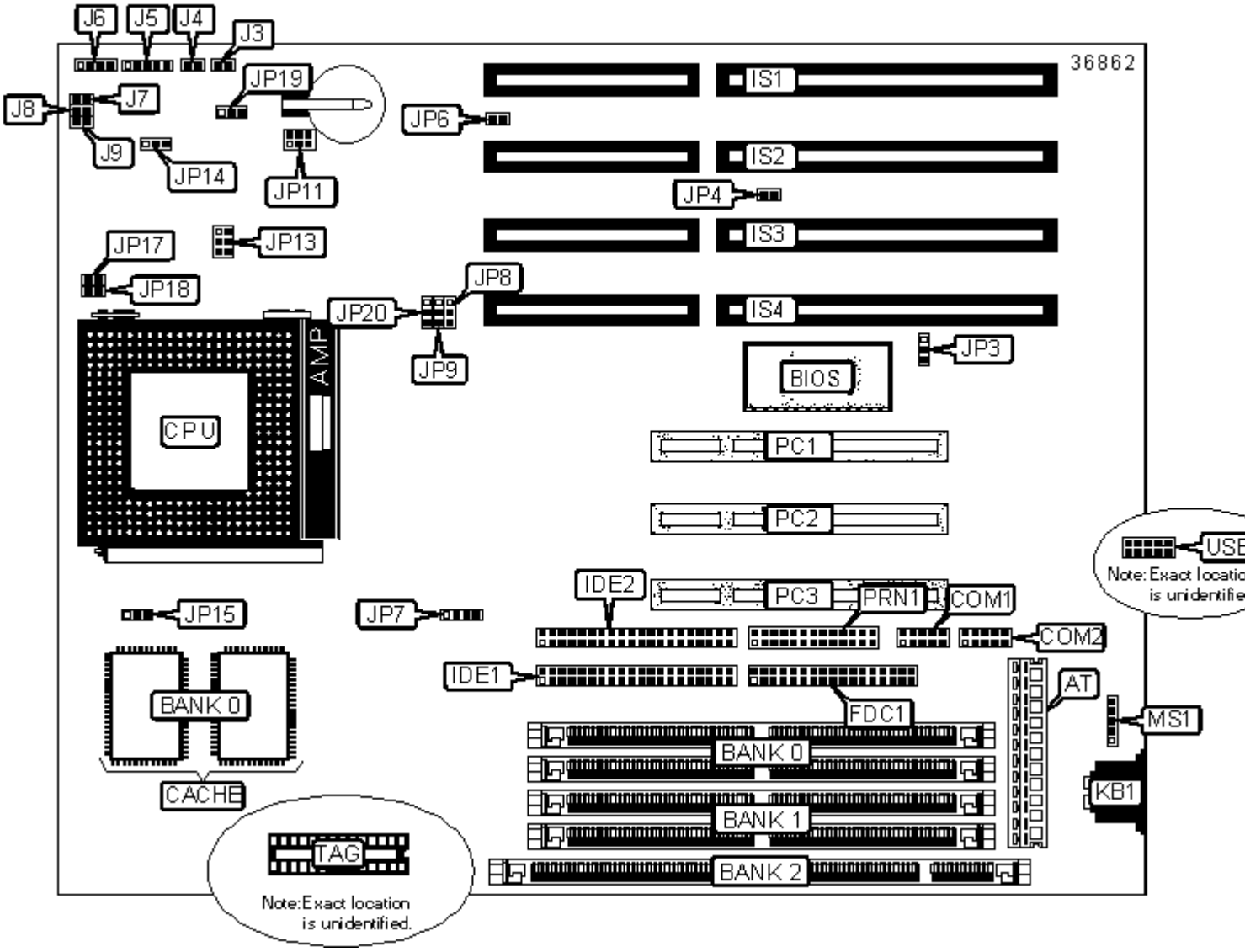
EDOM INTERNATIONAL CORPORATION

MP071B

Device Type	Mainboard
Processor	CX 6X86/IBM 6X86/CX 6X86L/IBM 6X86L/AM K5/AM K6/Pentium/Pentium MMX
Processor Speed	75/90/100/120/133/150/166/180/200/233MHz
Chip Set	Unidentified
Maximum Onboard Memory	128MB (EDO & SDRAM supported)
Cache	256/512KB
BIOS	Award
Dimensions	Unidentified
I/O Options	32-bit PCI slots (3), 16-bit ISA slots (4), floppy drive interface, IDE interfaces (2), AT Keyboard port, parallel interface, PS/2 mouse interface, serial interfaces (2), USB interface, AT power connector, cache slot



CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**
AT power connector	AT	Speaker	J6

Serial interface 1	COM1	Reset switch	J7
Serial interface 2	COM2	Turbo LED	J8
Floppy drive interface	FDC1	Turbo switch	J9
IDE interface 1	IDE1	AT keyboard port	KB1
IDE interface 2	IDE2	PS/2 mouse interface	MS1
16-bit ISA slots	IS1 - IS4	32-bit PCI slots	PC1 - PC3
Green PC connector	J3	Parallel interface	PRN1
IDE interface LED	J4	USB interface	USB
Power LED & keylock	J5	Cache slot	SL1

USER CONFIGURABLE SETTINGS			
Function		**Label**	**Position**
	Flash BIOS voltage select 12v	JP3	Pins 1 & 2 closed
	Flash BIOS voltage select 5v	JP3	Pins 2 & 3 closed
	AT bus speed select PCICLK/3	JP4	Open
	AT bus speed select PCICLK/4	JP4	Closed
»	CMOS memory normal operation	JP6	Open
	CMOS memory clear	JP6	Closed

SIMM CONFIGURATION		
Size	**Bank 0**	**Bank 1**
4MB	(2) 512M x 36	None
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36

40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36
Note: Board supports EDO & SDRAM memory. Note: Bank 1 can not be used if DIMM memory is installed in Bank 2.		

DIMM CONFIGURATION		
Size	**Bank 0**	**Bank 1**
8MB	(1) 1M x 64	None
16MB	(1) 2M x 64	None
32MB	(1) 4M x 64	None
64MB	(1) 8M x 64	None
Note: Board supports EDO & SDRAM memory. Note: Bank 1 can not be used if DIMM memory is installed in Bank 2.		

CACHE CONFIGURATION			
Size	**Bank 0**	**SL1**	**TAG**
256KB	(2) 32K x 32	Not installed	(1) 8K x 8
512KB	(2) 32K x 32	Installed	(1) 8K x 8
Note: Location of cache slot (SL1) is unidentified.			

CACHE CONFIGURATION (CONT.)		
Size	**JP7**	**JP15**
256KB	Pins 1 & 2 closed	Pins 1 & 2 closed
512KB	Pins 2 & 3 closed	Pins 2 & 3 closed

CPU SPEED SELECTION (CX 6X86)							
CPU speed	**Clock speed**	**Multiplier**	**JP8**	**JP9**	**JP17**	**JP18**	**JP20**
120MHz	50MHz	2x	1 & 2	1 & 2	Closed	Open	1 & 2
133MHz	55MHz	2x	1 & 2	1 & 2	Closed	Open	2 & 3
150MHz	60MHz	2x	2 & 3	1 & 2	Closed	Open	1 & 2
166MHz	66MHz	2x	1 & 2	2 & 3	Closed	Open	1 & 2
200MHz	75MHz	2x	2 & 3	1 & 2	Closed	Open	2 & 3
Note: Pins designated should be in the closed postion.							

CPU SPEED SELECTION (CX 6X86L)							
CPU speed	**Clock speed**	**Multiplier**	**JP8**	**JP9**	**JP17**	**JP18**	**JP20**
120MHz	50MHz	2x	1 & 2	1 & 2	Closed	Open	1 & 2
133MHz	55MHz	2x	1 & 2	1 & 2	Closed	Open	2 & 3
150MHz	60MHz	2x	2 & 3	1 & 2	Closed	Open	1 & 2
166MHz	66MHz	2x	1 & 2	2 & 3	Closed	Open	1 & 2
200MHz	75MHz	2x	2 & 3	1 & 2	Closed	Open	2 & 3
Note: Pins designated should be in the closed postion.							

CPU SPEED SELECTION (AM K5)							
CPU speed	**Clock speed**	**Multiplier**	**JP8**	**JP9**	**JP17**	**JP18**	**JP20**
75MHz	50MHz	1.5x	1 & 2	1 & 2	Open	Open	1 & 2
90MHz	60MHz	1.5x	2 & 3	1 & 2	Open	Open	1 & 2
100MHz	66MHz	1.5x	1 & 2	2 & 3	Open	Open	1 & 2
120MHz	60MHz	1.5x	2 & 3	1 & 2	Open	Open	1 & 2
133MHz	66MHz	1.5x	1 & 2	2 & 3	Open	Open	1 & 2
150MHz	60MHz	2.5x	2 & 3	1 & 2	Closed	Closed	1 & 2
166MHz	66MHz	2.5x	1 & 2	2 & 3	Closed	Closed	1 & 2

Note: Pins designated should be in the closed postion.

CPU SPEED SELECTION (AM K6)							
CPU speed	**Clock speed**	**Multiplier**	**JP8**	**JP9**	**JP17**	**JP18**	**JP20**
166MHz	66MHz	2.5x	1 & 2	2 & 3	Closed	Closed	1 & 2
200MHz	66MHz	3x	1 & 2	2 & 3	Open	Closed	1 & 2
233MHz	66MHz	3.5x	1 & 2	2 & 3	Open	Open	1 & 2

Note: Pins designated should be in the closed postion.

CPU SPEED SELECTION (PENTIUM)							
CPU speed	**Clock speed**	**Multiplier**	**JP8**	**JP9**	**JP17**	**JP18**	**JP20**
75MHz	50MHz	1.5x	1 & 2	1 & 2	Open	Open	1 & 2
90MHz	60MHz	1.5x	2 & 3	1 & 2	Open	Open	1 & 2
100MHz	66MHz	1.5x	1 & 2	2 & 3	Open	Open	1 & 2
120MHz	60MHz	2x	2 & 3	1 & 2	Closed	Open	1 & 2
133MHz	66MHz	2x	1 & 2	2 & 3	Closed	Open	1 & 2
150MHz	60MHz	2.5x	2 & 3	1 & 2	Closed	Closed	1 & 2
166MHz	66MHz	2.5x	1 & 2	2 & 3	Closed	Closed	1 & 2
180MHz	60MHz	3x	2 & 3	1 & 2	Open	Closed	1 & 2
200MHz	66MHz	3x	1 & 2	2 & 3	Open	Closed	1 & 2

Note: Pins designated should be in the closed postion.

CPU SPEED SELECTION (PENTIUM MMX)							
CPU speed	**Clock speed**	**Multiplier**	**JP8**	**JP9**	**JP17**	**JP18**	**JP20**
150MHz	60MHz	2.5x	2 & 3	1 & 2	Closed	Closed	1 & 2
166MHz	66MHz	2.5x	1 & 2	2 & 3	Closed	Closed	1 & 2
200MHz	66MHz	3x	1 & 2	2 & 3	Open	Closed	1 & 2

Note: Pins designated should be in the closed postion.

CPU VOLTAGE SELECTION		
Voltage	**JP13**	**JP19**
Single voltage	Pins 1 & 2, 3 & 4, 5 & 6 closed	Pins 1 & 2 closed
Dual voltage	Open	Pins 2 & 3 closed

CPU VOLTAGE SELECTION (SINGLE)	
Voltage	**JP14**
3.41V	Pins 1 & 2 closed
3.52V	Pins 2 & 3 closed
Note: JP11 is not used for the selection of single voltage setting.	

CPU VOLTAGE SELECTION (DUAL)			
Core Voltage	**I/O Voltage**	**JP11**	**JP14**
2.8V	3.41V	Pins 3 & 4 closed	Pins 1 & 2 closed
2.8V	3.52V	Pins 3 & 4 closed	Pins 2 & 3 closed
2.9V	3.41V	Pins 5 & 6 closed	Pins 1 & 2 closed
2.9V	3.52V	Pins 5 & 6 closed	Pins 2 & 3 closed
3.2V	3.41V	Pins 1 & 2 closed	Pins 1 & 2 closed
3.2V	3.52V	Pins 1 & 2 closed	Pins 2 & 3 closed