

SUPER MICRO

P6DGH

Device Type	Mainboard
Processor	Pentium II (2)
Processor Speed	233/266/300/333/350/366/400/450MHz
Chip Set	Intel 440GX
Maximum Onboard Memory	2GB (EDO & SDRAM supported)
Cache	256/512KB
BIOS	AMI
Dimensions	335mm x 310mm
I/O Options	32-bit PCI slots (9), 16-bit ISA slots (2), floppy drive interface, IDE interfaces (2), Ultra SCSI interface, Ultra-2 SCSI interfaces (2), parallel interface, PS/2 mouse interface, serial interfaces (2), IR connector, USB interfaces (2), ATX power connector, AGP slot, RAID slot, Wake-on-LAN connector, SB-link connector, i960 serial interface

Auxillary power connector	AUXPWR	Ultra-2 SCSI connector 2	JA2
IR connector	C1	Ultra SCSI connector	JA3
Power switch	C2	Chassis intrusion connector	JL1
Reset switch	C3	Overheat LED	JOH1
IDE interface LED	C4	Serial interface 1	JP20
Power LED & keylock	C5	Serial interface 2	JP21
PC speaker	C6	PS/2 mouse interface	JP25
16-bit ISA slots	IS1 - IS2	CPU fan power 1	JT1
IDE interface 1	J15	CPU fan power 2	JT2
IDE interface 2	J16	Overheat fan power/sensor	JT3
USB interface 1	J17	32-bit PCI slots	PC1 - PC9
USB interface 2	J18	SB-link connector	SBL
Parallel interface	J19	RAID slot	SL1
Floppy drive interface	J22	SCSI LED	SLED
AT keyboard port	J74	Wake-on-LAN connector	WOL

USER CONFIGURABLE SETTINGS

Function		Label	Position
»	Enable on board buzzer	BZON	Closed
	Disable on board buzzer	BZON	Open
»	Enable termination on Ultra-2 SCSI interface 1	JA5	Closed
	Disable termination on Ultra-2 SCSI interface 1	JA5	Open
»	Enable low byte termination on Ultra-2 SCSI interface 2	JA6	Closed
	Disable low byte termination on Ultra-2 SCSI interface 2	JA6	Open
»	Enable high byte termination on Ultra SCSI interface	JA7	Closed
	Disable high byte termination on Ultra SCSI interface	JA7	Open
»	CMOS memory normal operation	JBT1	Pins 1 & 2 closed
	CMOS memory clear	JBT1	Pins 2 & 3 closed

»	Power off after AC power recovery	JP20	Pins 2 & 3 closed
	Power on after AC power recovery	JP20	Pins 1 & 2 closed
»	Enable internal secondary arbiter for i960 interface	JP911	Open
	Disable internal secondary arbiter for i960 interface	JP911	Closed
»	Enable I/O IRZ for i960 interface	JP917	Closed
	Enable external debugging mode	JP917	Open
»	Enable i960 flash mode	JP924	Pins 1 & 2 closed
	Disable i960 flash mode	JP924	Pins 2 & 3 closed
»	Factory configured - do not alter	JPW	Unidentified

SIMM CONFIGURATION

Size	Bank 0
8MB	(2) 1M x 36
16MB	(2) 2M x 36
32MB	(2) 4M x 36
64MB	(2) 8M x 36

Note: SIMM memory is used only by the I2O architecture.

DIMM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2	Bank 3
16MB	(1) 2M x 64	None	None	None
32MB	(1) 2M x 64	(1) 2M x 64	None	None
32MB	(1) 4M x 64	None	None	None
48MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64	None
64MB	(1) 2M x 64			
64MB	(1) 4M x 64	(1) 4M x 64	None	None
64MB	(1) 8M x 64	None	None	None
96MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64	None
96MB	(1) 4M x 64	(1) 4M x 64	(1) 2M x 64	(1) 2M x 64

128MB	(1) 4M x 64			
128MB	(1) 8M x 64	(1) 8M x 64	None	None
128MB	(1) 16M x 64	None	None	None
160MB	(1) 8M x 64	(1) 8M x 64	(1) 2M x 64	(1) 2M x 64
192MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64	None
192MB	(1) 8M x 64	(1) 8M x 64	(1) 4M x 64	(1) 4M x 64
256MB	(1) 8M x 64			
256MB	(1) 16M x 64	(1) 16M x 64	None	None
256MB	(1) 32M x 64	None	None	None
288MB	(1) 16M x 64	(1) 16M x 64	(1) 2M x 64	(1) 2M x 64
320MB	(1) 16M x 64	(1) 16M x 64	(1) 4M x 64	(1) 4M x 64
384MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64	None
384MB	(1) 16M x 64	(1) 16M x 64	(1) 8M x 64	(1) 8M x 64
512MB	(1) 16M x 64			
512MB	(1) 32M x 64	(1) 32M x 64	None	None
512MB*	(1) 64M x 64	None	None	None
544MB	(1) 32M x 64	(1) 32M x 64	(1) 2M x 64	(1) 2M x 64
576MB	(1) 32M x 64	(1) 32M x 64	(1) 4M x 64	(1) 4M x 64
640MB	(1) 32M x 64	(1) 32M x 64	(1) 8M x 64	(1) 8M x 64
768MB	(1) 32M x 64	(1) 32M x 64	(1) 32M x 64	None
768MB	(1) 32M x 64	(1) 32M x 64	(1) 16M x 64	(1) 16M x 64
1024MB	(1) 32M x 64			
1024MB*	(1) 64M x 64	(1) 64M x 64	None	None
1056MB*	(1) 64M x 64	(1) 64M x 64	(1) 2M x 64	(1) 2M x 64
1088MB*	(1) 64M x 64	(1) 64M x 64	(1) 4M x 64	(1) 4M x 64
1152MB*	(1) 64M x 64	(1) 64M x 64	(1) 8M x 64	(1) 8M x 64
1280MB*	(1) 64M x 64	(1) 64M x 64	(1) 16M x 64	(1) 16M x 64
1536MB*	(1) 64M x 64	(1) 64M x 64	(1) 32M x 64	(1) 32M x 64
1536MB*	(1) 64M x 64	(1) 64M x 64	(1) 64M x 64	None

2048MB*	(1) 64M x 64			
<p>Note: Board supports EDO & SDRAM memory. Maximum registered SDRAM/EDO is 2GB. Maximum unbuffered SDRAM is 1GB.</p> <p>*Note: Memory configuration requires registered SDRAM/EDO.</p>				

i960 MODE SELECTION						
Setting		JP915	JP918	JP919	JP920	JP921
»	Mode 0	Open	Closed	Closed	Open	Closed
	Mode 3	Open	Open	Open	Open	Closed

CACHE CONFIGURATION	
<p>Note: 256KB/512KB cache is located on the Pentium II CPUs.</p>	

AT/ATX POWER MODE SELECTION		
Setting	JATPWR	JP100
AT	Pins 1 & 2, 3 & 4, 5 & 6 closed	Open
ATX	Open	Closed

CPU MULTIPLIER SELECTION				
Setting	JB1	JB2	JB3	JB4
3x	Closed	Open	Closed	Closed
3.5x	Open	Open	Closed	Closed
4x	Closed	Closed	Open	Closed
4.5x	Open	Closed	Open	Closed
5x	Closed	Open	Open	Closed
5.5x	Open	Open	Open	Closed
6x	Closed	Closed	Closed	Open

CPU BUS SPEED SELECTION	
Speed	JP11

Auto	Pins 1 & 2 closed
66MHz	Pins 2 & 3 closed
100MHz	Open