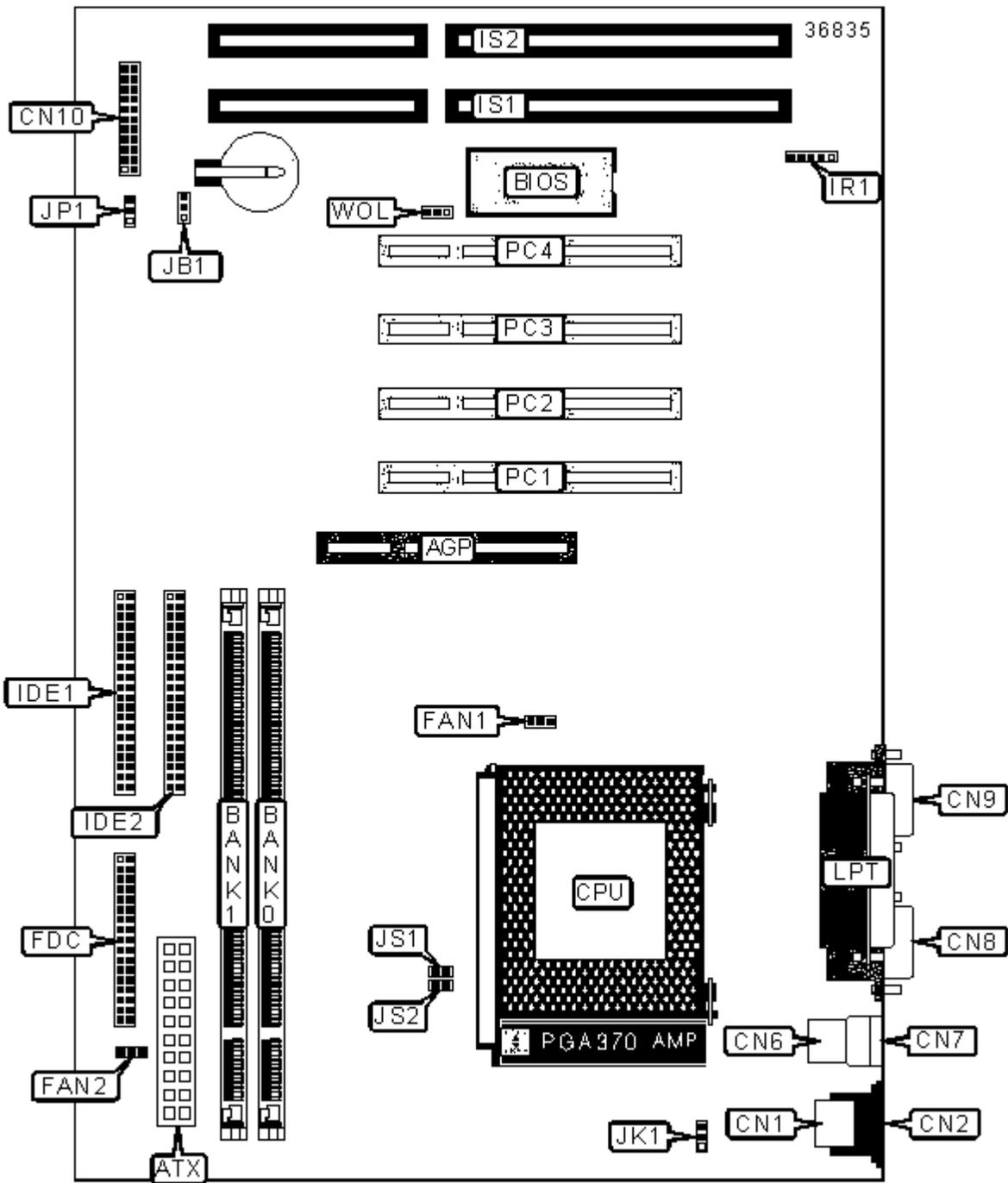


SUPERPOWER COMPUTER CO., LTD.

SP-7XV

Device Type	Mainboard
Processor	Celeron
Processor Speed	300/333/366MHz
Chip Set	VIA
Maximum Onboard Memory	768MB (EDO & SDRAM supported)
Cache	128KB (located on the Celeron CPU)
BIOS	Award
Dimensions	305mm x 190mm
I/O Options	32-bit PCI slots (4), 16-bit ISA slots (2), floppy drive interface, IDE interfaces (2), parallel port, PS/2 mouse port, PS/2 keyboard port, serial ports (2), IR connector, USB ports (2), ATX power connector, Wake-on-LAN connector, AGP slot



CONNECTIONS

Purpose	Location	Purpose	Location
AGP slot	AGP	Turbo LED	CN10/Pins 17 & 18
ATX power connector	ATX	Green PC LED	CN10/Pins 19 & 20
PS/2 mouse port	CN1	Power switch	CN10/Pins 21 & 22
PS/2 keyboard port	CN2	CPU fan power	FAN1
USB port 1	CN6	System fan power	FAN2

USB port 2	CN7	Floppy drive interface	FDC
Serial port 1	CN8	IDE interface 1	IDE1
Serial port 2	CN9	IDE interface 2	IDE2
Power LED & keylock	CN10/Pins 1, 3, 5, 7 & 9	IR connector	IR1
PC speaker	CN10/Pins 2, 4, 6 & 8	16-bit ISA slots	IS1 - IS2
IDE interface LED	CN10/Pins 11 & 12	Parallel Port	LPT
Green PC switch	CN10/Pins 13 & 14	32-bit PCI slots	PC1 - PC4
Reset switch	CN10/Pins 15 & 16	Wake-on-LAN connector	WOL

USER CONFIGURABLE SETTINGS

Function		Label	Position
»	CMOS memory normal operation	JB1	Pins 1 & 2 closed
	CMOS memory clear	JB1	Pins 2 & 3 closed
	Keyboard wake-up enabled	JK1	Pins 2 & 3 closed
	Keyboard wake-up disabled	JK1	Pins 1 & 2 closed
»	Power up on AC recovery disabled	JP1	Pins 1 & 2 closed
	Power up on AC recovery enabled	JP1	Pins 2 & 3 closed
	CPU bus clock select 66MHz	JS1	Closed
	CPU bus clock select 100MHz	JS1	Open
	CPU bus clock select automatic - overrides JS1	JS2	Closed
	CPU bus clock select controlled by JS1	JS2	Open

DIMM CONFIGURATION

Size	Bank 0	Bank 1
16MB	(1) 2M x 64	None
32MB	(1) 2M x 64	(1) 2M x 64
32MB	(1) 4M x 64	None

64MB	(1) 4M x 64	(1) 4M x 64
64MB	(1) 8M x 64	None
128MB	(1) 8M x 64	(1) 8M x 64
128MB	(1) 16M x 64	None
256MB	(1) 16M x 64	(1) 16M x 64
256MB	(1) 32M x 64	None
512MB	(1) 32M x 64	(1) 32M x 64

Note: Board supports EDO & SDRAM memory.

CACHE CONFIGURATION

Note: 128KB cache is located on the Celeron 300A and greater CPUs.