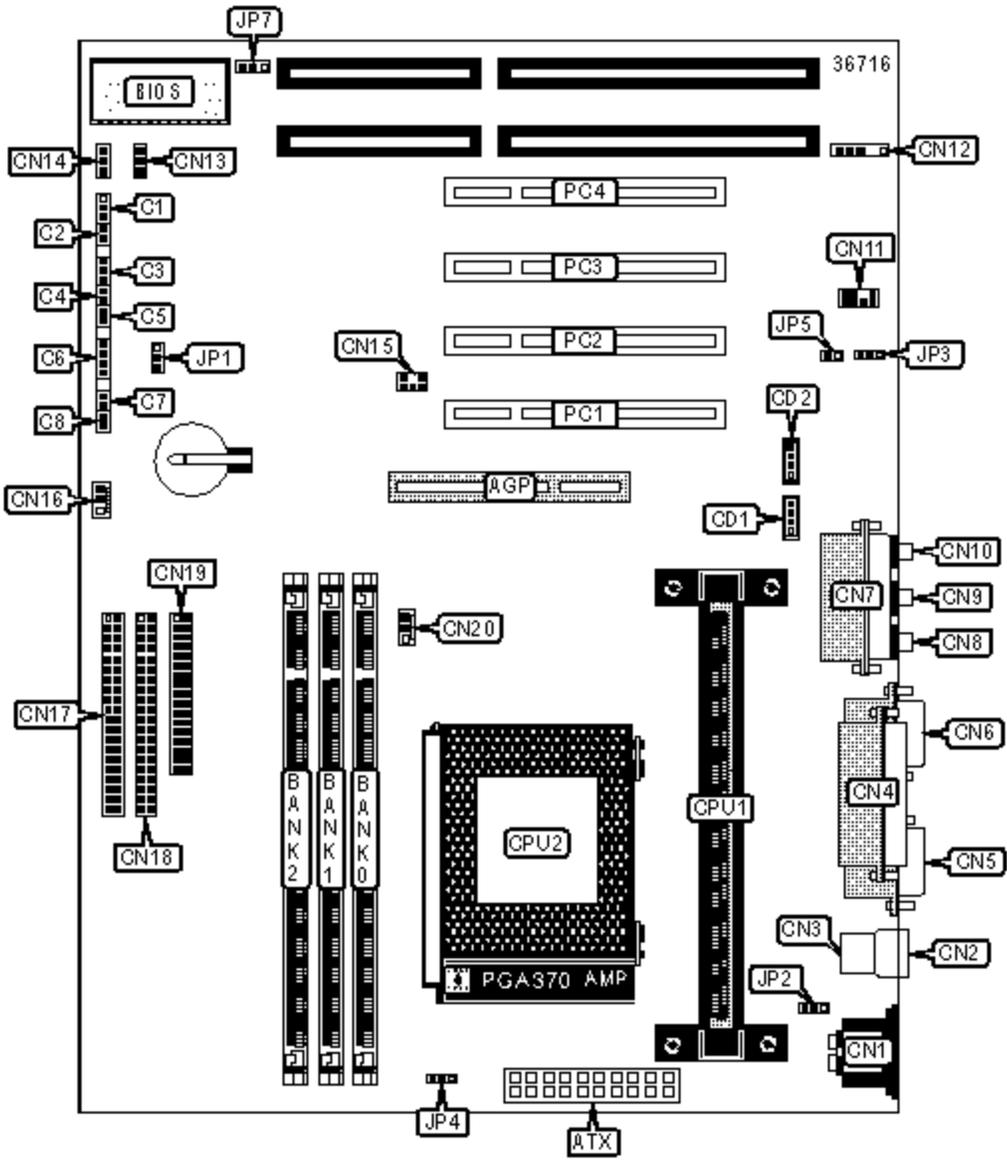# ELITEGROUP COMPUTER SYSTEMS, INC.

P6BAT-A+

Device Type	Mainboard
Processor	Celeron/Pentium II/Pentium III
Processor Speed	350/366/400/433/450/466/500MHz
Chip Set	Intel/Elite BX
Audio Chip Set	Elite
Maximum Onboard Memory	768MB (SDRAM supported)
Maximum Audio Memory	Unidentified
Cache	0/128/256/512KB (located on the CPU)
BIOS	Award
Dimensions	305mm x 220mm
I/O Options	32-bit PCI slots (4), AGP slot, floppy drive interface, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), IR connector, USB ports (2), ATX power connector, Wake-On LAN connector, Wake-On modem connector, SB-Link connector, audio in - CD-ROMs (2), game/MIDI port, line out, line in, microphone in, SPDIF connector

CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**

AGP slot	AGP	Serial port 2	CN6
ATX power connector	ATX	Game/MIDI port	CN7
Power LED connector	C1	Line out	CN8
Green PC connector	C2	Line in	CN9
Green LED connector	C3	Microphone in	CN10
Keylock connector	C4	SPDIF connector	CN11
Reset switch	C5	IR connector	CN12
Speaker	C6	Wake-On LAN connector	CN13
IDE interface LED	C7	Wake-On modem connector	CN14
Power switch	C8	SB-Link connector	CN15
Audio in - CD-ROM	CD1	System fan power	CN16
Audio in - CD-ROM	CD2	IDE interface 1	CN17
PS/2 mouse port	CN1	IDE interface 2	CN18
USB port 1	CN2	Floppy drive interface	CN19
USB port 2	CN3	CPU fan power	CN20
Parallel port	CN4	32-bit PCI slots	PC1 - PC4
Serial port 1	CN5		

USER CONFIGURABLE SETTINGS			
Function		**Label**	**Position**
»	CMOS memory normal operation	JP1	Pins 1 & 2 Closed
	CMOS memory clear	JP1	Pins 2 & 3 Closed
»	Keyboard power on enabled	JP2	Pins 2 & 3 Closed
	Keyboard power on disabled	JP2	Pins 1 & 2 Closed
»	Onboard audio enabled	JP3	Pins 1 & 2 Closed
	Onboard audio disabled	JP3	Pins 2 & 3 Closed
»	SPDIF output signal is 0.5 volts	JP5	Open
	SPDIF output signal is 5 volts	JP5	Closed

»	Flash BIOS enabled	JP7	Pins 1 & 2 Closed
	Flash BIOS disabled	JP7	Pins 2 & 3 Closed

BUS FREQUENCY SELECTION	
Setting	**JP4**
» Normal bus frequency	Pins 1 & 2 Closed
Force 100MHz	Pins 2 & 3 Closed
Note: System will automatically select 66MHz or 100MHz based on the processor when normal bus frequency settings is used. System will use 100MHz regardless of the processor when force 100MHz setting is used.	

DIMM CONFIGURATION			
Size	**Bank 0**	**Bank 1**	**Bank 2**
8MB	(1) 1M x 64	None	None
16MB	(1) 1M x 64	(1) 1M x 64	None
16MB	(1) 2M x 64	None	None
24MB	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64
32MB	(1) 2M x 64	(1) 2M x 64	None
32MB	(1) 4M x 64	None	None
32MB	(1) 2M x 64	(1) 1M x 64	(1) 1M x 64
48MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64
48MB	(1) 4M x 64	(1) 1M x 64	(1) 1M x 64
64MB	(1) 4M x 64	(1) 4M x 64	None
64MB	(1) 8M x 64	None	None
64MB	(1) 4M x 64	(1) 2M x 64	(1) 2M x 64
80MB	(1) 8M x 64	(1) 1M x 64	(1) 1M x 64
96MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64
96MB	(1) 8M x 64	(1) 2M x 64	(1) 2M x 64
128MB	(1) 8M x 64	(1) 8M x 64	None

128MB	(1) 16M x 64	None	None
128MB	(1) 8M x 64	(1) 4M x 64	(1) 4M x 64
144MB	(1) 16M x 64	(1) 1M x 64	(1) 1M x 64
160MB	(1) 16M x 64	(1) 2M x 64	(1) 2M x 64
192MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64
192MB	(1) 16M x 64	(1) 4M x 64	(1) 4M x 64
256MB	(1) 16M x 64	(1) 16M x 64	None
256MB	(1) 32M x 64	None	None
256MB	(1) 16M x 64	(1) 8M x 64	(1) 8M x 64
272MB	(1) 32M x 64	(1) 1M x 64	(1) 1M x 64
288MB	(1) 32M x 64	(1) 2M x 64	(1) 2M x 64
320MB	(1) 32M x 64	(1) 4M x 64	(1) 4M x 64
384MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64
384MB	(1) 32M x 64	(1) 8M x 64	(1) 8M x 64
512MB	(1) 32M x 64	(1) 32M x 64	None
512MB	(1) 32M x 64	(1) 16M x 64	(1) 16M x 64
768MB	(1) 32M x 64	(1) 32M x 64	(1) 32M x 64

Note: SDRAM memory is supported. PC-100 modules must be used if processor operates at a 100MHz bus (Pentium II, III cartridge). PC-100 modules or 66MHz modules may be installed if the PPGA Celeron processor is used (system will run memory at 66MHz).

CACHE CONFIGURATION

Note: 256KB/512KB cache is located on the Pentium II, III CPU. 128KB cache is located on the Celeron 300A and greater CPUs.

MISCELLANEOUS TECHNICAL NOTE

This mainboard is not a dual processor board. Only one CPU socket can be used at a time.