# IWILL CORPORATION

BD100 PLUS

Device Type   Mainboard
Processor   Celeron/Pentium II/Pentium III
Processor Speed   233/266/300/333/350/366/400/433/450/466/500/550MHz
Chip Set   Intel 440BX
Maximum Onboard Memory   1GB (EDO & SDRAM supported)
Cache   Unidentified
BIOS   Award
Dimensions   188mm x 305mm
I/O Options   32-bit PCI slots (5), floppy drive interface, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), IR connector, USB ports (2), ATX power connector, AGP slot, Wake-on LAN connector, SB-Link connector
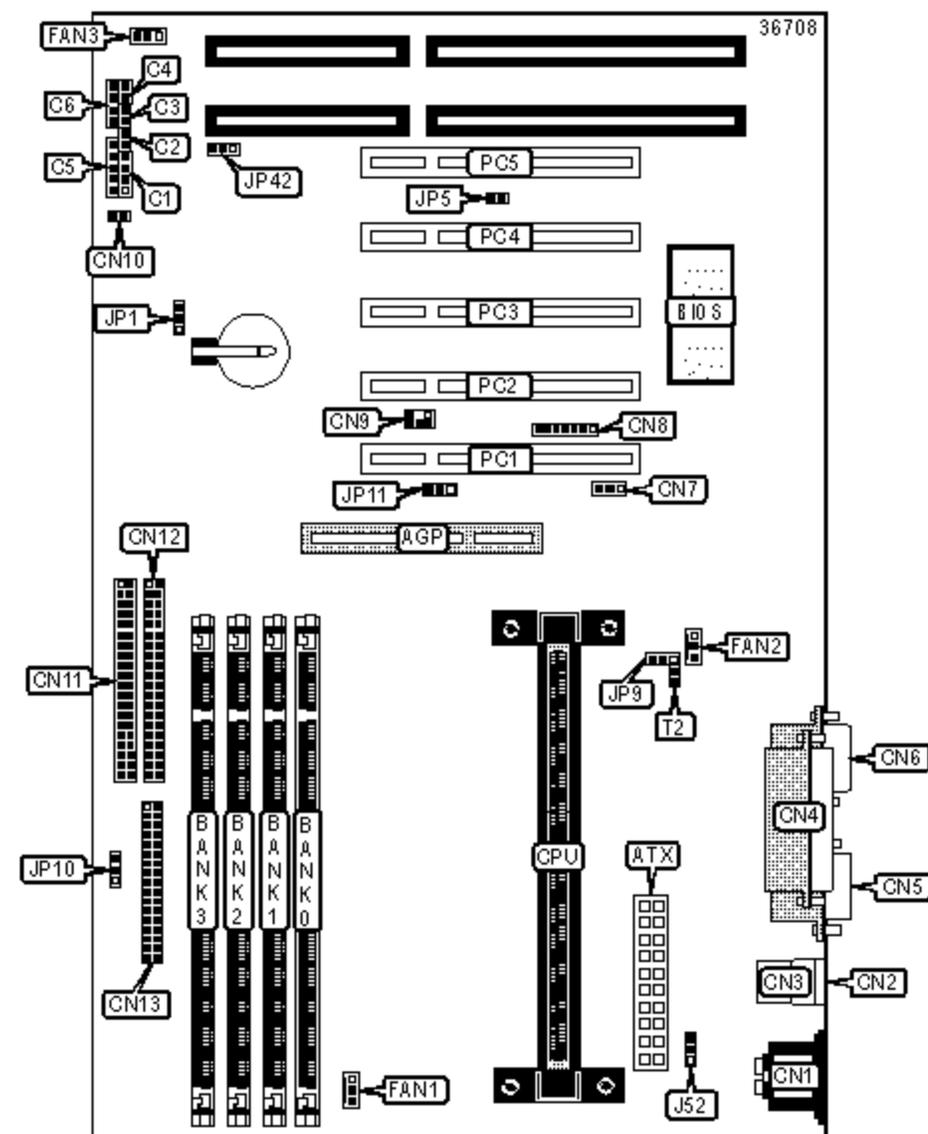
CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**
AGP slot	AGP	Serial port 2	CN6
ATX power connector	ATX	Wake-on LAN connector	CN7

ACPI LED	C1	IR connector	CN8
IDE interface LED	C2	SB-Link connector	CN9
Unidentified	C3	Power switch	CN10
Reset switch	C4	IDE interface 1	CN11
Power LED & keylock	C5	IDE interface 2	CN12
Speaker	C6	Floppy drive interface	CN13
PS/2 mouse port	CN1	CPU fan power	FAN1
USB port 1	CN2	System fan power	FAN2
USB port 2	CN3	System fan power	FAN3
Parallel port	CN4	32-bit PCI slots	PC1 - PC5
Serial port 1	CN5	Temperature sensor connector	T2

USER CONFIGURABLE SETTINGS			
	Function	**Label**	**Position**
»	PS/2 power-on disabled	J52	Pins 1 & 2 closed
	PS/2 power-on enabled	J52	Pins 2 & 3 closed
»	CMOS memory normal operation	JP1	Pins 1 & 2 closed
	CMOS memory clear	JP1	Pins 2 & 3 closed
	Factory configured - do not alter	JP5	Unidentified
»	Automatic Vcore voltage is generated	JP9	Open
	Voltage to Vcore is increased by 5%	JP9	Pins 1 & 2 closed
	Voltage to Vcore is increased by 10%	JP9	Pins 2 & 3 closed
»	VIO is 3.5V	JP10	Open
	VIO is 3.6V	JP10	Pins 1 & 2 closed
	VIO is 3.8V	JP10	Pins 2 & 3 closed
»	AGP frequency set to roughly two-thirds of the processors external frequency	JP11	Pins 1 & 2 closed
	AGP frequency set to match the processors external frequency	JP11	Pins 2 & 3 closed

	Keyboard power-on password enabled	JP42	Pins 1 & 2 closed
	Clear keyboard power-on password	JP42	Pins 2 & 3 closed

DIMM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
8MB	(1) 1M x 64	None	None	None
16MB	(1) 1M x 64	(1) 1M x 64	None	None
16MB	(1) 2M x 64	None	None	None
24MB	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64	None
32MB	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64
32MB	(1) 2M x 64	(1) 2M x 64	None	None
32MB	(1) 4M x 64	None	None	None
48MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64	None
64MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64
64MB	(1) 4M x 64	(1) 4M x 64	None	None
64MB	(1) 8M x 64	None	None	None
80MB	(1) 4M x 64	(1) 4M x 64	(1) 1M x 64	(1) 1M x 64
96MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64	None
96MB	(1) 4M x 64	(1) 4M x 64	(1) 2M x 64	(1) 2M x 64
128MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64
128MB	(1) 8M x 64	(1) 8M x 64	None	None
128MB	(1) 16M x 64	None	None	None
144MB	(1) 8M x 64	(1) 8M x 64	(1) 1M x 64	(1) 1M x 64
160MB	(1) 8M x 64	(1) 8M x 64	(1) 2M x 64	(1) 2M x 64
192MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64	None
192MB	(1) 8M x 64	(1) 8M x 64	(1) 4M x 64	(1) 4M x 64
256MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64
256MB	(1) 16M x 64	(1) 16M x 64	None	None

256MB	(1) 32M x 64	None	None	None
272MB	(1) 16M x 64	(1) 16M x 64	(1) 1M x 64	(1) 1M x 64
288MB	(1) 16M x 64	(1) 16M x 64	(1) 2M x 64	(1) 2M x 64
320MB	(1) 16M x 64	(1) 16M x 64	(1) 4M x 64	(1) 4M x 64
384MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64	None
384MB	(1) 16M x 64	(1) 16M x 64	(1) 8M x 64	(1) 8M x 64
512MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64
512MB	(1) 32M x 64	(1) 32M x 64	None	None
528MB	(1) 32M x 64	(1) 32M x 64	(1) 1M x 64	(1) 1M x 64
544MB	(1) 32M x 64	(1) 32M x 64	(1) 2M x 64	(1) 2M x 64
576MB	(1) 32M x 64	(1) 32M x 64	(1) 4M x 64	(1) 4M x 64
640MB	(1) 32M x 64	(1) 32M x 64	(1) 8M x 64	(1) 8M x 64
768MB	(1) 32M x 64	(1) 32M x 64	(1) 32M x 64	None
768MB	(1) 32M x 64	(1) 32M x 64	(1) 16M x 64	(1) 16M x 64
1024MB	(1) 32M x 64	(1) 32M x 64	(1) 32M x 64	(1) 32M x 64

Note: Board supports EDO & SDRAM memory.

Note: EDO & SDRAM can not be mixed.

CACHE CONFIGURATION

Note: 256/512KB cache is located on the Pentium II/Pentium III CPUs. 128KB cache is located on the Celeron 300A and greater CPUs.