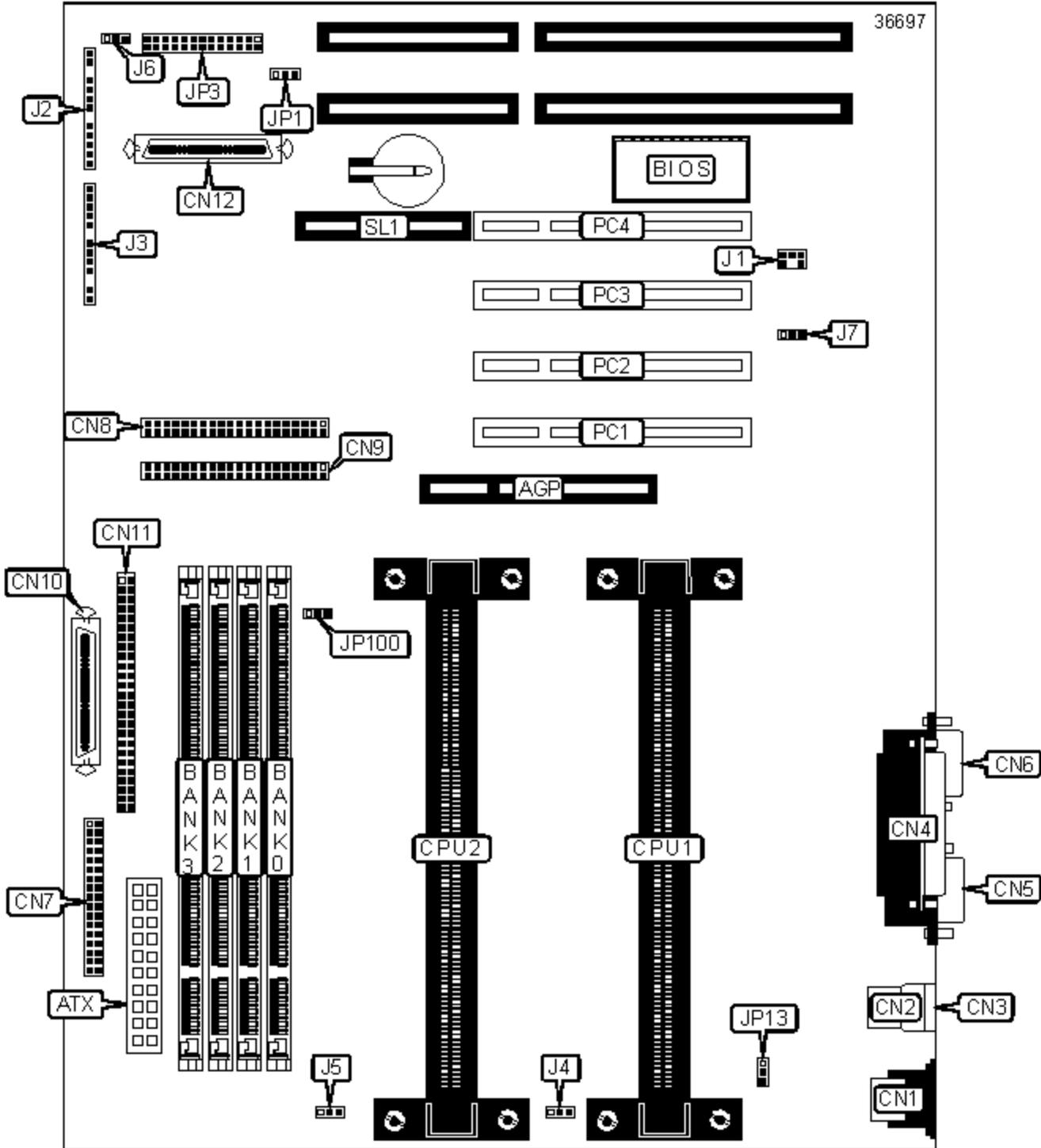


EPOX COMPUTER CO., LTD.

EP-BXBS (SMP)

Device Type	Mainboard
Processor	Pentium II (2)
Processor Speed	200/233/266/300/333/366/400/450/500/550MHz
Chip Set	Intel 440BX
Maximum Onboard Memory	1GB (EDO & SDRAM supported)
Cache	256/512KB (located on the Pentium II CPU)
BIOS	Award
Dimensions	330mm x 218mm
I/O Options	32-bit PCI slots (4), floppy drive interface, IDE interfaces (2), parallel port, PS/2 mouse port, SCSI connector, SCSI Wide connector, Ultra-2 SCSI connector, serial ports (2), IR connector, USB ports (2), ATX power connector, AGP slot, SB-link connector, Wake-on-LAN connector, RAID slot



CONNECTIONS

Purpose	Location	Purpose	Location
AGP slot	AGP	SB-link connector	J1
ATX power connector	ATX	Reset switch	J2/Pins 1 & 2
PS/2 mouse port	CN1	PC speaker	J2/Pins 4 - 7
USB port 2	CN2	Power LED & keylock	J2/Pins 9 - 13
USB port 1	CN3	IR connector	J3/Pins 1 - 5
Parallel port	CN4	IDE interface LED	J3/Pins 7 & 8

Serial port 1	CN5	Turbo LED	J3/Pins 9 & 10
Serial port 2	CN6	Power switch	J3/Pins 12 & 13
Floppy drive interface	CN7	CPU fan power	J4
IDE interface 1	CN8	CPU fan power	J5
IDE interface 2	CN9	Chassis fan power	J6
SCSI Wide connector	CN10	Wake-on-LAN connector	J7
SCSI connector	CN11	32-bit PCI slots	PC1 - PC4
Ultra-2 SCSI connector	CN12	SCSI RAID slot	SL1

USER CONFIGURABLE SETTINGS

Function		Label	Position
»	CMOS memory normal operation	JP1	Pins 1 & 2 closed
	CMOS memory clear	JP1	Pins 2 & 3 closed
»	Power-on keyboard disabled	JP13	Pins 2 & 3 closed
	Power-on keyboard enabled	JP13	Pins 1 & 2 closed
»	SDRAM clock speed set automatically	JP100	Pins 1 & 2 closed
	SDRAM clock speed set to 100MHz	JP100	Pins 2 & 3 closed

DIMM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2	Bank 3
8MB	(1) 1M x 64	None	None	None
16MB	(1) 1M x 64	(1) 1M x 64	None	None
16MB	(1) 2M x 64	None	None	None
24MB	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64	None
32MB	(1) 1M x 64			
32MB	(1) 2M x 64	(1) 2M x 64	None	None
32MB	(1) 4M x 64	None	None	None
48MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64	None

64MB	(1) 2M x 64			
64MB	(1) 4M x 64	(1) 4M x 64	None	None
64MB	(1) 8M x 64	None	None	None
80MB	(1) 4M x 64	(1) 4M x 64	(1) 1M x 64	(1) 1M x 64
96MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64	None
96MB	(1) 4M x 64	(1) 4M x 64	(1) 2M x 64	(1) 2M x 64
128MB	(1) 4M x 64			
128MB	(1) 8M x 64	(1) 8M x 64	None	None
128MB	(1) 16M x 64	None	None	None
144MB	(1) 8M x 64	(1) 8M x 64	(1) 1M x 64	(1) 1M x 64
160MB	(1) 8M x 64	(1) 8M x 64	(1) 2M x 64	(1) 2M x 64
192MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64	None
192MB	(1) 8M x 64	(1) 8M x 64	(1) 4M x 64	(1) 4M x 64
256MB	(1) 8M x 64			
256MB	(1) 16M x 64	(1) 16M x 64	None	None
256MB	(1) 32M x 64	None	None	None
272MB	(1) 16M x 64	(1) 16M x 64	(1) 1M x 64	(1) 1M x 64
288MB	(1) 16M x 64	(1) 16M x 64	(1) 2M x 64	(1) 2M x 64
320MB	(1) 16M x 64	(1) 16M x 64	(1) 4M x 64	(1) 4M x 64
384MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64	None
384MB	(1) 16M x 64	(1) 16M x 64	(1) 8M x 64	(1) 8M x 64
512MB	(1) 16M x 64			
512MB	(1) 32M x 64	(1) 32M x 64	None	None
528MB	(1) 32M x 64	(1) 32M x 64	(1) 1M x 64	(1) 1M x 64
544MB	(1) 32M x 64	(1) 32M x 64	(1) 2M x 64	(1) 2M x 64
576MB	(1) 32M x 64	(1) 32M x 64	(1) 4M x 64	(1) 4M x 64
640MB	(1) 32M x 64	(1) 32M x 64	(1) 8M x 64	(1) 8M x 64
768MB	(1) 32M x 64	(1) 32M x 64	(1) 32M x 64	None
768MB	(1) 32M x 64	(1) 32M x 64	(1) 16M x 64	(1) 16M x 64

768MB	(1) 32M x 64	(1) 32M x 64	(1) 16M x 64	(1) 16M x 64
1024MB	(1) 32M x 64			

Note: Board supports EDO & SDRAM memory. If using the 100MHz FSB, 100MHz SDRAM must be used.

CACHE CONFIGURATION

Note: 256KB/512KB cache is located on the Pentium II CPUs.

CPU SPEED SELECTION

CPU speed	Clock speed	JP3
200MHz	66MHz	Pins 1 & 2 closed
233MHz	66MHz	Pins 3 & 4 closed
» 266MHz	66MHz	Pins 5 & 6 closed
300MHz	66MHz	Pins 7 & 8 closed
300MHz	100MHz	Pins 1 & 2 closed
333MHz	66MHz	Pins 9 & 10 closed
350MHz	100MHz	Pins 3 & 4 closed
366MHz	66MHz	Pins 11 & 12 closed
400MHz	100MHz	Pins 5 & 6 closed
450MHz	100MHz	Pins 7 & 8 closed
500MHz	100MHz	Pins 9 & 10 closed
550MHz	100MHz	Pins 11 & 12 closed

Note: FSB speed is automatically set unless JP100 is set specifically to 100MHz.