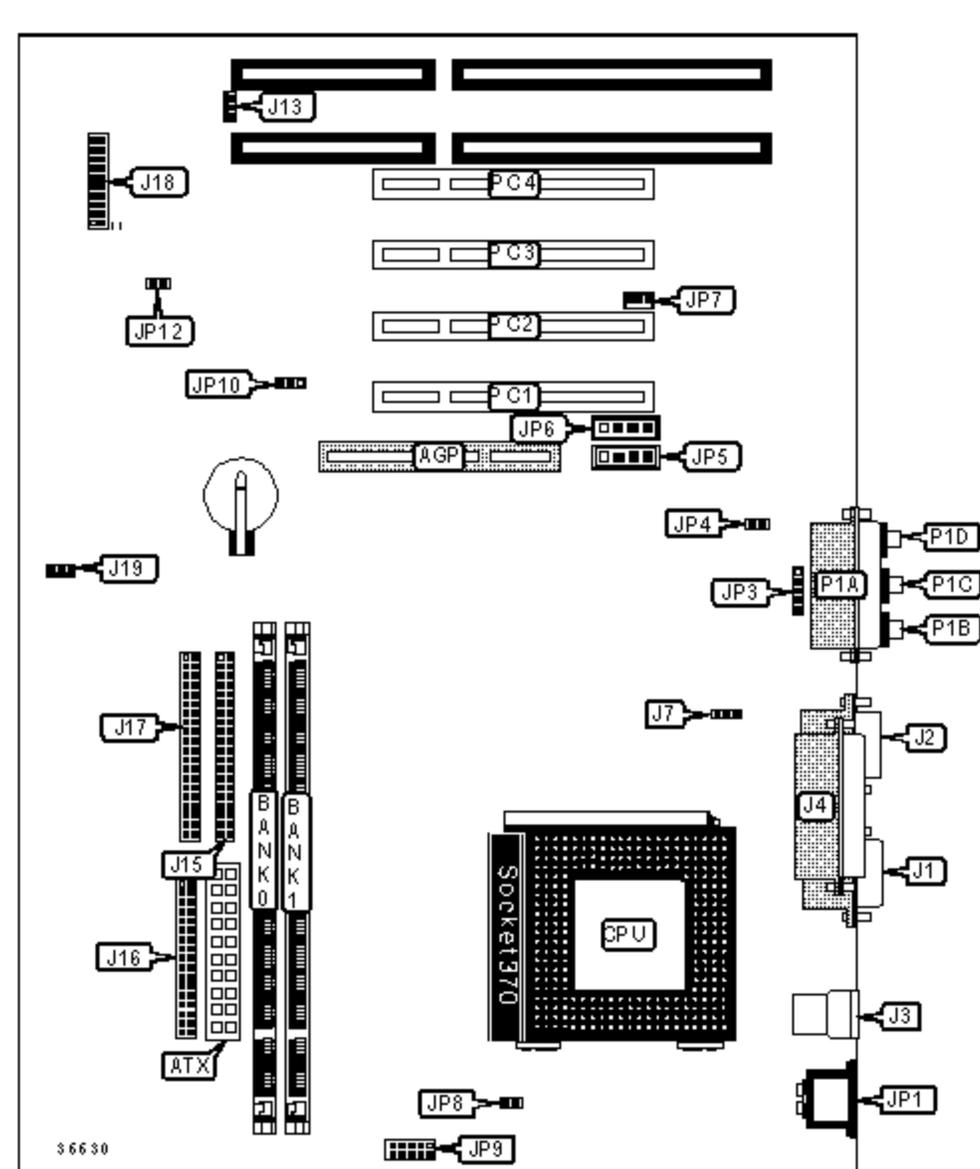


TMC RESEARCH CORPORATION

TI7NBA (440ZX) (VER. 1.0)

Device Type	Mainboard
Processor	Celeron, Pentium II
Processor Speed	300/333/366/400/433MHz
Chip Set	Intel 440ZX
Audio Chip Set	Creative Technology Ltd.
Maximum Onboard Memory	256MB (SDRAM supported)
Maximum Audio Memory	Unidentified
Cache	0/128KB (located on the Celeron/Mendocino CPU)
BIOS	Award
Dimensions	306mm x 180mm
I/O Options (backplane)	32-bit PCI slots (4), floppy drive interface, game port, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), IR connector, USB connector, ATX power connector, AGP slot, line in, line out, microphone in, audio in - CD-ROMs (2), Wake-on-LAN connector



CONNECTIONS

Purpose	Location	Purpose	Location
AGP slot	AGP	Turbo LED connector	J18/Pins 8 & 18

ATX power connector	ATX	Reset switch	J18/Pins 9 & 19
Serial port 1	J1	IDE interface LED	J18/Pins 10 & 20
Serial port 2	J2	AGP Fan Power	J19
USB port	J3	PS/2 mouse port	JP1
Parallel port	J4	IR connector	JP3
CPU fan power	J7	Unidentified	JP4
Chassis fan power	J13	Audio in - CD-ROM	JP5
IDE interface 2	J15	Audio in - CD-ROM	JP6
Floppy drive interface	J16	Wake-on-LAN connector	JP7
IDE interface 1	J17	Game port	P1A
Speaker	J18/Pins 1-4	Line out	P1B
Power LED & keylock	J18/Pins 11-15	Line in	P1C
Green PC Connector	J18/Pins 6 & 16	Microphone in	P1D
ATX power on switch	J18/Pins 7 & 17	32-bit PCI slots	PC1 - PC4

USER CONFIGURABLE SETTINGS

Function		Label	Position
»	Host frequency autodetection	JP8	Closed
	Host frequency 100 MHz	JP8	Open
»	CMOS memory normal operation	JP10	Pins 1 & 2 closed
	CMOS memory clear	JP10	Pins 2 & 3 closed

DIMM CONFIGURATION

Size	Bank 0	Bank 1
8MB	(1) 1M x 64	None
16MB	(1) 1M x 64	(1) 1M x 64
16MB	(1) 2M x 64	None

32MB	(1) 2M x 64	(1) 2M x 64
32MB	(1) 4M x 64	None
64MB	(1) 4M x 64	(1) 4M x 64
64MB	(1) 8M x 64	None
128MB	(1) 8M x 64	(1) 8M x 64
128MB	(1) 16M x 64	None
256MB	(1) 16M x 64	(1) 16M x 64
Note: Board supports SDRAM memory.		

CACHE CONFIGURATION

Note: 265/512KB is located on the Pentium II CPU. 128KB cache is located on the Celeron 300A and greater CPUs.