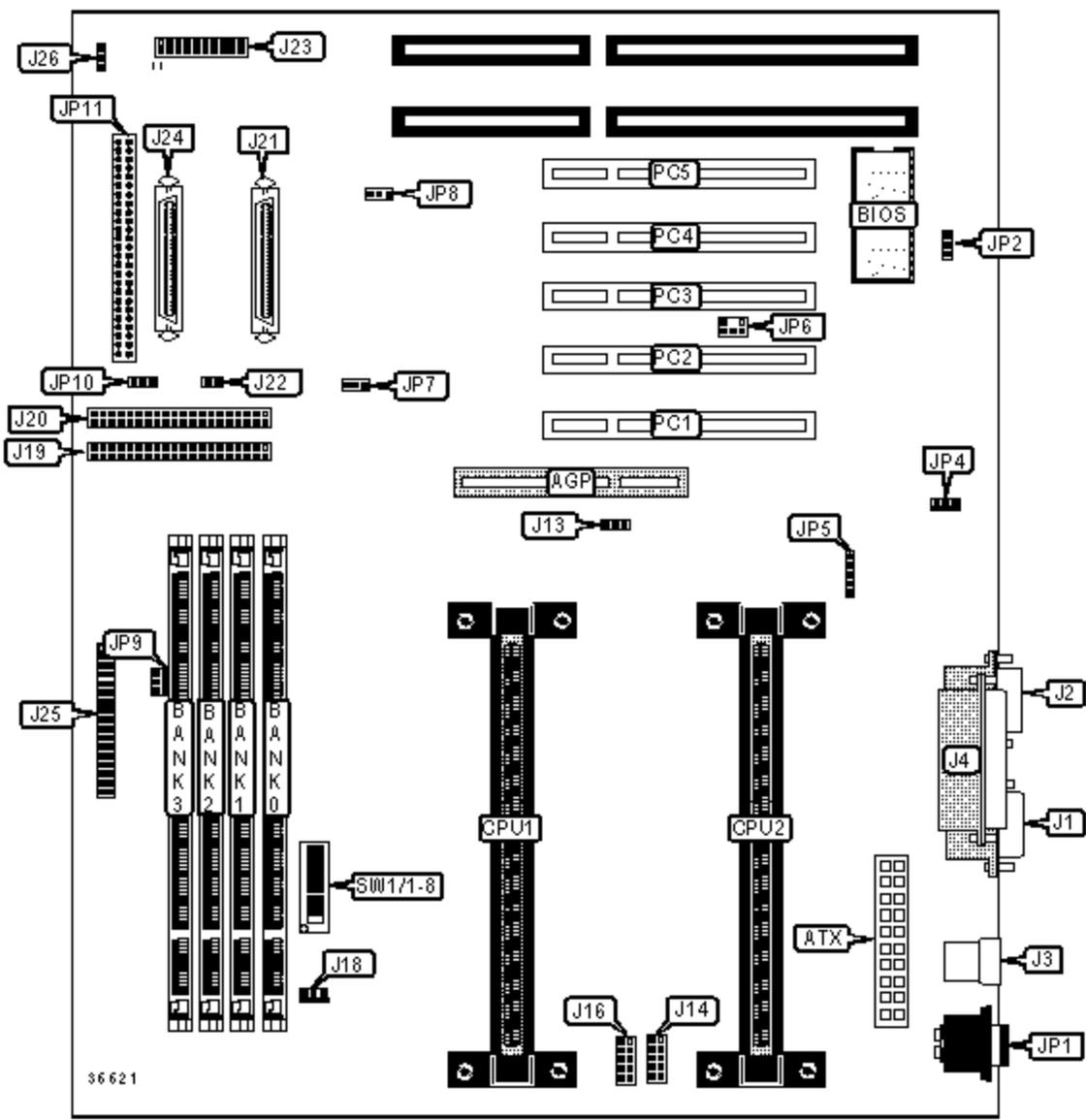


TMC RESEARCH CORPORATION

TD6NB (VER1.0)

Device Type Mainboard
Processor Pentium II
Processor Speed 233/266/300/333/350/375/400/450MHz
Chip Set Intel 440BX
Maximum Onboard Memory 512MB (SDRAM supported)
Cache 256/512KB (located on the Pentium II CPU)

BIOS Award
Dimensions 305mm x 255mm
I/O Options (backplane) 32-bit PCI slots (5), floppy drive interface, IDE interfaces (2), Ultra2 SCSI interface, Fast SCSI interface, Wide Ultra SCSI interface, parallel port, PS/2 mouse port, serial ports (2), IR connector, USB connector, ATX power connector, AGP slot, Wake-on-LAN connector, SB-Link connector



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CONNECTIONS

Purpose	Location	Purpose	Location
AGP slot	AGP	ATX power on	J23/Pins 7 & 17

ATX power connector	ATX	Turbo LED	J23/Pins 8 & 18
Serial port 1	J1	Reset switch	J23/Pins 9 & 19
Serial port 2	J2	IDE interface LED	J23/Pins 10 & 20
USB connector	J3	Ultra Wide SCSI interface	J24
Parallel port	J4	Floppy drive interface	J25
CPU fan power 1	J13	Chassis fan power	J26
CPU fan power 2	J18	PS/2 mouse port	JP1
IDE interface 2	J19	Wake-on-LAN connector	JP4
IDE interface 1	J20	IR connector	JP5
Ultra2 SCSI interface	J21	SB-Link connector	JP6
Speaker	J23/Pins 1 - 5	Fast SCSI connector	JP11
Power LED & keylock	J23/Pins 11 -15	32-bit PCI slots	PC1 - PC5
Green PC connector	J23/Pins 6 & 16		

USER CONFIGURABLE SETTINGS

Function		Label	Position
	Ultra2 SCSI terminator enabled	J22	Closed
	Ultra2 SCSI terminator disabled	J22	Open
»	PWRON select by power button	JP2	Pins 1 & 2 closed
	PWRON select by system cable	JP2	Pins 2 & 3 closed
»	CMOS memory normal operation	JP7	Pins 1 & 2 closed
	CMOS memory clear	JP7	Pins 2 & 3 closed
	IOAPIC Interrupt disabled	JP8	Pins 1 & 2 closed
	IOAPIC Interrupt enabled	JP8	Pins 2 & 3 closed
»	CMOS hardware enabled	JP9	Closed
	CMOS hardware disabled	JP9	Open
	Ultra Wide SCSI terminator disabled	JP10	Pins 1 & 2 closed

CACHE CONFIGURATION

Note: 256KB/512KB cache is located on the Pentium II CPU.

DIMM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2	Bank 3
8MB	(1) 1M x 64	None	None	None
16MB	(1) 1M x 64	(1) 1M x 64	None	None
16MB	(1) 2M x 64	None	None	None
24MB	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64	None
32MB	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64	(1) 1M x 64
32MB	(1) 2M x 64	(1) 2M x 64	None	None
32MB	(1) 4M x 64	None	None	None
48MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64	None
64MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64
64MB	(1) 4M x 64	(1) 4M x 64	None	None
64MB	(1) 8M x 64	None	None	None
80MB	(1) 4M x 64	(1) 4M x 64	(1) 1M x 64	(1) 1M x 64
96MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64	None
96MB	(1) 4M x 64	(1) 4M x 64	(1) 2M x 64	(1) 2M x 64
128MB	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64	(1) 4M x 64
128MB	(1) 8M x 64	(1) 8M x 64	None	None
128MB	(1) 16M x 64	None	None	None
144MB	(1) 8M x 64	(1) 8M x 64	(1) 1M x 64	(1) 1M x 64
160MB	(1) 8M x 64	(1) 8M x 64	(1) 2M x 64	(1) 2M x 64
192MB	(1) 8M x 64	(1) 8M x 64	(1) 8M x 64	None
192MB	(1) 8M x 64	(1) 8M x 64	(1) 4M x 64	(1) 4M x 64

256MB	(1) 8M x 64			
256MB	(1) 16M x 64	(1) 16M x 64	None	None
272MB	(1) 16M x 64	(1) 16M x 64	(1) 1M x 64	(1) 1M x 64
288MB	(1) 16M x 64	(1) 16M x 64	(1) 2M x 64	(1) 2M x 64
320MB	(1) 16M x 64	(1) 16M x 64	(1) 4M x 64	(1) 4M x 64
384MB	(1) 16M x 64	(1) 16M x 64	(1) 16M x 64	None
384MB	(1) 16M x 64	(1) 16M x 64	(1) 8M x 64	(1) 8M x 64
512MB	(1) 16M x 64			

Note: Board supports SDRAM memory.

CPU SPEED SELECTION (PENTIUM)

CPU speed	Clock speed	Multiplier	SW1/1	SW1/2	SW1/3	SW1/4
233MHz	66MHz	3.5x	Off	Off	Off	Off
266MHz	66MHz	4x	Off	Off	Off	Off
300MHz	66MHz	4.5x	Off	Off	Off	Off
300MHz	100MHz	3x	Off	Off	Off	Off
333MHz	66MHz	5x	Off	Off	Off	Off
350MHz	100MHz	3.5x	Off	Off	Off	Off
400MHz	100MHz	4x	Off	Off	Off	Off
450MHz	100MHz	4.5x	Off	Off	Off	Off

CPU SPEED SELECTION (PENTIUM) CONT.

CPU speed	Clock speed	Multiplier	SW1/5	SW1/6	SW1/7	SW1/8
233MHz	66MHz	3.5x	On	Off	Off	On
266MHz	66MHz	4x	Off	On	On	On
300MHz	66MHz	4.5x	Off	On	Off	On
300MHz	100MHz	3x	On	Off	On	On
333MHz	66MHz	5x	Off	Off	On	On

350MHz	100MHz	3.5x	On	Off	Off	On
400MHz	100MHz	4x	Off	On	On	On
450MHz	100MHz	4.5x	Off	On	Off	On