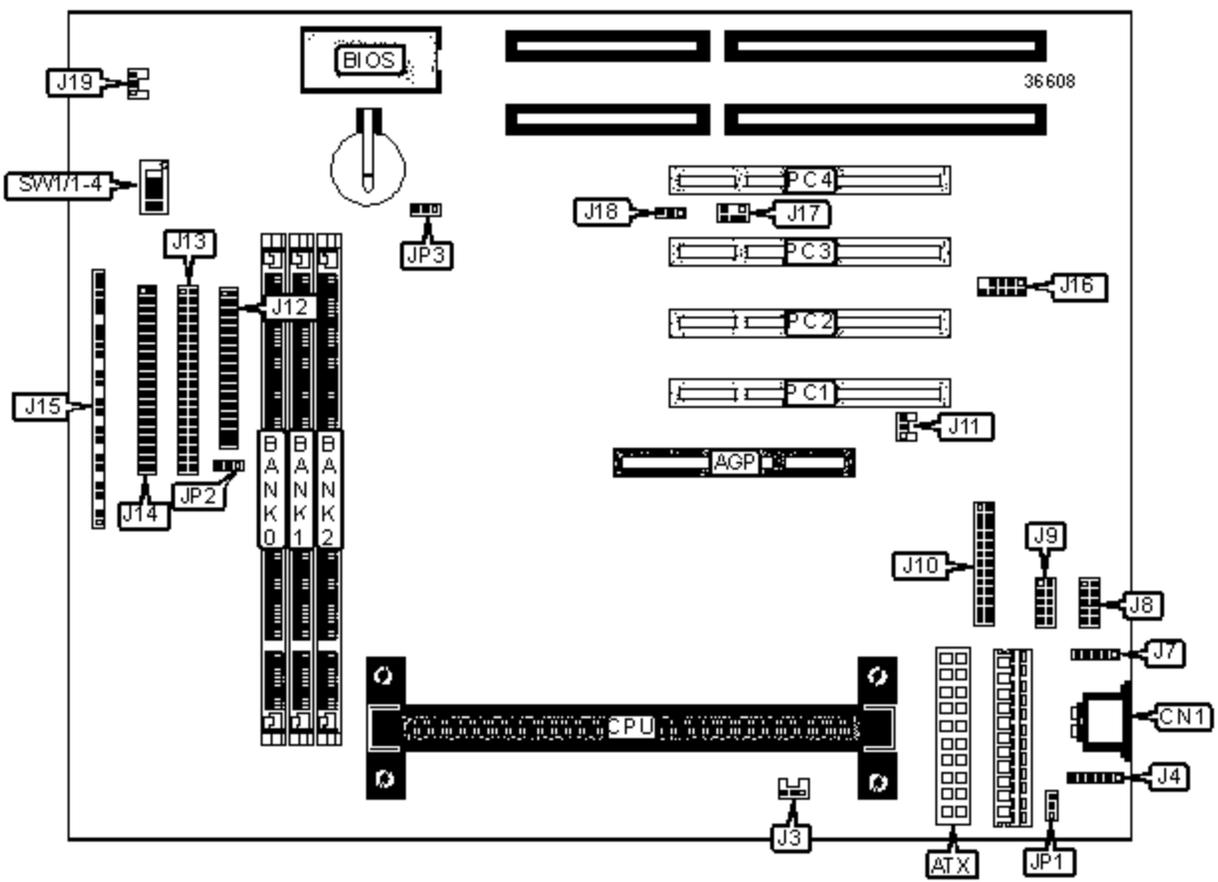


# DIAMOND FLOWER, INC.

## PB50-BX (REV. AA+)

<b>Device Type</b>	Mainboard
<b>Processor</b>	Celeron/Pentium II/Pentium III
<b>Processor Speed</b>	233/266/300/333/350/366/400/433/450/500MHz
<b>Chip Set</b>	Intel 440BX
<b>Maximum Onboard Memory</b>	384/768MB (SDRAM supported)
<b>Cache</b>	0/128/256/512KB (located on the CPU)
<b>BIOS</b>	Award
<b>Dimensions</b>	265mm x 224mm
<b>I/O Options (backplane)</b>	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel interface, PS/2 mouse interface, PS/2 keyboard port, serial interfaces (2), IR connector, USB interfaces (2), ATX power connector, AGP slot, Wake on LAN connector, SB-Link connector



CONNECTIONS			
Purpose	Location	Purpose	Location
AGP slot	AGP	ATX standby LED	J15/Pins 1 & 2
ATX power connector	ATX	IDE interface LED	J15/Pins 4 & 5
PS/2 keyboard port	CN1	Green PC LED	J15/Pins 7 & 8
CPU fan power	J3	Power switch	J15/Pins 10 & 11
PS/2 mouse interface	J4	Green PC connector	J15/Pins 13 & 14

IR connector	J7	Reset switch	J15/Pins 16 & 17
Serial interface 2	J8	Speaker	J15/Pins 19-22
Serial interface 1	J9	Power LED & keylock	J15/Pins 24-28
Parallel interface	J10	USB interfaces (2)	J16
AGP fan power	J11	SB-Link connector	J17
Floppy drive interface	J12	Wake on LAN connector	J18
IDE interface 2	J13	Chassis fan power	J19
IDE interface 1	J14	32-bit PCI slots	PC1 - PC4

### USER CONFIGURABLE SETTINGS

Function		Label	Position
»	Keyboard/mouse power on disabled	JP1	Pins 1 & 2 Closed
	Keyboard/mouse power on enabled	JP1	Pins 2 & 3 Closed
»	CMOS memory normal operation	JP3	Pins 1 & 2 Closed
	CMOS memory clear	JP3	Pins 2 & 3 Closed

### DIMM CONFIGURATION

Size	Bank 0	Bank 1	Bank 2
16MB	(1) 2M x 64	None	None
32MB	(1) 2M x 64	(1) 2M x 64	None
32MB	(1) 4M x 64**	None	None
48MB	(1) 2M x 64	(1) 2M x 64	(1) 2M x 64
64MB	(1) 4M x 64**	(1) 4M x 64**	None
64MB	(1) 8M x 64**	None	None
64MB	(1) 4M x 64	(1) 2M x 64	(1) 2M x 64
96MB	(1) 4M x 64**	(1) 4M x 64**	(1) 4M x 64**
96MB	(1) 8M x 64	(1) 2M x 64	(1) 2M x 64
128MB	(1) 8M x 64**	(1) 8M x 64**	None

128MB	(1) 16M x 64**	None	None
128MB	(1) 8M x 64**	(1) 4M x 64**	(1) 4M x 64**
160MB	(1) 16M x 64	(1) 2M x 64	(1) 2M x 64
192MB	(1) 8M x 64**	(1) 8M x 64**	(1) 8M x 64**
192MB	(1) 16M x 64**	(1) 4M x 64**	(1) 4M x 64**
256MB	(1) 16M x 64**	(1) 16M x 64**	None
256MB	(1) 32M x 64*	None	None
256MB	(1) 16M x 64**	(1) 8M x 64**	(1) 8M x 64**
320MB	(1) 32M x 64*	(1) 4M x 64*	(1) 4M x 64*
384MB	(1) 16M x 64**	(1) 16M x 64**	(1) 16M x 64**
384MB	(1) 32M x 64*	(1) 8M x 64*	(1) 8M x 64*
512MB	(1) 32M x 64*	(1) 32M x 64*	None
512MB	(1) 32M x 64*	(1) 16M x 64*	(1) 16M x 64*
768MB	(1) 32M x 64*	(1) 32M x 64*	(1) 32M x 64*

Note: Board supports SDRAM memory. Maximum registered SDRAM is 768MB. Maximum unbuffered SDRAM is 384MB.

\*Note: Designated DIMM's must be registered memory.

\*\*Note: Designated DIMM combinations can be either registered or unbuffered memory.

### CACHE CONFIGURATION

Note: 256KB/512KB cache is located on the Pentium II/Pentium III CPU. 128KB cache is located on the Celeron 300A and greater CPU.

### CPU SPEED SELECTION (CELERON)

CPU speed	Clock speed	Multiplier	SW1/1	SW1/2	SW1/3	SW1/4
266MHz	66MHz	4x	On	On	Off	On
300MHz	66MHz	4.5x	Off	On	Off	On
333MHz	66MHz	5x	On	Off	Off	On
366MHz	66MHz	5.5x	Off	Off	Off	On

400MHz	66MHz	6x	On	On	On	Off
433MHz	66MHz	6.5x	Off	On	On	Off

### CPU SPEED SELECTION (PENTIUM II)

CPU speed	Clock speed	Multiplier	SW1/1	SW1/2	SW1/3	SW1/4
233MHz	66MHz	3.5x	Off	Off	On	On
266MHz	66MHz	4x	On	On	Off	On
300MHz	66MHz	4.5x	Off	On	Off	On
333MHz	66MHz	5x	On	Off	Off	On
350MHz	100MHz	3.5x	Off	Off	On	On
400MHz	100MHz	4x	On	On	Off	On
450MHz	100MHz	4.5x	Off	On	Off	On

### CPU SPEED SELECTION (PENTIUM III)

CPU speed	Clock speed	Multiplier	SW1/1	SW1/2	SW1/3	SW1/4
450MHz	100MHz	4.5x	Off	On	Off	On
500MHz	100MHz	5x	On	Off	Off	On

### BUS SPEED SELECTION

Setting		JP2
»	Auto-detect	Pins 1 & 2 closed
	66MHz	Pins 2 & 3 closed
	100MHz	Open