

SL-62C

Connections			
Purpose	Location	Purpose	Location
AGP slot	AGP	PS/2 mouse interface	CN7
ATX power connector	ATX	IDE interface LED	J1A
Chassis fan power	C1	IR connector	J1B
Wake on LAN connector	C2	Soft off power supply	J1C
Power fan power	C3	Green PC connector	J1D
CPU fan power	C4	Speaker	J2A

Serial port 2	CN1	Reset switch	J2B
Serial port 1	CN2	Power LED & keylock	J2C
Parallel port	CN3	Green PC LED	J2D
Floppy drive interface	CN4	SB-link connector	J4
IDE interface 2	CN5	32-bit PCI slots	PC1 - PC4
IDE interface 1	CN6	USB connector	USB

USER CONFIGURABLE SETTINGS			
Function		Label	Position
»	CMOS memory normal operation	JBAT	Pins 1 & 2 closed
	CMOS memory clear	JBAT	Pins 2 & 3 closed
»	Keyboard power on disabled	JP1	Pins 1 & 2 closed
	Keyboard power on enabled	JP1	Pins 2 & 3 closed
»	Normal VGA card installed	JVGA	Closed
	Special VGA card installed	JVGA	Open

DIMM CONFIGURATION		
Size	Bank 0	Bank 1
8MB	(1) 1M x 64	None
16MB	(1) 2M x 64	None
16MB	(1) 1M x 64	(1) 1M x 64
24MB	(1) 2M x 64	(1) 1M x 64
32MB	(1) 4M x 64	None
32MB	(1) 2M x 64	(1) 2M x 64
40MB	(1) 4M x 64	(1) 1M x 64
48MB	(1) 4M x 64	(1) 2M x 64
64MB	(1) 8M x 64	None

64MB	(1) 4M x 64	(1) 4M x 64
72MB	(1) 8M x 64	(1) 1M x 64
80MB	(1) 8M x 64	(1) 2M x 64
96MB	(1) 8M x 64	(1) 4M x 64
128MB	(1) 16M x 64	None
128MB	(1) 8M x 64	(1) 8M x 64
136MB	(1) 16M x 64	(1) 1M x 64
144MB	(1) 16M x 64	(1) 2M x 64
160MB	(1) 16M x 64	(1) 4M x 64
192MB	(1) 16M x 64	(1) 8M x 64
256MB	(1) 16M x 64	(1) 16M x 64
Note: Board accepts SDRAM & PC100 memory.		

CACHE CONFIGURATION
Note: 512KB cache is located on the Pentium II CPU. 128KB cache is located on the Celeron 300A & 333 CPU.

CPU SPEED SELECTION								
CPU speed	Clock speed	Multiplier	JP3	JP4	JP8	JP9	JP10	JP11
233MHz	66MHz	3.5x	1 & 2	2 & 3	2 & 3	Open	Open	2 & 3
266MHz	66MHz	4x	1 & 2	2 & 3	Open	2 & 3	2 & 3	2 & 3
300MHz	66MHz	4.5x	1 & 2	2 & 3	Open	2 & 3	Open	2 & 3
333MHz	66MHz	5x	1 & 2	2 & 3	Open	Open	2 & 3	2 & 3
350MHz	100MHz	3.5x	1 & 2	2 & 3	2 & 3	Open	Open	2 & 3
400MHz	100MHz	4x	1 & 2	2 & 3	Open	2 & 3	2 & 3	2 & 3
450MHz	100MHz	4.5x	1 & 2	2 & 3	Open	2 & 3	Open	2 & 3
500MHz	100MHz	5x	1 & 2	2 & 3	Open	Open	2 & 3	2 & 3

USB SELECTION				
Setting		JP5	JP6	
»	Redirect all USB ports to USB connector	Pins 1 & 2 closed	Pins 1 & 2 closed	
	Redirect USB1 to AGP port	Pins 2 & 3 closed	Pins 2 & 3 closed	