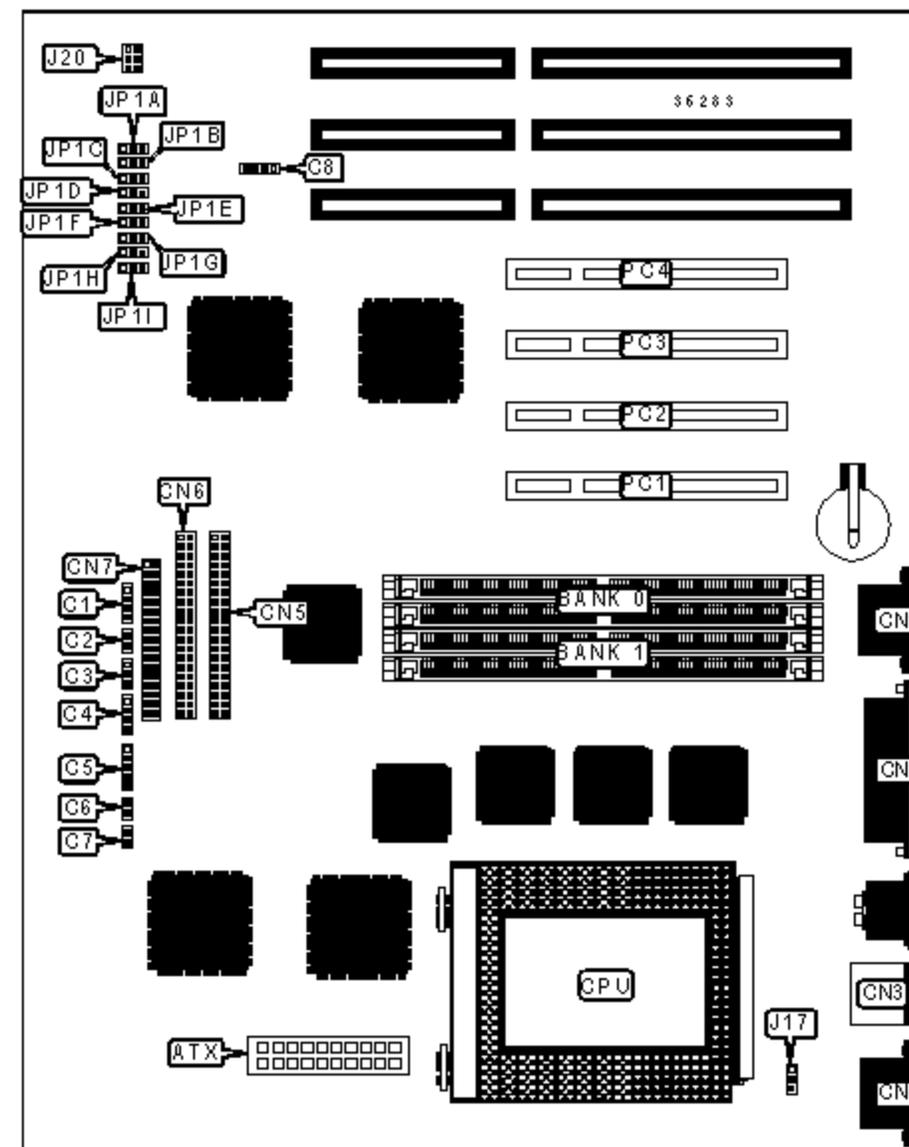


ZENITH DATA SYSTEMS

Z-STATION GT PENTIUM PRO

Device Type	Mainboard
Processor	Pentium Pro
Processor Speed	150/166MHz
Chip Set	Intel
Maximum Onboard Memory	128MB
Cache	256KB (located on Pentium Pro CPU)
BIOS	AMI
Dimensions	305mm x 244mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), IR connector, ATX power connector



CONNECTIONS

Purpose	Location	Purpose	Location
ATX power connector	ATX	Parallel port	CN2
VGA feature connector	C1	PS/2 mouse port	CN3
Speaker	C2	Serial port 1	CN4

Speaker	C2	Serial port 1	CN4
Reset switch	C3	IDE interface 2	CN5
Power LED	C4	IDE interface 1	CN6
IDE interface LED	C5	Floppy drive interface	CN7
IR connector	C6	CPU fan power	J17
Green PC connector	C7	Card slot fan connector	J20
Soft off power supply	C8	32-bit PCI slots	PC1 – PC4
Serial port 2	CN1		

USER CONFIGURABLE SETTINGS

Function		Label	Position
»	Factory configured - do not alter	JP1D	Pins 2 & 3 closed
»	Password normal operation	JP1E	Pins 2 & 3 closed
	Password clear	JP1E	Pins 1 & 2 closed
»	CMOS memory normal operation	JP1F	Pins 2 & 3 closed
	CMOS memory clear	JP1F	Pins 1 & 2 closed
»	Setup access enabled	JP1G	Pins 2 & 3 closed
	Setup access disabled	JP1G	Pins 1 & 2 closed
»	BIOS normal operation	JP1H	Pins 2 & 3 closed
	BIOS recovery mode	JP1H	Pins 1 & 2 closed
»	Factory configured - do not alter	JP1I	Pins 2 & 3 closed

SIMM CONFIGURATION

Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36

24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36

CACHE CONFIGURATION

Note: 256KB cache is located on the Pentium Pro CPU.

CPU SPEED SELECTION

CPU speed	Clock speed	Multiplier	JP1A	JP1B	JP1C
150MHz	60MHz	2.5x	1 & 2	1 & 2	1 & 2
166MHz	66MHz	2.5x	2 & 3	2 & 3	1 & 2

Note: Pins designated should be in the closed position.