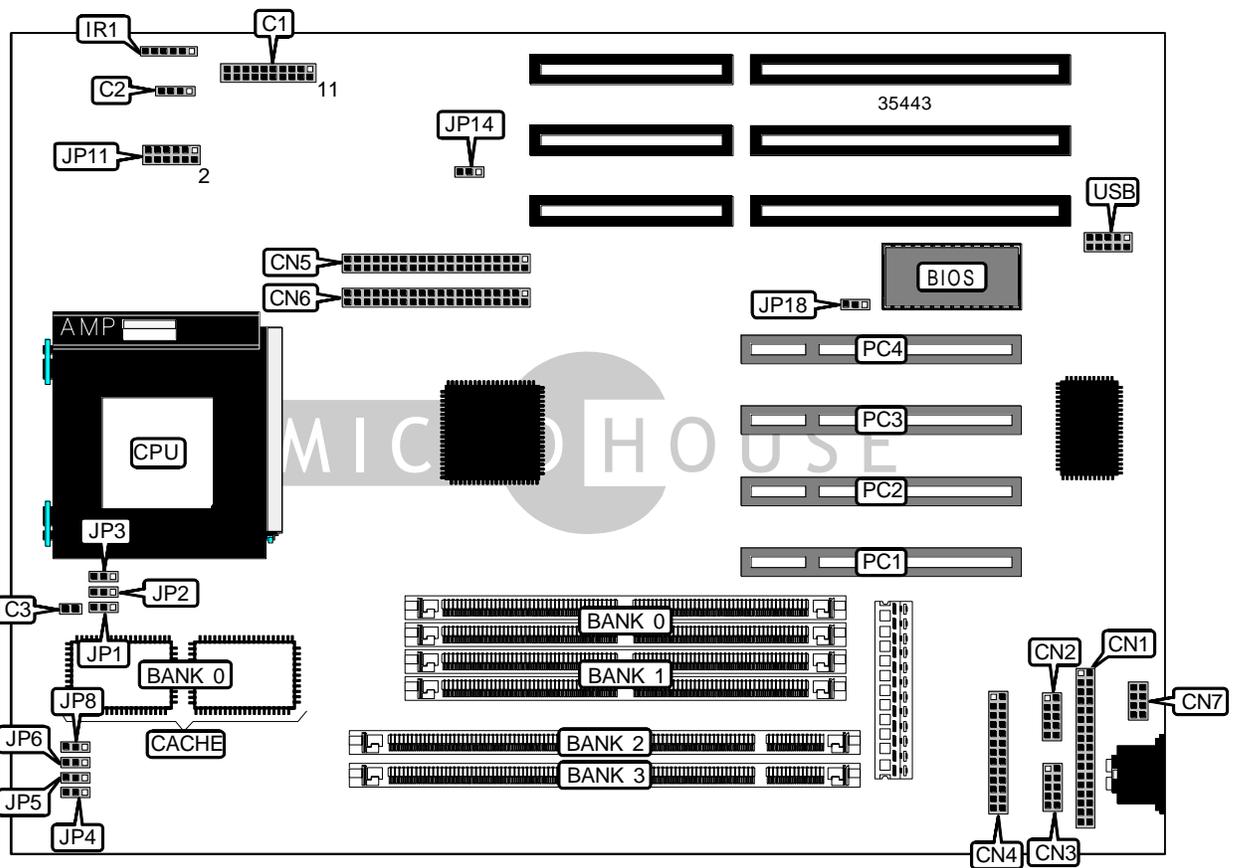


ACER, INC.
A P 5 8

Device Type	Mainboard
Processor	CX 6X86/CX 6X86L/CX 686MX/AM K5/AM K6/IDT C6/Pentium/ Pentium MMX
Processor Speed	75/90/100/120/133/150/166/200/233/266/300MHz
Chip Set	SIS
Video Chip Set	None
Maximum Onboard Memory	384MB (EDO & SDRAM supported)
Maximum Video Memory	None
Cache	256/512KB
BIOS	Award
Dimensions	280mm x 220mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse interface, serial ports (2), IR connector, USB connector
NPU Options	None



Continued on next page. . .

... continued from previous page

CONNECTIONS			
Purpose	Location	Purpose	Location
Power LED & keylock	C1/pins 1 – 5	Serial port 2	CN3
Speaker	C1/pins 7 – 10	Parallel port	CN4
Green PC LED	C1/pins 12 & 13	IDE interface 1	CN5
Green PC connector	C1/pins 15 – 17	IDE interface 2	CN6
Reset switch	C1/pins 19 & 20	PS/2 mouse interface	CN7
IDE interface LED	C2	IR connector	IR1
CPU fan power	C3	32-bit PCI slots	PC1 – PC4
Floppy drive interface	CN1	USB connector	USB
Serial port 1	CN2		

USER CONFIGURABLE SETTINGS		
Function	Label	Position
í PCI clock select synchronous	JP8	Pins 1 & 2 closed
PCI clock select asynchronous	JP8	Pins 2 & 3 closed
í CMOS memory normal operation	JP14	Pins 1 & 2 closed
CMOS memory clear	JP14	Pins 2 & 3 closed
í On board I/O enabled	JP18	Pins 1 & 2 closed
On board I/O disabled	JP18	Pins 2 & 3 closed

SIMM CONFIGURATION		
Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36
128MB	(2) 16M x 36	None
136MB	(2) 16M x 36	(2) 1M x 36

Continued on next page...

... continued from previous page

SIMM CONFIGURATION (CON'T)		
Size	Bank 0	Bank 1
144MB	(2) 16M x 36	(2) 2M x 36
160MB	(2) 16M x 36	(2) 4M x 36
192MB	(2) 16M x 36	(2) 8M x 36
256MB	(2) 16M x 36	(2) 16M x 36
Note: Board accepts EDO memory.		

DIMM CONFIGURATION		
Size	Bank 2	Bank 3
8MB	(1) 1M x 64	None
16MB	(1) 2M x 64	None
16MB	(1) 1M x 64	(1) 1M x 64
24MB	(1) 2M x 64	(1) 1M x 64
32MB	(1) 4M x 64	None
32MB	(1) 2M x 64	(1) 2M x 64
40MB	(1) 4M x 64	(1) 1M x 64
48MB	(1) 4M x 64	(1) 2M x 64
64MB	(1) 8M x 64	None
64MB	(1) 4M x 64	(1) 4M x 64
72MB	(1) 8M x 64	(1) 1M x 64
80MB	(1) 8M x 64	(1) 2M x 64
96MB	(1) 8M x 64	(1) 4M x 64
128MB	(1) 16M x 64	None
128MB	(1) 8M x 64	(1) 8M x 64
136MB	(1) 16M x 64	(1) 1M x 64
144MB	(1) 16M x 64	(1) 2M x 64
160MB	(1) 16M x 64	(1) 4M x 64
192MB	(1) 16M x 64	(1) 8M x 64
256MB	(1) 16M x 64	(1) 16M x 64
Note: Board accepts SDRAM memory.		

CACHE CONFIGURATION	
Size	Bank 0
256KB	(2) 32K x 32
512KB	(2) 64K x 32

Continued on next page. . .

... continued from previous page

CPU SPEED SELECTION (CX 6X86)								
CPU speed	Clock speed	Multiplier	JP1	JP2	JP3	JP4	JP5	JP6
120MHz	50MHz	2x	2 & 3	1 & 2	1 & 2	2 & 3	2 & 3	1 & 2
133MHz	55MHz	2x	2 & 3	1 & 2	1 & 2	2 & 3	2 & 3	2 & 3
150MHz	60MHz	2x	2 & 3	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2
166MHz	66MHz	2x	2 & 3	1 & 2	1 & 2	2 & 3	1 & 2	1 & 2
200MHz	75MHz	2x	2 & 3	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (CX 6X86L)								
CPU speed	Clock speed	Multiplier	JP1	JP2	JP3	JP4	JP5	JP6
150MHz	60MHz	2x	2 & 3	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2
166MHz	66MHz	2x	2 & 3	1 & 2	1 & 2	2 & 3	1 & 2	1 & 2
200MHz	75MHz	2x	2 & 3	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (CX 6X86MX)								
CPU speed	Clock speed	Multiplier	JP1	JP2	JP3	JP4	JP5	JP6
166MHz	60MHz	2.5x	2 & 3	2 & 3	1 & 2	1 & 2	2 & 3	1 & 2
200MHz	66MHz	2.5x	2 & 3	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2
200MHz	75MHz	2x	2 & 3	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3
233MHz	66MHz	3x	1 & 2	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2
233MHz	83MHz	2x	2 & 3	1 & 2	1 & 2	2 & 3	1 & 2	2 & 3
266MHz	66MHz	3.5x	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2	1 & 2

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (AM K5)								
CPU speed	Clock speed	Multiplier	JP1	JP2	JP3	JP4	JP5	JP6
75MHz	50MHz	1.5x	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3	1 & 2
90MHz	60MHz	1.5x	1 & 2	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2
100MHz	66MHz	1.5x	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2	1 & 2
120MHz	60MHz	1.5x	1 & 2	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2
133MHz	66MHz	1.5x	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2	1 & 2
166MHz	66MHz	1.75x	2 & 3	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2

Note: Pins designated should be in the closed position.

Continued on next page...

... continued from previous page

CPU SPEED SELECTION (AM K6)								
CPU speed	Clock speed	Multiplier	JP1	JP2	JP3	JP4	JP5	JP6
166MHz	66MHz	2.5x	2 & 3	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2
200MHz	66MHz	3x	1 & 2	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2
233MHz	66MHz	3.5x	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2	1 & 2
266MHz	66MHz	4x	2 & 3	1 & 2	2 & 3	2 & 3	1 & 2	1 & 2
300MHz	66MHz	4.5x	2 & 3	2 & 3	2 & 3	2 & 3	1 & 2	1 & 2

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (IDT C6)								
CPU speed	Clock speed	Multiplier	JP1	JP2	JP3	JP4	JP5	JP6
150MHz	75MHz	2x	2 & 3	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3
180MHz	60MHz	3x	1 & 2	2 & 3	1 & 2	1 & 2	2 & 3	1 & 2
200MHz	66MHz	3x	1 & 2	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (INTEL)								
CPU speed	Clock speed	Multiplier	JP1	JP2	JP3	JP4	JP5	JP6
75MHz	50MHz	1.5x	1 & 2	1 & 2	1 & 2	2 & 3	2 & 3	1 & 2
90MHz	60MHz	1.5x	1 & 2	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2
100MHz	66MHz	1.5x	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2	1 & 2
120MHz	60MHz	2x	2 & 3	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2
133MHz	66MHz	2x	2 & 3	1 & 2	1 & 2	2 & 3	1 & 2	1 & 2
150MHz	60MHz	2.5x	2 & 3	2 & 3	1 & 2	1 & 2	2 & 3	1 & 2
166MHz	66MHz	2.5x	2 & 3	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2
200MHz	66MHz	3x	1 & 2	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (INTEL MMX)								
CPU speed	Clock speed	Multiplier	JP1	JP2	JP3	JP4	JP5	JP6
150MHz	60MHz	2.5x	2 & 3	2 & 3	1 & 2	1 & 2	2 & 3	1 & 2
166MHz	66MHz	2.5x	2 & 3	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2
200MHz	66MHz	3x	1 & 2	2 & 3	1 & 2	2 & 3	1 & 2	1 & 2
233MHz	66MHz	3.5x	1 & 2	1 & 2	1 & 2	2 & 3	1 & 2	1 & 2

Note: Pins designated should be in the closed position.

Continued on next page...

ACER, INC.
A P 5 8

... continued from previous page

CPU VOLTAGE SELECTION	
Voltage	JP11
2.2v	Pins 11 & 12 closed
2.8v	Pins 7 & 8 closed
2.9v	Pins 5 & 6 closed
3.2v	Pins 9 & 10 closed
3.45v	Pins 1 & 2 closed
3.45v/3.52v (IDT only)	Pins 1 & 2, 3 & 4 closed
3.52v	Pins 3 & 4 closed