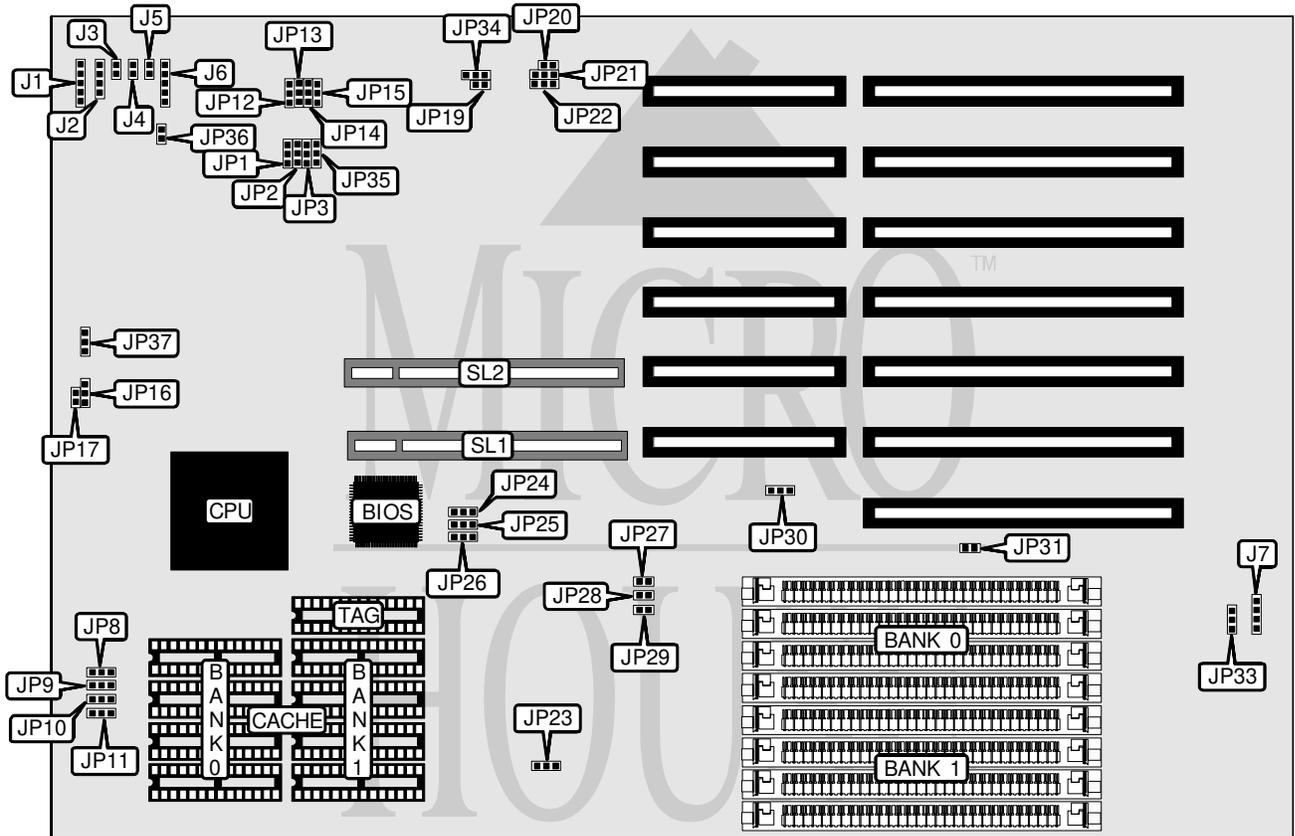


DIAMOND FLOWER, INC.

3486-ECV

Processor	80386DX/CX486DLC/AMD486DX/80486SX/80486DX/80486DX2
Processor Speed	25/33/40/50(internal)/50/66(internal)MHz
Chip Set	EFAR
Max. Onboard DRAM	32MB
Cache	64/128/256KB
BIOS	AMI
Dimensions	330mm x 218mm
I/O Options	32-bit VESA local bus slots (2)
NPU Options	80387DX



CONNECTIONS

Purpose	Location	Purpose	Location
Power LED & keylock	J1	Reset switch	J5
Speaker	J2	Keyboard connector	J6
Turbo LED	J3	External battery	J7
Turbo switch	J4	32-bit VESA Local bus slots	SL1 & SL2

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USER CONFIGURABLE SETTINGS		
Function	Jumper	Position
Flash BIOS write protect disabled	JP31	Open
Flash BIOS write protect enabled	JP31	Closed
Factory configured - do not alter.	JP32	Open
Battery select internal	JP33	pins 2 & 3 closed
Battery select external	JP33	pins 1 & 2 closed
Fast ready select disabled	JP36	Open
Fast ready select enabled	JP36	Closed

Note: The location of the factory configured jumper is unidentified.

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(4) 256K x 9	NONE
2MB	(4) 256K x 9	(4) 256K x 9
4MB	(4) 1M x 9	NONE
5MB	(4) 256K x 9	(4) 1M x 9
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	NONE
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION			
Size	Bank 0	Bank 1	TAG
64KB	(4) 8K x 8	(4) 8K x 8	(1) 8K x 8
128KB	(4) 32K x 8	NONE	(1) 8K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(1) 32K x 8

CACHE JUMPER CONFIGURATION							
Size	JP8	JP9	JP10	JP11	JP27	JP28	JP29
64KB	2 & 3	1 & 2	1 & 2	1 & 2	Closed	Open	Open
128KB	1 & 2	1 & 2	2 & 3	2 & 3	Closed	Closed	Open
256KB	1 & 2	1 & 2	1 & 2	1 & 2	Closed	Closed	Closed

Note: pins designated should be in the closed position.

CPU SPEED CONFIGURATION - 486SX/DX & 487SX							
Speed	JP1	JP2	JP3	JP16	JP17	JP34	JP37
25MHz	Closed	Open	Open	2 & 3	Open	1 & 2	2 & 3
33MHz	Closed	Open	Closed	2 & 3	Open	1 & 2	2 & 3
40MHz	Open	Closed	Closed	1 & 2	Open	1 & 2	2 & 3
50MHz	Closed	Open	Open	1 & 2	Open	2 & 3	2 & 3

Note: pins designated should be in the closed position.

CPU SPEED CONFIGURATION - 486DX2 & OVERDRIVE							
Speed	JP1	JP2	JP3	JP16	JP17	JP34	JP37
50iMHz	Closed	Open	Open	2 & 3	Open	1 & 2	2 & 3
66iMHz	Closed	Open	Closed	2 & 3	Open	1 & 2	2 & 3

Note: pins designated should be in the closed position.

CPU SPEED CONFIGURATION - 386DX & 486DLC							
Speed	JP1	JP2	JP3	JP16	JP17	JP34	JP37
25MHz	Closed	Open	Open	2 & 3	Closed	1 & 2	2 & 3
33MHz	Closed	Open	Closed	2 & 3	Closed	1 & 2	2 & 3
40MHz	Closed	Closed	Open	2 & 3	Closed	1 & 2	2 & 3

Note: pins designated should be in the closed position.

CPU SPEED CONFIGURATION - CX 486S/S2							
Speed	JP1	JP2	JP3	JP16	JP17	JP34	JP37
33MHz	Closed	Closed	Closed	1 & 2	Open	1 & 2	2 & 3
40MHz	Open	Closed	Closed	1 & 2	Open	1 & 2	2 & 3

Note: pins designated should be in the closed position.

CPU SPEED CONFIGURATION - AMD 486DX							
Speed	JP1	JP2	JP3	JP16	JP17	JP34	JP37
40MHz	Closed	Closed	Open	2 & 3	Open	2 & 3	2 & 3

Note: pins designated should be in the closed position.

CPU TYPE CONFIGURATION								
Type	JP6	JP7	JP19	JP23	JP24	JP25	JP26	JP30
386	Open	Open	Closed	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
486DLC	Open	Open	Open	2 & 3	1 & 2	1 & 2	1 & 2	2 & 3
486	Closed	Closed	Open	1 & 2	2 & 3	2 & 3	2 & 3	2 & 3

Note: pins designated should be in the closed position.

CPU TYPE CONFIGURATION				
Type	JP20	JP21	JP22	JP35
486SX	Open	Closed	Open	Open
487SX	Closed	pins 1 & 2 open	pins 1 & 2 open	Open
486DX/DX2	Closed	pins 1 & 2 open	pins 2 & 3 open	Open
CX486S	Open	pins 2 & 3 open	Open	Open
CX486S2	Open	pins 2 & 3 open	Open	Closed

Note: pins designated should be in the closed position.

CPU JUMPER CONFIGURATION								
Type	JP6	JP7	JP19	JP23	JP24	JP25	JP26	JP30
386DX	Open	Open	Closed	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
486DLC	Open	Open	Closed	2 & 3	1 & 2	1 & 2	1 & 2	2 & 3

Note: pins designated should be in the closed position.

CPU JUMPER CONFIGURATION						
CPU	JP6	JP7	JP19	JP20	JP21	JP22
AMD486	Closed	Closed	Open	Closed	1 & 2	2 & 3
CX486S	Closed	Closed	Open	Open	2 & 3	Open
486SX	Closed	Closed	Open	Open	2 & 3	Open
486DX/DX2	Closed	Closed	Open	Closed	1 & 2	2 & 3

Note: pins designated should be in the closed position.

CPU JUMPER CONFIGURATION						
CPU	JP23	JP24	JP25	JP26	JP30	JP35
AMD486	1 & 2	2 & 3	2 & 3	2 & 3	Open	2 & 3
CX486S	1 & 2	2 & 3	2 & 3	2 & 3	2 & 3	Open
486SX	1 & 2	2 & 3	2 & 3	2 & 3	2 & 3	Open
486DX/DX2	1 & 2	2 & 3	2 & 3	2 & 3	2 & 3	Open

Note: pins designated should be in the closed position.

VESA CPU TYPE CONFIGURATION		
CPU Type	JP12	JP13
80386	pins 1 & 2 closed	pins 2 & 3 closed
80486	pins 2 & 3 closed	pins 1 & 2 closed

VESA WAIT STATE/BUS SPEED CONFIGURATION			
CPU speed	Wait states	JP14	JP15
< 33MHz	0 wait states	pins 1 & 2 closed	pins 1 & 2 closed
> 33MHz	1 wait state	pins 2 & 3 closed	pins 2 & 3 closed