MICROMODULE SYSTEMS, INC.
GEMINI (REV. 2.0)

Processor	Pentium
Processor Speed	120/133/150MHz
Chip Set	Pico
Video Chip Set	None
Maximum Onboard Memory	256MB
Maximum Video Memory	None
Cache	256KB
BIOS	IBM/SystemSoft/Phoenix
Dimensions	330mm x 218mm
I/O Options	32-bit PCI slots (3)
NPU Options	None
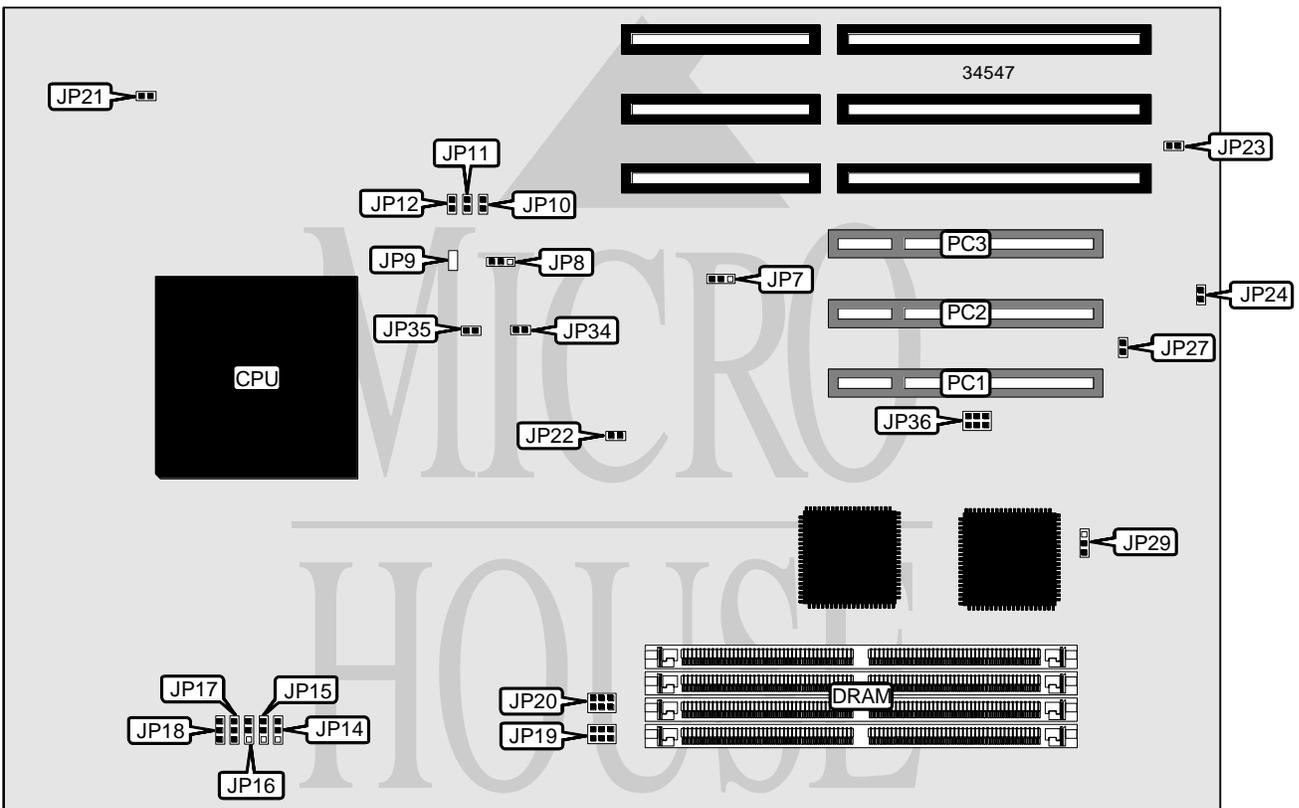
CONNECTIONS	
Purpose	**Location**
32-bit PCI slots	PC1 – PC3

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USER CONFIGURABLE SETTINGS		
Function	**Label**	**Position**
Test/monitor header	JP1	Unidentified
Test/monitor header	JP2	Unidentified
Test/monitor header	JP3	Unidentified
Test/monitor header	JP4	Unidentified
Test/monitor header	JP5	Unidentified
Test/monitor header	JP6	Unidentified
í PCI voltage select 5v	JP7	Pins 1 & 2 closed
PCI voltage select 3v	JP7	Pins 2 & 3 closed
Test/monitor header	JP9	Unidentified
Test/monitor header	JP13	Unidentified
í Factory configured - do not alter	JP17	Unidentified
í Factory configured - do not alter	JP18	Unidentified
í Speaker enabled	JP21	Closed
Speaker disabled	JP21	Open
í CMOS memory normal operation	JP22	Open
CMOS memory clear	JP22	Closed
í VPP power disabled	JP23	Open
VPP power enabled	JP23	Closed
í ROM write disabled	JP24	Open
ROM write enabled	JP24	Closed
í Data rate select pin 6	JP27	Open
Data rate select pin 6 & 27	JP27	Closed
Test/monitor header	JP28	Unidentified
í H8 clock select PLL clock generator	JP29	Pins 1 & 2 closed
H8 clock select crystal oscillator	JP29	Pins 2 & 3 closed
Test/monitor header	JP501	Unidentified
Test/monitor header	JP502	Unidentified
Note: The location of the test/monitor headers is unidentified.		

SIMM CONFIGURATION		
Size	**Bank 0**	**Bank 1**
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36

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SIMM CONFIGURATION (CON'T)		
Size	**Bank 0**	**Bank 1**
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36
128MB	(2) 16M x 36	None
136MB	(2) 16M x 36	(2) 1M x 36
144MB	(2) 16M x 36	(2) 2M x 36
160MB	(2) 16M x 36	(2) 4M x 36
192MB	(2) 16M x 36	(2) 8M x 36
256MB	(2) 16M x 36	(2) 16M x 36

SIMM MODULE POWER SELECTION	
Setting	**JP19**
í Enabled	Pins 1 & 2, 3 & 4, 5 & 6 closed
Disabled	Open

SIMM POWER SUPPLY SELECTION	
Setting	**JP20**
í Enabled	Pins 1 & 2, 3 & 4, 5 & 6 closed
Disabled	Open

CACHE CONFIGURATION
Note: The location of the cache is unidentified.

CPU SPEED SELECTION			
Speed	**JP10**	**JP11**	**JP12**
32MHz	Open	Closed	Closed
40MHz	Open	Open	Closed
50MHz	Closed	Closed	Open
60MHz	Closed	Open	Closed
66MHz	Open	Closed	Open
75MHz	Closed	Closed	Closed
80MHz	Closed	Open	Open

CPU TYPE SELECTION	
Type	**JP8**
P54C	Pins 1 & 2 closed
P55C	Pins 2 & 3 closed

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SERIAL PORT 2 SELECTION			
Setting	**JP36/pins 1 & 2**	**JP36/pins 3 & 4**	**JP36/pins 5 & 6**
Used as H8	Closed	Closed	Open
í Used as IR connector	Open	Open	Closed

PCI REQ/INT SELECTION		
Setting	**JP34**	**JP35**
í Normal operation	Open	Open
Test mode	Closed	Closed

CLOCK SKEW SELECTION			
Skew	**JP14**	**JP15**	**JP16**
0	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
.55	Pins 1 & 2 closed	Pins 1 & 2 closed	Pins 2 & 3 closed
1.10	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
1.65	Pins 1 & 2 closed	Pins 2 & 3 closed	Pins 2 & 3 closed
-2.20	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 1 & 2 closed
-1.65	Pins 2 & 3 closed	Pins 1 & 2 closed	Pins 2 & 3 closed
-1.10	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 1 & 2 closed
-.55	Pins 2 & 3 closed	Pins 2 & 3 closed	Pins 2 & 3 closed