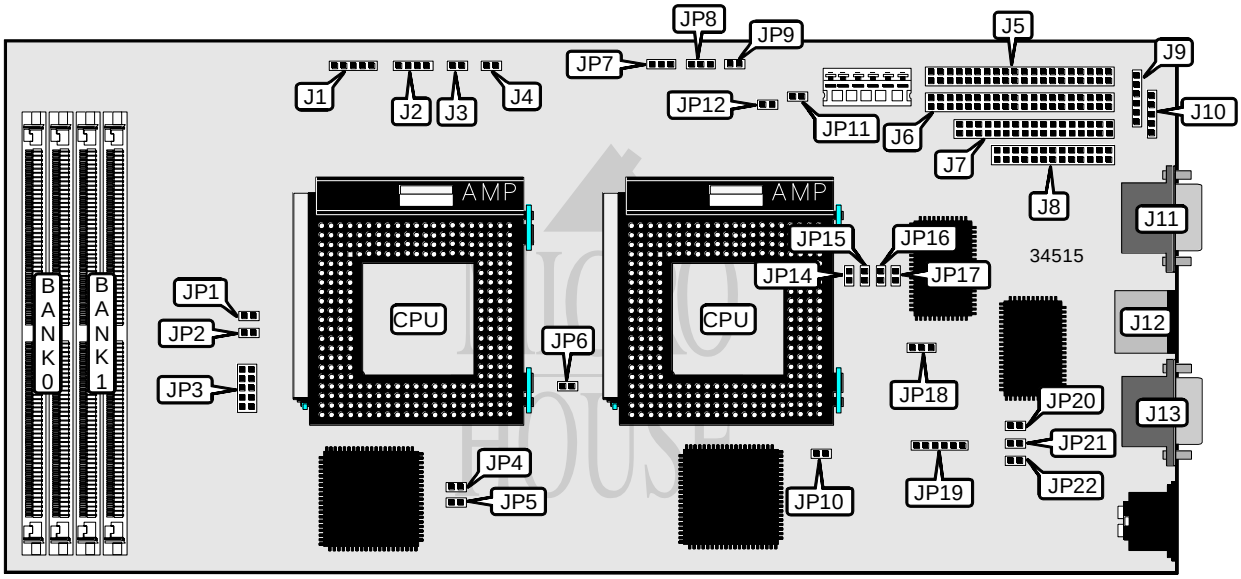
TECHNOLAND, INC.
P E A K 5 0 2 0

Processor	Pentium
Processor Speed	75/90/100/120/133/150/166/180/200MHz
Chip Set	Intel
Video Chip Set	None
Maximum Onboard Memory	256MB (EDO supported)
Maximum Video Memory	None
Cache	256/512KB
BIOS	Award
Dimensions	Unidentified
I/O Options	Floppy drive interface, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), USB connector
NPU Options	None



CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**
Power LED & keylock	J1	USB connector	J9
Speaker	J2	Auxiliary keyboard connector	J10
Reset switch	J3	Serial port 2	J11
IDE interface LED	J4	PS/2 mouse port	J12
IDE interface 1	J5	Serial port 1	J13
IDE interface 2	J6	+5v power	JP6
Floppy drive interface	J7	+5v power	JP11
Parallel port	J8		

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USER CONFIGURABLE SETTINGS		
Function	**Label**	**Position**
í CMOS memory normal operation	JP10	Open
CMOS memory clear	JP10	Closed
í SMI source select APIC	JP18	Pins 1 & 2 closed
SMI source select PIIX3	JP18	Pins 2 & 3 closed
BIOS type select EPROM	JP19	Pins 2 & 3, 4 & 5 closed
BIOS type select flash BIOS	JP19	Pins 1 & 2, 5 & 6 closed
í On board I/O enabled	JP20	Open
On board I/O disabled	JP20	Closed
í Watchdog timer port select Port F2	JP21	Closed
Watchdog timer port select Port F6	JP21	Open
í Monitor type select color	JP22	Closed
Monitor type select monochrome	JP22	Open

SIMM CONFIGURATION		
Size	**Bank 0**	**Bank 1**
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36
128MB	(2) 16M x 36	None
192MB	(2) 16M x 36	(2) 8M x 36
256MB	(2) 16M x 36	(2) 16M x 36
Note: Board accepts EDO memory. Banks are interchangeable.		

SIMM REFRESH CONFIGURATION	
Refresh rate	**JP9**
50MHz	Closed
60MHz	Closed
66MHz	Open

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CACHE CONFIGURATION
Note: The location & chip sizes are unidentified.

CACHE JUMPER CONFIGURATION			
Size	**JP2**	**JP3/pins 3 & 4**	**JP3/pins 5 & 6**
None	N/A	Open	Open
256KB	Open	Open	Closed
512KB	Closed	Closed	Open

CACHE SIZE CONFIGURATION	
Size	**JP3/pins 1 & 2**
í Extended	Open
Normal	Closed

CACHE TYPE CONFIGURATION		
Type	**JP3/pins 7 & 8**	**JP3/pins 9 & 10**
í Pipeline burst	Open	Open
2 banks PBSRAM	Closed	Closed

CPU SPEED SELECTION							
CPU speed	**Clock speed**	**Multiplier**	**JP4**	**JP5**	**JP7**	**JP8**	**JP12**
75MHz	50MHz	1.5x	Closed	Closed	1 & 2	1 & 2	Open
90MHz	60MHz	1.5x	Open	Closed	1 & 2	1 & 2	1 & 2
100MHz	66MHz	1.5x	Closed	Open	1 & 2	1 & 2	1 & 2
120MHz	60MHz	2x	Open	Closed	1 & 2	2 & 3	1 & 2
133MHz	66MHz	2x	Closed	Open	1 & 2	2 & 3	1 & 2
150MHz	60MHz	2.5x	Open	Closed	2 & 3	2 & 3	1 & 2
166MHz	66MHz	2.5x	Closed	Open	2 & 3	2 & 3	1 & 2
180MHz	60MHz	3x	Open	Closed	2 & 3	1 & 2	1 & 2
200MHz	66MHz	3x	Closed	Open	2 & 3	1 & 2	1 & 2
Note: Pins designated should be in the closed position.							

CPU TYPE SELECTION	
Type	**JP1**
Cyrix	Closed
Intel	Open

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CPU VOLTAGE SELECTION (SINGLE)				
Voltage	**JP14**	**JP15**	**JP16**	**JP17**
2.8v	Closed	Open	Open	Open
3.3v	Closed	Closed	Open	Closed

CPU VOLTAGE SELECTION (DUAL)					
Voltage	**V core**	**JP14**	**JP15**	**JP16**	**JP17**
2.8v	2.9v	Closed	Open	Open	Closed
2.8v	3.3v	Closed	Closed	Open	Closed
3.3v	2.8v	Closed	Open	Open	Open
3.3v	2.9v	Closed	Open	Open	Closed