DTK COMPUTER, INC.
PAM-0030P (VER. 1.00)

Processor	Pentium
Processor Speed	60/66MHz
Chip Set	OPTI
Video Chip Set	None
Maximum Onboard Memory	128MB
Maximum Video Memory	None
Cache	64/128/256/512KB
BIOS	AMI/Award
Dimensions	330mm x 220mm
I/O Options	32-bit VESA local bus slots (2)
NPU Options	None
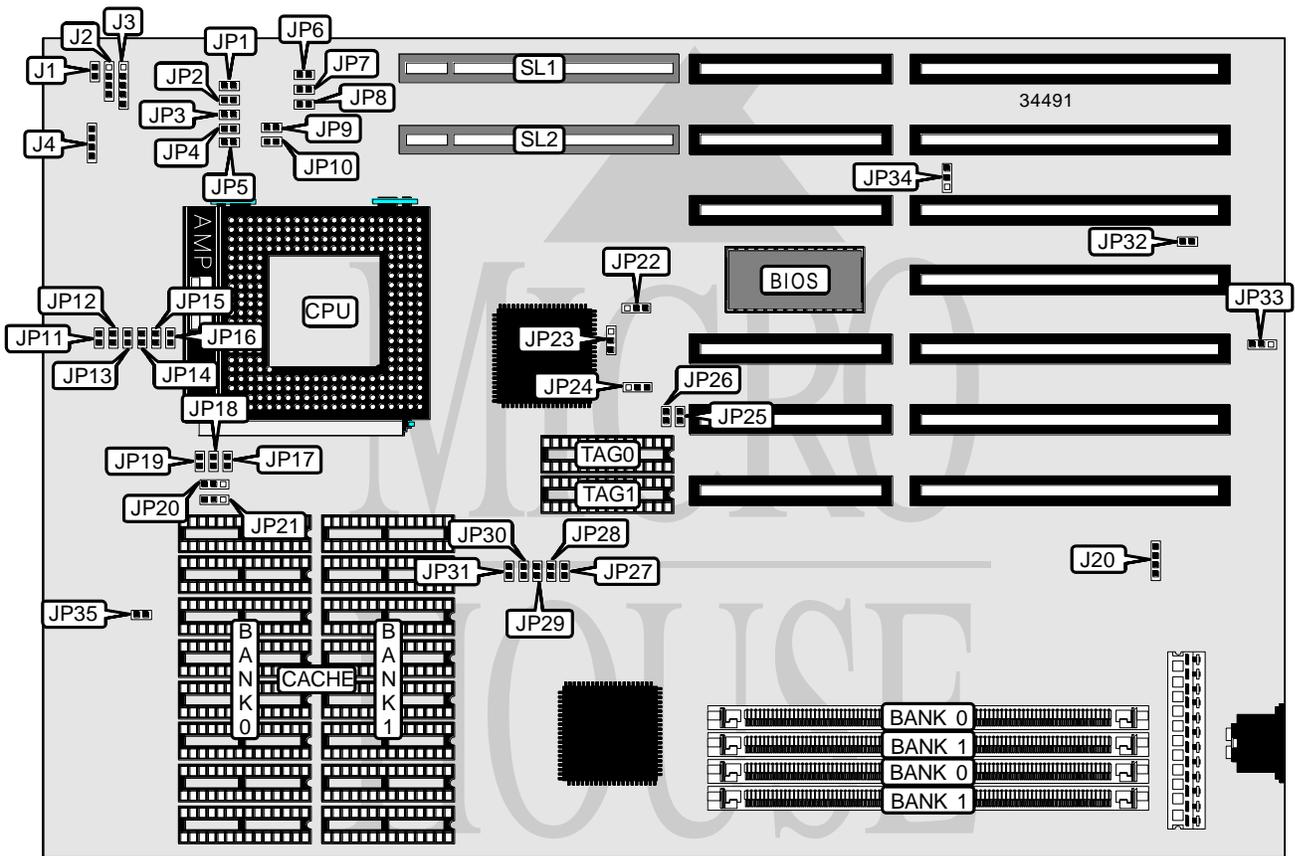
CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**
Reset switch	J1	External battery	J20
Speaker	J2	Turbo switch	JP9
Power LED & keylock	J3	Turbo LED	JP10
Chassis fan power	J4	32-bit VESA local bus slots	SL1 & SL2

USER CONFIGURABLE SETTINGS		
Function	**Label**	**Position**
í MDHDOE# inactive select end of last T2	JP25	Closed
MDHDOE# inactive select beginning of last T2	JP25	Open
í Back to back I/O delay disabled	JP29	Open
Back to back I/O delay enabled	JP29	Closed
í LDEV# inactive select end of first T2	JP30	Open
LDEV# inactive select end of second T2	JP30	Closed
í VL-BUS enabled	JP31	Open
VL-BUS disabled	JP31	Closed
í Monitor type select color	JP32	Closed
Monitor type select monochrome	JP32	Open
í Power management IRQ select IRQ15	JP34	Pins 2 & 3 closed
Power management IRQ select IRQ12	JP34	Pins 1 & 2 closed
í Normal operation	JP35	Open
Quick testing only	JP35	Closed

DRAM CONFIGURATION		
Size	**Bank 0**	**Bank 1**
2MB	(2) 256K x 36	None
4MB	(2) 512K x 36	None
4MB	(2) 256K x 36	(2) 256K x 36
6MB	(2) 512K x 36	(2) 256K x 36
6MB	(2) 256K x 36	(2) 512K x 36
8MB	(2) 1M x 36	None
8MB	(2) 512K x 36	(2) 512K x 36
10MB	(2) 256K x 36	(2) 1M x 36
12MB	(2) 1M x 36	(2) 512K x 36
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
18MB	(2) 256K x 36	(2) 2M x 36
20MB	(2) 512K x 36	(2) 2M x 36
24MB	(2) 1M x 36	(2) 2M x 36

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DRAM CONFIGURATION (CON'T)		
Size	**Bank 0**	**Bank 1**
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
34MB	(2) 256K x 36	(2) 4M x 36
36MB	(2) 512K x 36	(2) 4M x 36
40MB	(2) 1M x 36	(2) 4M x 36
48MB	(2) 2M x 36	(2) 4M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
66MB	(2) 256K x 36	(2) 8M x 36
68MB	(2) 512K x 36	(2) 8M x 36
72MB	(2) 1M x 36	(2) 8M x 36
80MB	(2) 2M x 36	(2) 8M x 36
96MB	(2) 4M x 36	(2) 8M x 36
128MB	(2) 8M x 36	(2) 8M x 36

CACHE CONFIGURATION				
Size	**Bank 0**	**Bank 1**	**TAG 0**	**TAG 1**
64KB	(8) 8K x 8	None	None	(1) 8K x 8
128KB	(8) 8K x 8	(8) 8K x 8	None	(1) 8K x 8
256KB	(8) 32K x 8	None	None	(1) 8K x 8
512KB	(8) 32K x 8	(8) 32K x 8	(1) 8K x 8	(1) 8K x 8

CACHE JUMPER CONFIGURATION						
Size	**JP17**	**JP18**	**JP19**	**JP20**	**JP21**	**JP24**
64KB	Open	Open	Open	1 & 2	1 & 2	1 & 2
128KB	Closed	Open	Open	2 & 3	2 & 3	2 & 3
256KB	Closed	Closed	Open	1 & 2	1 & 2	1 & 2
512KB	Closed	Closed	Closed	2 & 3	2 & 3	2 & 3
Note: Pins designated should be in the closed position.						

CPU SPEED SELECTION			
Speed	**JP6**	**JP7**	**JP8**
50MHz	Closed	Open	Open
60MHz	Open	Closed	Open
66MHz	Closed	Open	Closed

CPU TYPE SELECTION		
Type	**JP1**	**JP2**
80386	Open	Closed
80486	Closed	Open
í Pentium	Closed	Closed

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VL BUS WAIT STATE SELECTION	
Setting	**JP3**
0	Open
í 1	Closed

VL BUS SPEED SELECTION	
Speed	**JP4**
<= 33MHz	Open
í >33 MHz	Closed

VL BUS LCLK SELECTION			
Setting	**JP22**	**JP23**	**JP26**
í Synch = ½ CPUCLK	Pins 2 & 3 closed	Pins 2 & 3 closed	Open
Asynch U28 OSC	Pins 1 & 2 closed	Pins 1 & 2 closed	Closed

AT CLK SELECTION		
Setting	**JP27**	**JP28**
LCLK/2	Open	Open
LCLK/3	Closed	Open
í LCLK/4	Open	Closed
LCLK/5	Closed	Closed

MONITOR STANDBY SELECTION			
Timer	**JP11**	**JP12**	**JP13**
3 minutes	Open	Open	Closed
6 minutes	Open	Closed	Open
12 minutes	Closed	Open	Open

SYSTEM POWER MANAGEMENT SELECTION			
Timer	**JP14**	**JP15**	**JP16**
6 minutes	Open	Open	Closed
12 minutes	Open	Closed	Open
24 minutes	Closed	Open	Open

CMOS/BATTERY SELECTION	
Setting	**JP33**
í CMOS memory normal operation	Open
Battery type select internal	Pins 2 & 3 closed
CMOS memory clear	Pins 1 & 2 closed