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Processor	80486SX/SL80486SX/UMC486DX/AM486DX/80486DX/SL80486DX/ CX486DX2/IBM486DX2/TI486DX2/UMC486DX2/AM486DX2/ 80486DX2/SL80486DX2/CX486DX4/IBM486DX4/AM486DX4/ (SL)AM486DX4/SL80486DX4/CXP24D/P24D/P24T/CX5X86
Processor Speed	25/33/40/50(internal)/50/66(internal)/75(internal)/100(internal)/ 120(internal)MHz
Chip Set	UMC
Video Chip Set	None
Maximum Onboard Memory	256MB
Maximum Video Memory	None
Cache	64/128/256/512KB
BIOS	Unidentified
Dimensions	254mm x 218mm
I/O Options	32-bit PCI slots (3), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, serial ports (2)
NPU Options	None
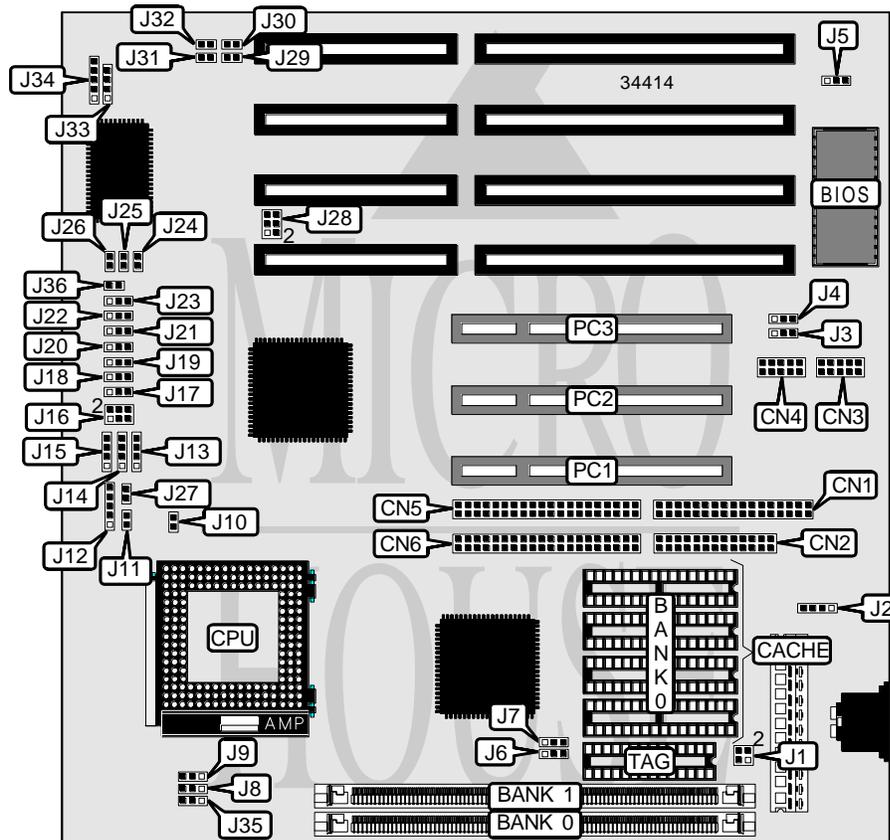
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CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**
Floppy drive interface	CN1	IDE interface LED	J29
Parallel port	CN2	Turbo switch	J30
Serial port	CN3	Reset switch	J31
Serial port	CN4	Turbo LED	J32
IDE interface 2	CN5	Speaker	J33
IDE interface 1	CN6	Power LED & keylock	J34
External battery	J2	32-bit PCI slots	PC1 – PC3
Green PC connector	J10		

USER CONFIGURABLE SETTINGS		
Function	**Label**	**Position**
? Factory configured - do not alter	J1	Unidentified
Battery type select external	J2	Closed
Battery type select internal	J2	Pins 2 & 3 closed
CMOS memory clear	J2	Pins 3 & 4 closed
? Factory configured - do not alter	J5	Unidentified

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
1MB	(1) 256K x 36	None
4MB	(1) 1M x 36	None
8MB	(1) 2M x 36	None
8MB	(1) 1M x 36	(1) 1M x 36
12MB	(1) 2M x 36	(1) 1M x 36
16MB	(1) 4M x 36	None
16MB	(1) 2M x 36	(1) 2M x 36
20MB	(1) 4M x 36	(1) 1M x 36
24MB	(1) 4M x 36	(1) 2M x 36
32MB	(1) 8M x 36	None
32MB	(1) 4M x 36	(1) 4M x 36
36MB	(1) 8M x 36	(1) 1M x 36
40MB	(1) 8M x 36	(1) 2M x 36
48MB	(1) 8M x 36	(1) 4M x 36
64MB	(1) 16M x 36	None
64MB	(1) 8M x 36	(1) 8M x 36
68MB	(1) 16M x 36	(1) 1M x 36
72MB	(1) 16M x 36	(1) 2M x 36
80MB	(1) 16M x 36	(1) 4M x 36
96MB	(1) 16M x 36	(1) 8M x 36
128MB	(1) 32M x 36	None
128MB	(1) 16M x 36	(1) 16M x 36

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DRAM CONFIGURATION (CON'T)		
Size	**Bank 0**	**Bank 1**
132MB	(1) 32M x 36	(1) 1M x 36
136MB	(1) 32M x 36	(1) 2M x 36
144MB	(1) 32M x 36	(1) 4M x 36
160MB	(1) 32M x 36	(1) 8M x 36
192MB	(1) 32M x 36	(1) 16M x 36
256MB	(1) 64M x 36	None
256MB	(1) 32M x 36	(1) 32M x 36

CACHE CONFIGURATION		
Size	**Bank 0**	**TAG**
64KB	(4) 16K x 8	Unidentified
128KB	(4) 32K x 8	Unidentified
256KB	(4) 64K x 8	Unidentified
512KB	(4) 128K x 8	Unidentified

CPU SPEED SELECTION	
Speed	**J28**
25MHz	Pins 1 & 2 closed
33MHz	Pins 1 & 2, 3 & 4, 5 & 6 closed
40MHz	Pins 1 & 2, 3 & 4 closed
50iMHz	Pins 1 & 2 closed
50MHz	Pins 5 & 6 closed
66iMHz	Pins 1 & 2, 3 & 4, 5 & 6 closed
75iMHz	Pins 1 & 2 closed
80iMHz	Pins 1 & 2, 3 & 4 closed
100iMHz	Pins 1 & 2, 3 & 4, 5 & 6 closed
120iMHz	Pins 1 & 2, 3 & 4 closed

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CPU TYPE SELECTION						
Type	**J6**	**J7**	**J12**	**J13**	**J14**	**J15**
80486SX	1 & 2	1 & 2	Open	2 & 3	Open	Open
SL80486SX	1 & 2	1 & 2	2 & 3, 4 & 5	2 & 3	Open	Open
UMC486DX	1 & 2	1 & 2	2 & 3, 4 & 5	1 & 2, 3 & 4	Open	3 & 4
AM486DX	1 & 2	2 & 3	Open	1 & 2, 3 & 4	Open	3 & 4
80486DX	1 & 2	1 & 2	Open	1 & 2, 3 & 4	Open	3 & 4
SL80486DX	1 & 2	1 & 2	2 & 3, 4 & 5	1 & 2, 3 & 4	Open	3 & 4
CX486DX2	2 & 3	1 & 2	1 & 2, 3 & 4	1 & 2, 3 & 4	2 & 3	3 & 4
IBM486DX2	2 & 3	1 & 2	1 & 2, 3 & 4	1 & 2, 3 & 4	2 & 3	3 & 4
TI486DX2	2 & 3	1 & 2	1 & 2, 3 & 4	1 & 2, 3 & 4	2 & 3	3 & 4
UMC486DX2	1 & 2	1 & 2	2 & 3, 4 & 5	1 & 2, 3 & 4	Open	3 & 4
AM486DX2	1 & 2	2 & 3	Open	1 & 2, 3 & 4	Open	3 & 4
80486DX2	1 & 2	1 & 2	Open	1 & 2, 3 & 4	Open	3 & 4
SL80486DX2	1 & 2	1 & 2	2 & 3, 4 & 5	1 & 2, 3 & 4	Open	3 & 4
CX486DX4	2 & 3	1 & 2	1 & 2, 3 & 4	1 & 2, 3 & 4	2 & 3	3 & 4
IBM486DX4	2 & 3	1 & 2	1 & 2, 3 & 4	1 & 2, 3 & 4	2 & 3	3 & 4
AM486DX4	1 & 2	2 & 3	Open	1 & 2, 3 & 4	Open	3 & 4
(SL)AM486DX4	1 & 2	1 & 2	2 & 3, 4 & 5	1 & 2, 3 & 4	Open	3 & 4
SL80486DX4	1 & 2	1 & 2	2 & 3, 4 & 5	1 & 2, 3 & 4	Open	3 & 4
CXP24D	2 & 3	1 & 2	2 & 3, 4 & 5	1 & 2, 3 & 4	Open	3 & 4
P24D	1 & 2	1 & 2	2 & 3, 4 & 5	1 & 2, 3 & 4	Open	3 & 4
P24T	1 & 2	1 & 2	2 & 3, 4 & 5	1 & 2, 3 & 4	1 & 2	2 & 3
CS5X86	1 & 2	1 & 2	1 & 2, 3 & 4	2 & 3, 4 & 5	Open	3 & 4
Note: Pins designated should be in the closed position.						

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CPU TYPE SELECTION (CON'T)				
Type	**J16**	**J17**	**J18**	**J19**
80486SX	1 & 2	Open	Open	Open
SL80486SX	3 & 4	Open	Open	Open
UMC486DX	3 & 4	Open	Open	Open
AM486DX	1 & 2	Open	Open	Open
80486DX	1 & 2	Open	Open	Open
SL80486DX	3 & 4	Open	Open	Open
CX486DX2	3 & 4	2 & 3	1 & 2	2 & 3
IBM486DX2	3 & 4	2 & 3	1 & 2	2 & 3
TI486DX2	3 & 4	2 & 3	1 & 2	2 & 3
UMC486DX2	3 & 4	Open	Open	Open
AM486DX2	1 & 2	Open	Open	Open
80486DX2	1 & 2	Open	Open	Open
SL80486DX2	3 & 4	Open	Open	Open
CX486DX4	3 & 4	2 & 3	1 & 2	Open
IBM486DX4	3 & 4	2 & 3	1 & 2	Open
AM486DX4	1 & 2	Open	Open	Open
(SL)AM486DX4	3 & 4	Open	Open	Open
SL80486DX4	3 & 4	Open	Open	Open
CXP24D	3 & 4	Open	Open	Open
P24D	3 & 4	Open	Open	Open
P24T	3 & 4, 5 & 6	1 & 2	2 & 3	Open
CS5X86	3 & 4	Open	Open	Open
Note: Pins designated should be in the closed position.				

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CPU TYPE SELECTION (CON'T)					
Type	**J20**	**J21**	**J22**	**J23**	**J24**
80486SX	Open	Open	Open	Open	Open
SL80486SX	1 & 2	Open	Open	1 & 2	Open
UMC486DX	1 & 2	2 & 3	Open	1 & 2	Open
AM486DX	Open	Open	Open	Open	Open
80486DX	Open	Open	Open	Open	Open
SL80486DX	1 & 2	2 & 3	Open	1 & 2	Open
CX486DX2	2 & 3	Open	Open	1 & 2	Open
IBM486DX2	2 & 3	Open	Open	1 & 2	Open
TI486DX2	2 & 3	Open	Open	1 & 2	Open
UMC486DX2	1 & 2	2 & 3	Open	1 & 2	Open
AM486DX2	Open	Open	Open	Open	Open
80486DX2	Open	Open	Open	Open	Open
SL80486DX2	1 & 2	2 & 3	Open	1 & 2	Open
CX486DX4	2 & 3	Open	Open	1 & 2	Open
IBM486DX4	2 & 3	Open	Open	1 & 2	Open
AM486DX4	Open	Open	Open	Open	Open
(SL)AM486DX4	1 & 2	Open	2 & 3	1 & 2	Closed
SL80486DX4	1 & 2	Open	Open	1 & 2	Open
CXP24D	1 & 2	Open	2 & 3	1 & 2	Closed
P24D	1 & 2	2 & 3	2 & 3	1 & 2	Closed
P24T	1 & 2	2 & 3	Open	1 & 2	Open
CS5X86	1 & 2	Open	2 & 3	1 & 2	Closed
Note: Pins designated should be in the closed position.					

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CPU TYPE SELECTION (CON'T)				
Type	**J25**	**J26**	**J27**	**J36**
80486SX	Open	Open	Open	Closed
SL80486SX	Open	Open	Open	Open
UMC486DX	Open	Open	Open	Open
AM486DX	Open	Open	Open	Closed
80486DX	Open	Open	Open	Closed
SL80486DX	Open	Open	Open	Open
CX486DX2	Open	Open	Open	Open
IBM486DX2	Open	Open	Open	Open
TI486DX2	Open	Open	Open	Open
UMC486DX2	Open	Open	Open	Open
AM486DX2	Open	Open	Open	Closed
80486DX2	Open	Open	Open	Closed
SL80486DX2	Open	Open	Open	Open
CX486DX4	Open	Open	Closed	Open
IBM486DX4	Open	Open	Closed	Open
AM486DX4	Open	Open	Open	Closed
(SL)AM486DX4	Closed	Closed	Open	Open
SL80486DX4	Open	Open	Open	Open
CXP24D	Closed	Closed	Closed	Open
P24D	Closed	Closed	Open	Open
P24T	Open	Open	Open	Open
CS5X86	Closed	Closed	Closed	Open

CPU VOLTAGE SELECTION				
Voltage	**J8**	**J9**	**J11**	**J35**
3.3v	Pins 2 & 3 closed	Pins 2 & 3 closed	Closed	Pins 2 & 3 closed
4v	Pins 2 & 3 closed	Pins 2 & 3 closed	Open	Pins 2 & 3 closed
5v	Pins 1 & 2 closed	Pins 1 & 2 closed	Closed	Pins 1 & 2 closed

DMA CHANNEL SELECTION		
Channel	**J3**	**J4**
1	Pins 1 & 2 closed	Pins 1 & 2 closed
3	Pins 2 & 3 closed	Pins 2 & 3 closed