

MICRO-STAR INTERNATIONAL CO., LTD.
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CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 1	CN1	Speaker	JFP/pins 7 - 10
Serial port 2	CN2	Turbo LED	JFP/pins 12 & 13
Parallel port	CN3	Reset switch	JFP/pins 19 & 20
Floppy drive interface	CN4	IDE interface LED	JFP/pins 21 & 22
IDE interface 2	CN5	Green PC LED	JGL
IDE interface 1	CN6	Green PC connector	JGS
Chassis fan power	FAN	PS/2 mouse interface	JM1
IR connector	IR1	32-bit PCI slots	PC1 – PC4
Power LED & keylock	JFP/pins 1 - 5	USB connector	USB

USER CONFIGURABLE SETTINGS		
Function	Label	Position
í Factory configured - do not alter	JP1	Unidentified
í CMOS memory normal operation	JRTC	Open
CMOS memory clear	JRTC	Closed

DIMM/DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
8MB	(2) 1M x 36	None	None	None
8MB	None	None	(1) 1M x 64	None
16MB	(2) 2M x 36	None	None	None
16MB	None	None	(1) 2M x 64	None
16MB	(2) 1M x 36	(2) 1M x 36	None	None
16MB	(2) 1M x 36	None	None	(1) 1M x 64
24MB	None	(2) 2M x 36	(1) 1M x 64	None
32MB	(2) 4M x 36	None	None	None
32MB	None	None	(1) 4M x 64	None
32MB	(2) 2M x 36	None	None	(1) 2M x 64
32MB	None	(2) 2M x 36	(1) 2M x 64	None
40MB	(2) 4M x 36	(2) 1M x 36	None	None
40MB	(2) 4M x 36	None	None	(1) 1M x 64
48MB	(2) 2M x 36	None	None	(1) 4M x 64
48MB	(2) 4M x 36	(2) 2M x 36	None	None
48MB	(2) 4M x 36	None	None	(1) 2M x 64
64MB	(2) 8M x 36	None	None	None
64MB	None	None	None	(1) 8M x 64
64MB	(2) 4M x 36	(2) 4M x 36	None	None
64MB	(2) 4M x 36	None	None	(1) 4M x 64
64MB	None	None	(1) 4M x 64	(1) 4M x 64

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DIMM/DRAM CONFIGURATION (CON'T)				
Size	Bank 0	Bank 1	Bank 2	Bank 3
72MB	(2) 8M x 36	(2) 1M x 36	None	None
72MB	(2) 8M x 36	None	None	(1) 1M x 64
80MB	None	(2) 2M x 36	(1) 8M x 64	None
80MB	(2) 8M x 36	(2) 2M x 36	None	None
96MB	(2) 4M x 36	None	None	(1) 8M x 64
96MB	(2) 8M x 36	(2) 4M x 36	None	None
96MB	(2) 8M x 36	None	None	(1) 4M x 64
128MB	None	None	(1) 8M x 64	(1) 8M x 64
128MB	(2) 8M x 36	(2) 8M x 36	None	None
128MB	(2) 8M x 36	None	None	(1) 8M x 64

Note: Board accepts EDO memory.

DIMM VOLTAGE CONFIGURATION	
Voltage	J2
3.3v	Pins 3 & 5, 4 & 6 closed
5v	Pins 1 & 3, 2 & 4 closed

CACHE CONFIGURATION	
Size	Bank 0
256KB	(2) 32K x 32
512KB	(2) 64K x 32

CPU SPEED SELECTION (CYRIX)								
CPU speed	Clock speed	Multiplier	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6
120MHz	50MHz	2x	On	On	Off	On	Off	Off
133MHz	55MHz	2x	Off	On	Off	On	Off	Off
150MHz	60MHz	2x	On	Off	Off	On	Off	Off
166MHz	66MHz	2x	Off	Off	Off	On	Off	Off

CPU SPEED SELECTION (AMD K5)								
CPU speed	Clock speed	Multiplier	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6
90MHz	60MHz	1.5x	On	Off	Off	Off	Off	Off
100MHz	66MHz	1.5x	Off	Off	Off	Off	Off	Off
133MHz	66MHz	2x	Off	Off	Off	On	Off	Off
166MHz	66MHz	2.5x	Off	Off	Off	On	On	Off

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CPU SPEED SELECTION (AMD K6)								
CPU speed	Clock speed	Multiplier	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6
166MHz	66MHz	2.5x	On	On	Off	On	On	Off
200MHz	66MHz	3x	Off	Off	Off	Off	On	Off

CPU SPEED SELECTION (INTEL)								
CPU speed	Clock speed	Multiplier	SW1/1	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6
75MHz	50MHz	1.5x	On	On	Off	Off	Off	Off
90MHz	60MHz	1.5x	On	Off	Off	Off	Off	Off
100MHz	66MHz	1.5x	Off	Off	Off	Off	Off	Off
120MHz	60MHz	2x	On	Off	Off	On	Off	Off
133MHz	66MHz	2x	Off	Off	Off	On	Off	Off
150MHz	60MHz	2.5x	On	Off	Off	On	On	Off
166MHz	66MHz	2.5x	Off	Off	Off	On	On	Off
200MHz	66MHz	3x	Off	Off	Off	Off	On	Off

CPU VOLTAGE SELECTION					
Voltage	JV1	JV2	JV3	JV4	JV5
3.3v	Closed	Open	Closed	Closed	1 & 3, 2 & 4
3.5v	Closed	Closed	Closed	Closed	1 & 3, 2 & 4

Note: Pins designated should be in the closed position.

CPU VOLTAGE SELECTION						
Voltage	V core	JV1	JV2	JV3	JV4	JV5
3.3v	2.8v	Open	Open	Open	Closed	5 & 7, 6 & 8
3.3v	2.9v	Open	Open	Open	Closed	5 & 7, 6 & 8
3.3v	3.2v	Open	Open	Closed	Closed	5 & 7, 6 & 8

Note: Pins designated should be in the closed position.