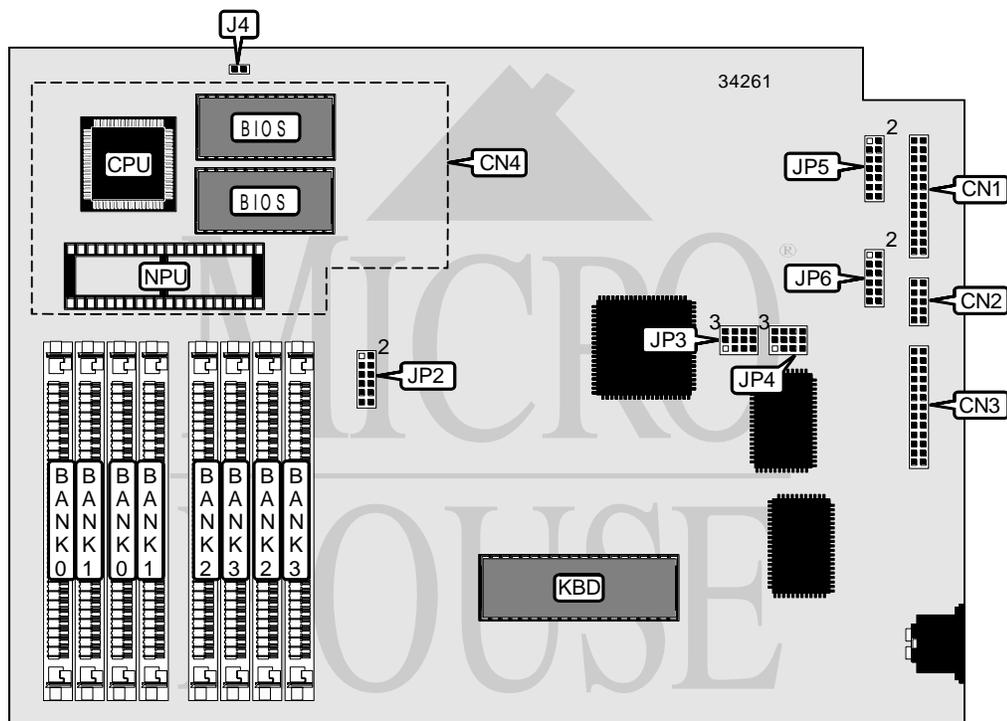


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STEP 386CX

Processor	80286/80386SX
Processor Speed	Unidentified
Chip Set	Unidentified
Video Chip Set	None
Maximum Onboard Memory	8MB
Maximum Video Memory	None
Cache	None
BIOS	Unidentified
Dimensions	254mm x 218mm
I/O Options	Parallel port, serial ports (2), Instep Board connector
NPU Options	80287/80387SX



CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 1	CN1	Parallel port	CN3
Serial port 2	CN2	Instep Board connector	CN4

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STEP 386CX

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USER CONFIGURABLE SETTINGS		
Function	Label	Position
IDE interface enabled	J4	Open
IDE interface disabled	J4	Closed

DRAM CONFIGURATION				
Size	Bank 0	Bank 1	Bank 2	Bank 3
512KB	(2) 256K x 9	None	None	None
1MB	(2) 256K x 9	(2) 256K x 9	None	None
2MB	(2) 1M x 9	None	None	None
2.5MB	(2) 256K x 9	(2) 1M x 9	None	None
2.5MB	(2) 1M x 9	(2) 256K x 9	None	None
4MB	(2) 1M x 9	(2) 1M x 9	None	None
6MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	None
8MB	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9	(2) 1M x 9

SERIAL PORT 1 INTERRUPT SELECTION	
IRQ	JP2
IRQ3	Pins 9 & 10 closed
í IRQ4	Pins 11 & 12 closed

SERIAL PORT 2 INTERRUPT SELECTION	
IRQ	JP2
í IRQ3	Pins 5 & 6 closed
IRQ4	Pins 7 & 8 closed

PARALLEL PORT INTERRUPT SELECTION	
IRQ	JP2
IRQ5	Pins 3 & 4 closed
í IRQ7	Pins 1 & 2 closed

HANDSHAKE SELECTION (PORT 1)	
Setting	JP3
Enabled	1 & 2, 4 & 5, 7 & 8, 10 & 11
Disabled	2 & 3, 5 & 6, 8 & 9, 11 & 12

Note: Pins designated should be in the closed position.

HANDSHAKE SELECTION (PORT 2)	
Setting	JP4
Enabled	1 & 2, 4 & 5, 7 & 8, 10 & 11
Disabled	2 & 3, 5 & 6, 8 & 9, 11 & 12

Note: Pins designated should be in the closed position.

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 STEP 386CX

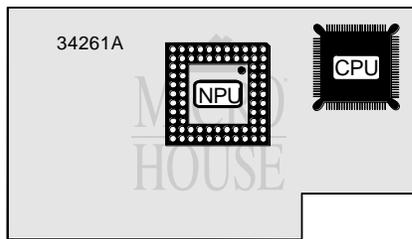
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DTE/DCE SELECTION (PORT 1)	
Setting	JP5
DTE	1 & 2, 3 & 4, 5 & 6, 7 & 8, 9 & 10, 11 & 12
DCE	1 & 3, 2 & 4, 5 & 7, 6 & 8, 9 & 11, 12 & 14

Note: Pins designated should be in the closed position.

DTE/DCE SELECTION (PORT 2)	
Setting	JP6
DTE	1 & 2, 3 & 4, 5 & 6, 7 & 8, 9 & 10, 11 & 12
DCE	1 & 3, 2 & 4, 5 & 7, 6 & 8, 9 & 11, 12 & 14

Note: Pins designated should be in the closed position.



80386SX INSTEP BOARD

MISCELLANEOUS TECHNICAL NOTE
 To upgrade to an 80386SX processor, install 80386SX Instep Board at CN4.