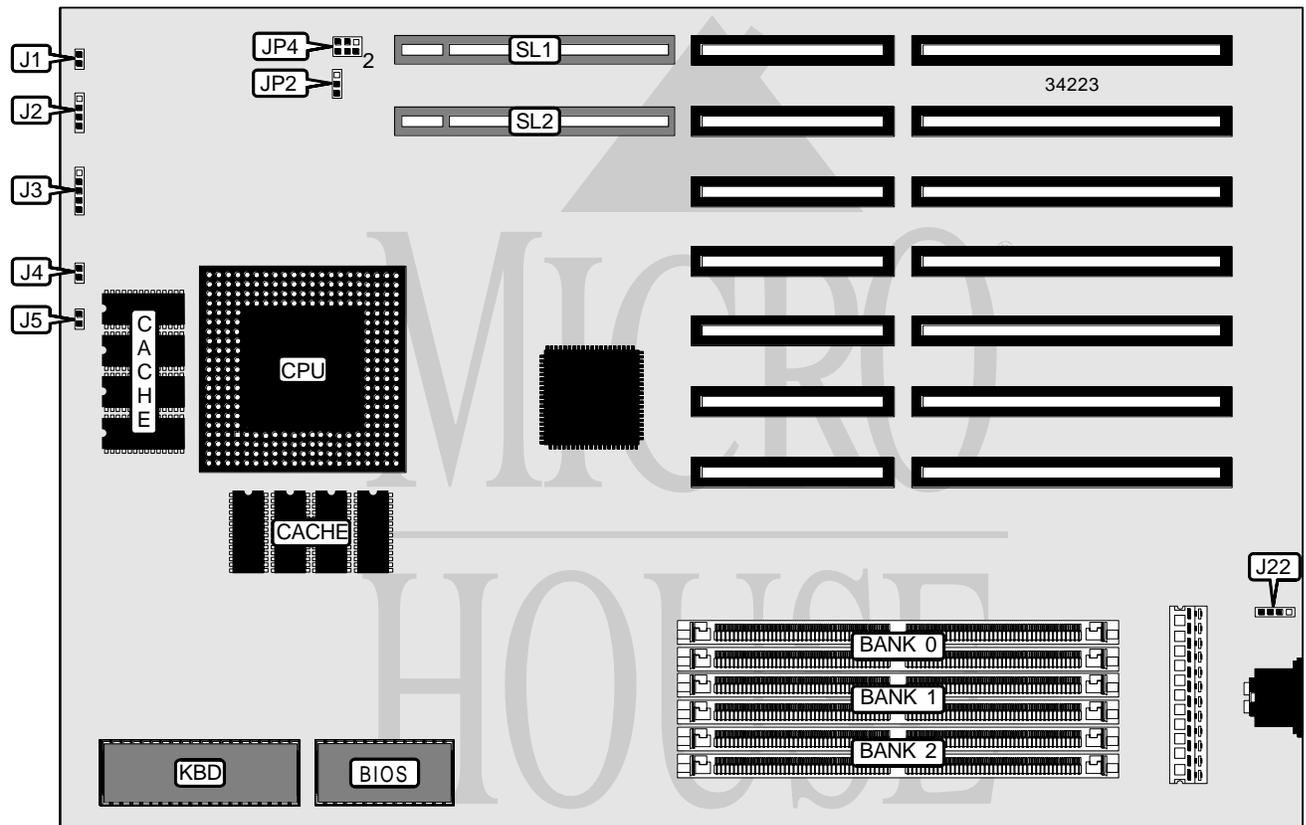


NEXGEN, INC.

NEXGEN VL

| | |
|-------------------------------|---------------------------------|
| Processor | NX586 |
| Processor Speed | 75/80/90/100MHz |
| Chip Set | Unidentified |
| Video Chip Set | None |
| Maximum Onboard Memory | 192MB |
| Maximum Video Memory | None |
| Cache | 256KB |
| BIOS | Unidentified |
| Dimensions | 330mm x 218mm |
| I/O Options | 32-bit VESA local bus slots (2) |
| NPU Options | None |



| CONNECTIONS | | | |
|---------------------|----------|-----------------------------|-----------|
| Purpose | Location | Purpose | Location |
| Reset switch | J1 | Turbo LED | J5 |
| Speaker | J2 | External battery | J22 |
| Power LED & keylock | J3 | 32-bit VESA local bus slots | SL1 & SL2 |
| Turbo switch | J4 | | |

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| USER CONFIGURABLE SETTINGS | | |
|---|----------------|-------------------|
| Function | Label | Position |
| í CMOS memory normal operation | JP2 | Pins 1 & 2 closed |
| CMOS memory clear | JP2 | Pins 2 & 3 closed |
| VL bus burst support read only enabled | JP4/pins 5 & 6 | Closed |
| VL bus burst support read only disabled | JP4/pins 5 & 6 | Open |

| DRAM CONFIGURATION | | | |
|--------------------|---------------|---------------|---------------|
| Size | Bank 0 | Bank 1 | Bank 2 |
| 2MB | (2) 256K x 36 | None | None |
| 4MB | (2) 256K x 36 | (2) 256K x 36 | None |
| 4MB | (2) 512K x 36 | None | None |
| 8MB | (2) 512K x 36 | (2) 512K x 36 | None |
| 10MB | (2) 1M x 36 | (2) 256K x 36 | None |
| 16MB | (2) 1M x 36 | (2) 1M x 36 | None |
| 16MB | (2) 2M x 36 | None | None |
| 20MB | (2) 2M x 36 | (2) 512K x 36 | None |
| 22MB | (2) 2M x 36 | (2) 512K x 36 | (2) 256K x 36 |
| 24MB | (2) 2M x 36 | (2) 1M x 36 | None |
| 24MB | (2) 1M x 36 | (2) 1M x 36 | (2) 1M x 36 |
| 32MB | (2) 4M x 36 | None | None |
| 32MB | (2) 2M x 36 | (2) 2M x 36 | None |
| 34MB | (2) 4M x 36 | (2) 256K x 36 | None |
| 38MB | (2) 4M x 36 | (2) 256K x 36 | (2) 512K x 36 |
| 40MB | (2) 4M x 36 | (2) 1M x 36 | None |
| 48MB | (2) 4M x 36 | (2) 2M x 36 | None |
| 48MB | (2) 4M x 36 | (2) 1M x 36 | (2) 1M x 36 |
| 56MB | (2) 4M x 36 | (2) 2M x 36 | (2) 1M x 36 |
| 64MB | (2) 8M x 36 | None | None |
| 64MB | (2) 4M x 36 | (2) 4M x 36 | None |
| 66MB | (2) 8M x 36 | (2) 256K x 36 | None |
| 76MB | (2) 8M x 36 | (2) 512K x 36 | (2) 1M x 36 |
| 80MB | (2) 8M x 36 | (2) 2M x 36 | None |
| 80MB | (2) 8M x 36 | (2) 1M x 36 | (2) 1M x 36 |
| 88MB | (2) 8M x 36 | (2) 2M x 36 | (2) 1M x 36 |
| 96MB | (2) 8M x 36 | (2) 4M x 36 | None |
| 96MB | (2) 4M x 36 | (2) 4M x 36 | (2) 4M x 36 |
| 96MB | (2) 8M x 36 | (2) 2M x 36 | (2) 2M x 36 |
| 104MB | (2) 8M x 36 | (2) 4M x 36 | (2) 1M x 36 |
| 112MB | (2) 8M x 36 | (2) 4M x 36 | (2) 2M x 36 |
| 128MB | (2) 8M x 36 | (2) 4M x 36 | (2) 4M x 36 |
| 128MB | (2) 8M x 36 | (2) 8M x 36 | None |
| 136MB | (2) 8M x 36 | (2) 8M x 36 | (2) 1M x 36 |
| 192MB | (2) 8M x 36 | (2) 8M x 36 | (2) 8M x 36 |

Note: Board also accepts x 32 SIMMs.

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| CACHE CONFIGURATION | |
|---|--|
| Note: 256KB cache is factory installed and is not configurable. | |

| VL BUS WAIT STATE SELECTION | |
|-----------------------------|----------------|
| Setting | JP4/pins 1 & 2 |
| 0 | Open |
| 1 | Closed |

| VL BUS SPEED SELECTION | |
|------------------------|----------------|
| Speed | JP4/pins 3 & 4 |
| <= 33MHz | Open |
| >33 MHz | Closed |