



EFA CORPORATION  
P5TVX-AT

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CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 1	CN1	Green PC connector	J10
Serial port 2	CN2	IDE interface LED	J11
Parallel port	CN3	Speaker	J12
Floppy drive interface	CN4	Power LED & keylock	J14
IDE interface 1	CN5	Reset switch	J16
IDE interface 2	CN6	Turbo LED	J17
USB connector	J2	External battery	JP3
USB connector	J3	Turbo switch	JP18
PS/2 mouse interface	J4	32-bit PCI slots	PC1 – PC4
IR connector	J9	Cache slot	SL1

USER CONFIGURABLE SETTINGS		
Function	Label	Position
í I/O chip M5113 enabled	JP5	Pins 1 & 2 closed
I/O chip M5113 disabled	JP5	Pins 2 & 3 closed
í AT bus clock select /4	JP8	Closed
AT bus clock select /3	JP8	Open
í Monitor type select color	JP10	Closed
Monitor type select monochrome	JP10	Open
í Mouse type select PS/2	JP11	Closed
Mouse type select AT	JP11	Open
í Factory configured – do not alter	JP15	Unidentified
í Flash ROM select 1MB	JP20	Pins 1 & 2 closed
Flash ROM select 2MB	JP20	Pins 2 & 3 closed
í Flash BIOS voltage select 5v	JP21	Pins 1 & 2 closed
Flash BIOS voltage select 12v	JP21	Pins 2 & 3 closed
í CMOS memory normal operation	JP24	Open
CMOS memory clear	JP24	Closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None

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DRAM CONFIGURATION (CON'T)		
Size	Bank 0	Bank 1
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36

Note: Board accepts EDO memory. Board also accepts x 32 SIMMs. Do not install SIMMs & DIMMs at the same time.

DIMM CONFIGURATION	
Size	Bank 2
8MB	(1) 1M x 64
16MB	(1) 2M x 64
32MB	(1) 4M x 64

CACHE CONFIGURATION			
Size	Bank 0	SL1	TAG
256KB	(2) 32K x 32	Not installed	Unidentified
512KB	(2) 32K x 32	256KB module installed	Unidentified

CPU SPEED SELECTION (CYRIX)					
CPU speed	Clock speed	Multiplier	JP3	JP6	JP7
120MHz	50MHz	2x	3 & 5, 4 & 6	2 & 3	1 & 2
133MHz	55MHz	2x	1 & 3, 2 & 4	2 & 3	1 & 2
150MHz	60MHz	2x	2 & 4, 3 & 5	2 & 3	1 & 2
166MHz	66MHz	2x	1 & 3, 4 & 6	2 & 3	1 & 2

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (AMD)					
CPU speed	Clock speed	Multiplier	JP3	JP6	JP7
75MHz	50MHz	1.5x	3 & 5, 4 & 6	2 & 3	2 & 3
90MHz	60MHz	1.5x	2 & 4, 3 & 5	2 & 3	2 & 3
100MHz	66MHz	1.5x	1 & 3, 4 & 6	2 & 3	2 & 3
120MHz	60MHz	1.5x	2 & 4, 3 & 5	2 & 3	2 & 3
133MHz	66MHz	1.5x	1 & 3, 4 & 6	2 & 3	2 & 3
150MHz	60MHz	2x	2 & 4, 3 & 5	2 & 3	1 & 2
166MHz	66MHz	2x	1 & 3, 4 & 6	2 & 3	1 & 2

Note: Pins designated should be in the closed position.

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CPU SPEED SELECTION (INTEL)					
CPU speed	Clock speed	Multiplier	JP3	JP6	JP7
75MHz	50MHz	1.5x	3 & 5, 4 & 6	2 & 3	2 & 3
90MHz	60MHz	1.5x	2 & 4, 3 & 5	2 & 3	2 & 3
100MHz	66MHz	1.5x	1 & 3, 4 & 6	2 & 3	2 & 3
120MHz	60MHz	2x	2 & 4, 3 & 5	2 & 3	1 & 2
133MHz	66MHz	2x	1 & 3, 4 & 6	2 & 3	1 & 2
150MHz	60MHz	2.5x	2 & 4, 3 & 5	1 & 2	1 & 2
166MHz	66MHz	2.5x	1 & 3, 4 & 6	1 & 2	1 & 2
200MHz	66MHz	3x	1 & 3, 4 & 6	1 & 2	2 & 3

Note: Pins designated should be in the closed position.

CPU TYPE SELECTION	
Type	JP23
Cyrix	Pins 3 & 5, 4 & 6 closed
AMD	Pins 3 & 5, 4 & 6 closed
P54C	Pins 3 & 5, 4 & 6 closed
CX6X86L	Pins 1 & 3, 2 & 4 closed
P55C	Pins 1 & 3, 2 & 4 closed

CPU VOLTAGE SELECTION		
Voltage	V core	JP12
3.3v	2.5v	Pins 1 & 2, 7 & 8 closed
3.3v	2.7v	Pins 3 & 4, 7 & 8 closed
3.3v	2.8v	Pins 5 & 6, 7 & 8 closed
3.52v	2.5v	Pins 1 & 2, 9 & 10 closed
3.52v	2.7v	Pins 3 & 4, 9 & 10 closed
3.52v	2.8v	Pins 5 & 6, 9 & 10 closed