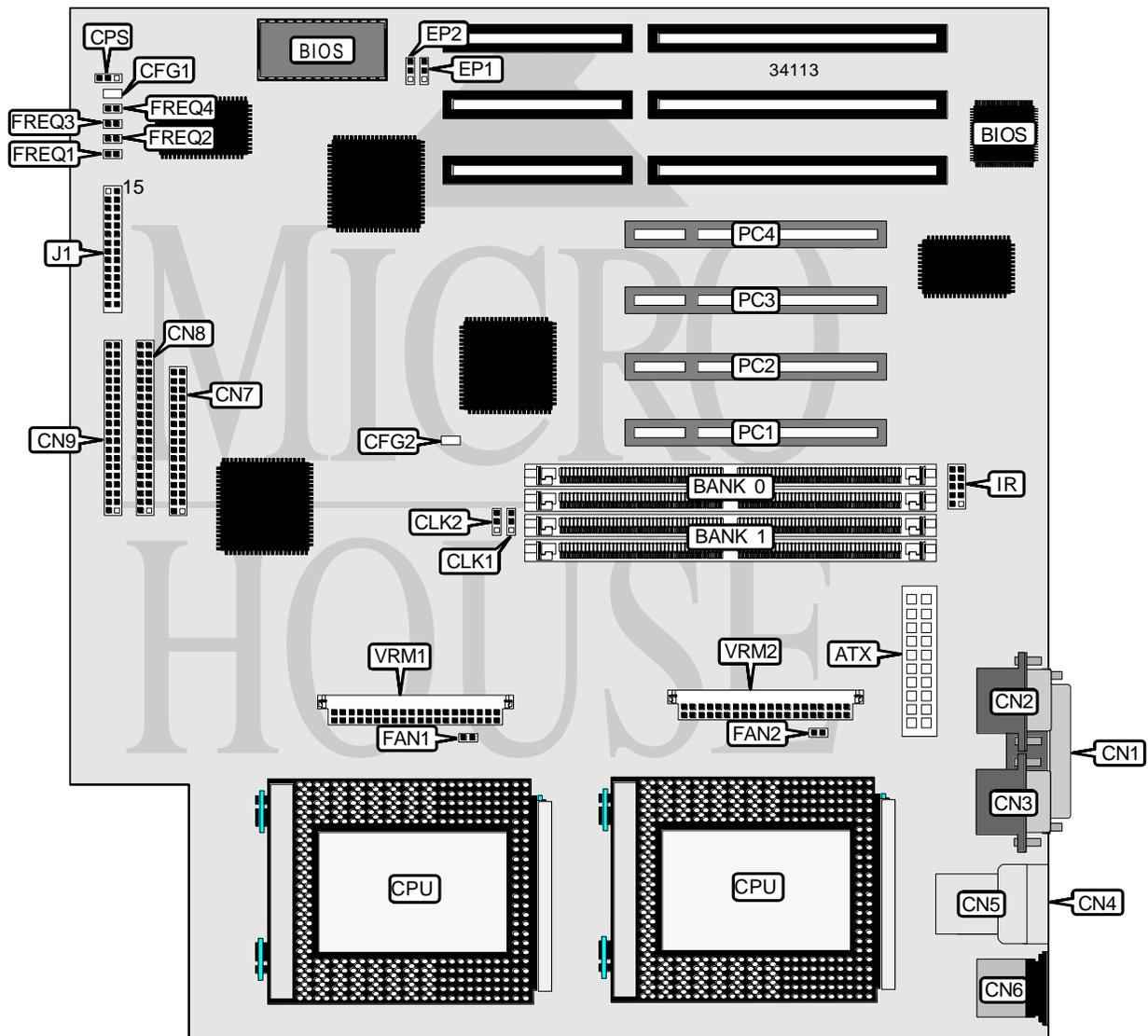


FIRST INTERNATIONAL COMPUTER, INC.

P N - 6 2 1 0

Processor	Pentium Pro
Processor Speed	150/166/180/200MHz
Chip Set	Intel
Video Chip Set	None
Maximum Onboard Memory	512MB (EDO supported)
Maximum Video Memory	None
Cache	256/512KB (located on Pentium Pro CPU)
BIOS	AMI
Dimensions	305mm x 244mm
I/O Options	32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), IR connector, VRM connectors (2), USB connector, ATX power connector
NPU Options	None



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CONNECTIONS			
Purpose	Location	Purpose	Location
ATX power connector	ATX	IR connector	IR
Parallel port	CN1	Power LED & keylock	J1 pins 1 - 5
Serial port 2	CN2	Turbo LED	J1 pins 7 & 8
Serial port 1	CN3	Green PC connector	J1 pins 10 & 11
USB connector 1	CN4	Green PC LED	J1 pins 13 & 14
USB connector 2	CN5	Speaker	J1 pins 15 - 18
PS/2 mouse port	CN6	IDE interface LED	J1 pins 20 & 21
Floppy drive interface	CN7	Remote control power	J1 pins 23 & 24
IDE interface 1	CN8	Reset switch	J1 pins 27 & 28
IDE interface 2	CN9	32-bit PCI slots	PC1 – PC4
Chassis fan power	FAN1	VRM connector	VRM1
Chassis fan power	FAN2	VRM connector	VRM2

USER CONFIGURABLE SETTINGS		
Function	Label	Position
í Factory configured - do not alter	CFG1	Unidentified
í Factory configured - do not alter	CFG2	Unidentified
í Password disabled	CPS	Open
Password enabled	CPS	Closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36
128MB	(2) 16M x 36	None
256MB	(2) 16M x 36	(2) 16M x 36
512MB	(2) 32M x 36	(2) 32M x 36

Note: Board accepts EDO memory.

CACHE CONFIGURATION
Note: 256KB/512KB cache is located on the Pentium Pro CPU.

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CPU SPEED SELECTION					
CPU speed	Clock speed	Multiplier	CLK1	CLK2	FREQ1
150MHz	50MHz	3x	2 & 3	2 & 3	Closed
150MHz	60MHz	2.5x	2 & 3	1 & 2	Open
166MHz	66MHz	2.5x	1 & 2	2 & 3	Open
180MHz	60MHz	3x	2 & 3	1 & 2	Closed
200MHz	50MHz	4x	2 & 3	2 & 3	Closed
200MHz	66MHz	3x	1 & 2	2 & 3	Closed

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (CON'T)					
CPU speed	Clock speed	Multiplier	FREQ2	FREQ3	FREQ4
150MHz	50MHz	3x	Open	Closed	Closed
150MHz	60MHz	2.5x	Closed	Closed	Closed
166MHz	66MHz	2.5x	Closed	Closed	Closed
180MHz	60MHz	3x	Open	Closed	Closed
200MHz	50MHz	4x	Closed	Open	Closed
200MHz	66MHz	3x	Open	Closed	Closed

Note: Pins designated should be in the closed position.

FLASH BIOS SELECTION (1MB)		
Setting	EP1	EP2
AMD	Open	Pins 2 & 3 closed
ATMEL	Pins 2 & 3 closed	Open
Intel/MXIC	Pins 1 & 2 closed	Pins 1 & 2 closed
í SST	Pins 2 & 3 closed	Open

FLASH BIOS SELECTION (2MB)		
Setting	EP1	EP2
AMD	Open	Pins 2 & 3 closed
Intel/MXIC	Pins 1 & 2 closed	Open
ATMEL	Open	Open
í SST	Open	Open