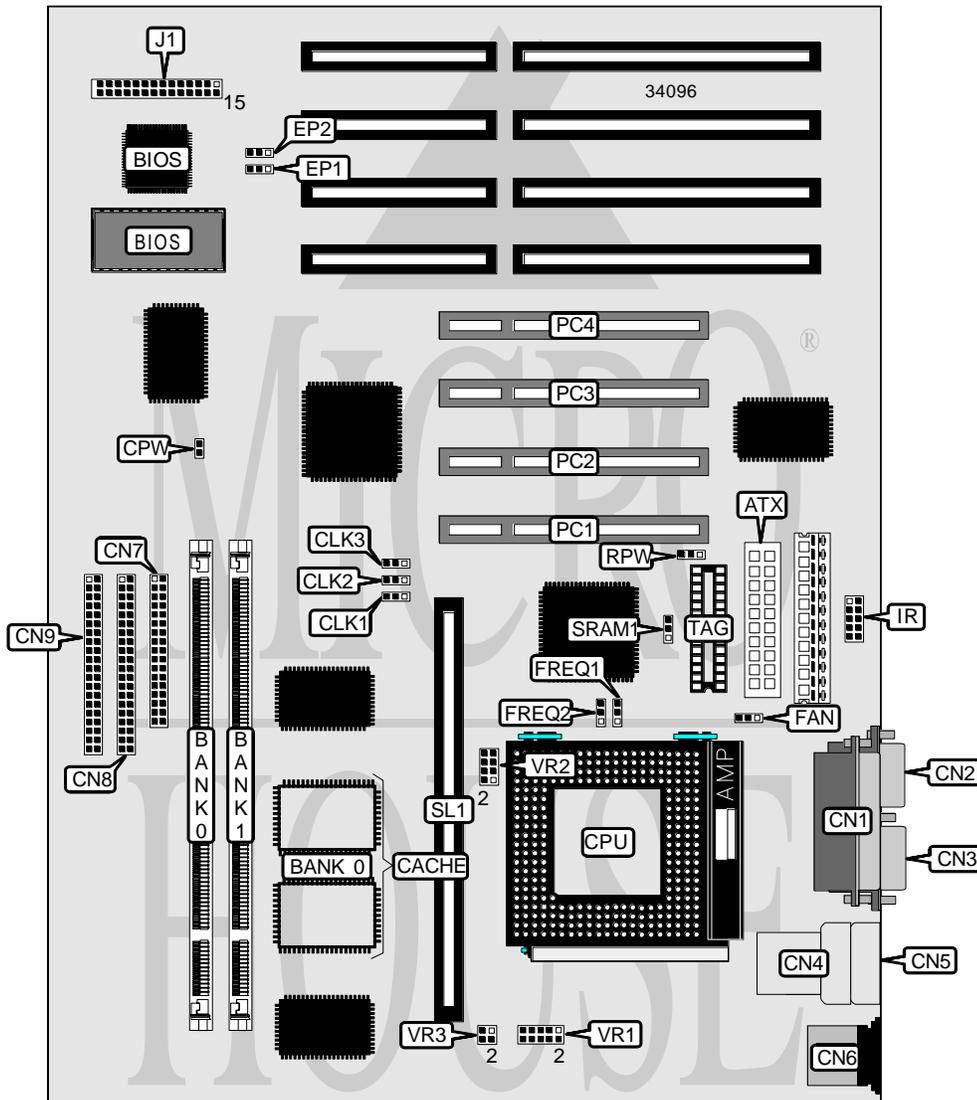


FIRST INTERNATIONAL COMPUTER, INC. PT-2010

| | |
|-------------------------------|---|
| Processor | CX M1/IBM/AM K5/Pentium |
| Processor Speed | 75/90/100/120/133/150/166/180/200MHz |
| Chip Set | Intel |
| Video Chip Set | None |
| Maximum Onboard Memory | 128MB (EDO supported) |
| Maximum Video Memory | None |
| Cache | 256/512KB |
| BIOS | Unidentified |
| Dimensions | 305mm x 244mm |
| I/O Options | 32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), cache slot, IR connector, USB connectors (2), ATX power connector |
| NPU Options | None |



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| CONNECTIONS | | | |
|------------------------|----------|-----------------------------|-----------------|
| Purpose | Location | Purpose | Location |
| ATX power connector | ATX | Power LED & keylock | J1 pins 1 - 5 |
| Parallel port | CN1 | Turbo LED | J1 pins 7 & 8 |
| Serial port 2 | CN2 | Green PC connector | J1 pins 10 & 11 |
| Serial port 1 | CN3 | Green PC LED | J1 pins 13 & 14 |
| USB connector 1 | CN4 | Speaker | J1 pins 15 - 18 |
| USB connector 2 | CN5 | IDE interface LED | J1 pins 20 & 21 |
| PS/2 mouse port | CN6 | Remote control power | J1 pins 23 & 24 |
| Floppy drive interface | CN7 | Reset switch | J1 pins 27 & 28 |
| IDE interface 2 | CN8 | 32-bit PCI slots | PC1 – PC4 |
| IDE interface 1 | CN9 | Remote control power switch | RPW |
| Chassis fan power | FAN | Cache slot | SL1 |
| IR connector | IR | | |

| USER CONFIGURABLE SETTINGS | | |
|-------------------------------|-------|--------------------------|
| Function | Label | Position |
| í Password disabled | CPW | Open |
| Password enabled | CPW | Closed |
| í Power supply type select AT | VR3 | Pins 1 & 2, 3 & 4 closed |
| Power supply type select ATX | VR3 | Open |

| DIMM CONFIGURATION | | |
|--------------------|-------------|-------------|
| Size | Bank 0 | Bank 1 |
| 8MB | (1) 1M x 64 | None |
| 16MB | (1) 2M x 64 | None |
| 16MB | (1) 1M x 64 | (1) 1M x 64 |
| 24MB | (1) 1M x 64 | (1) 2M x 64 |
| 24MB | (1) 3M x 64 | None |
| 32MB | (1) 4M x 64 | None |
| 32MB | (1) 2M x 64 | (1) 2M x 64 |
| 32MB | (1) 1M x 64 | (1) 3M x 64 |
| 40MB | (1) 1M x 64 | (1) 4M x 64 |
| 40MB | (1) 2M x 64 | (1) 3M x 64 |
| 48MB | (1) 2M x 64 | (1) 4M x 64 |
| 48MB | (1) 3M x 64 | (1) 3M x 64 |
| 64MB | (1) 4M x 64 | (1) 4M x 64 |
| 72MB | (1) 8M x 64 | (1) 1M x 64 |
| 80MB | (1) 8M x 64 | (1) 2M x 64 |
| 96MB | (1) 8M x 64 | (1) 4M x 64 |
| 128MB | (1) 8M x 64 | (1) 8M x 64 |

Note: Board accepts EDO memory.

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| CACHE CONFIGURATION | | | |
|---------------------|--------------|------------------------|-----------------|
| Size | Bank 0 | SL1 | TAG |
| 256KB (A) | (2) 32K x 32 | Not installed | (1) 16K/32K x 8 |
| 256KB (B) | None | 256KB module installed | None |
| 512KB (A) | (2) 64K x 32 | Not installed | (1) 16K/32K x 8 |
| 512KB (B) | (2) 32K x 32 | 256KB module installed | (1) 16K/32K x 8 |
| 512KB (C) | None | 512KB module installed | None |

| CACHE JUMPER CONFIGURATION | |
|----------------------------|-------------------|
| Size | SRAM1 |
| 256KB (A) | Pins 1 & 2 closed |
| 256KB (B) | Pins 1 & 2 closed |
| 512KB (A) | Pins 2 & 3 closed |
| 512KB (B) | Pins 2 & 3 closed |
| 512KB (C) | Pins 2 & 3 closed |

| CPU SPEED SELECTION (CYRIX) | | | | | | | |
|-----------------------------|-------------|------------|-------|-------|-------|-------|-------|
| CPU speed | Clock speed | Multiplier | CLK1 | CLK2 | CLK3 | FREQ1 | FREQ2 |
| 120MHz | 50MHz | 2x | 2 & 3 | 2 & 3 | 2 & 3 | 2 & 3 | 1 & 2 |
| 133MHz | 55MHz | 2x | 2 & 3 | 2 & 3 | 1 & 2 | 2 & 3 | 1 & 2 |
| 150MHz | 60MHz | 2x | 1 & 2 | 2 & 3 | 1 & 2 | 2 & 3 | 1 & 2 |
| 166MHz | 66MHz | 2x | 2 & 3 | 1 & 2 | 2 & 3 | 2 & 3 | 1 & 2 |
| 150MHz (M2) | 60MHz | 2.5x | 1 & 2 | 2 & 3 | 2 & 3 | 2 & 3 | 2 & 3 |
| 166MHz (M2) | 66MHz | 2.5x | 2 & 3 | 1 & 2 | 2 & 3 | 2 & 3 | 2 & 3 |
| 180MHz (M2) | 60MHz | 3x | 1 & 2 | 2 & 3 | 2 & 3 | 1 & 2 | 2 & 3 |
| 200MHz (M2) | 66MHz | 3.5x | 2 & 3 | 1 & 2 | 2 & 3 | 1 & 2 | 1 & 2 |

Note: Pins designated should be in the closed position.

| CPU SPEED SELECTION (IBM) | | | | | | | |
|---------------------------|-------------|------------|-------|-------|-------|-------|-------|
| CPU speed | Clock speed | Multiplier | CLK1 | CLK2 | CLK3 | FREQ1 | FREQ2 |
| 120MHz | 50MHz | 2x | 2 & 3 | 2 & 3 | 2 & 3 | 2 & 3 | 1 & 2 |
| 133MHz | 55MHz | 2x | 2 & 3 | 2 & 3 | 1 & 2 | 2 & 3 | 1 & 2 |
| 150MHz | 60MHz | 2x | 1 & 2 | 2 & 3 | 1 & 2 | 2 & 3 | 1 & 2 |
| 166MHz | 66MHz | 2x | 2 & 3 | 1 & 2 | 2 & 3 | 2 & 3 | 1 & 2 |
| 150MHz (M2) | 60MHz | 2.5x | 1 & 2 | 2 & 3 | 2 & 3 | 2 & 3 | 2 & 3 |
| 166MHz (M2) | 66MHz | 2.5x | 2 & 3 | 1 & 2 | 2 & 3 | 2 & 3 | 2 & 3 |
| 180MHz (M2) | 60MHz | 3x | 1 & 2 | 2 & 3 | 2 & 3 | 1 & 2 | 2 & 3 |
| 200MHz (M2) | 66MHz | 3.5x | 2 & 3 | 1 & 2 | 2 & 3 | 1 & 2 | 1 & 2 |

Note: Pins designated should be in the closed position.

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| CPU SPEED SELECTION (AMD) | | | | | | | |
|---------------------------|-------------|------------|-------|-------|-------|-------|-------|
| CPU speed | Clock speed | Multiplier | CLK1 | CLK2 | CLK3 | FREQ1 | FREQ2 |
| 75MHz | 50MHz | 1.5x | 2 & 3 | 2 & 3 | 2 & 3 | 1 & 2 | 1 & 2 |
| 90MHz | 60MHz | 1.5x | 1 & 2 | 2 & 3 | 2 & 3 | 1 & 2 | 1 & 2 |
| 100MHz | 66MHz | 1.5x | 2 & 3 | 1 & 2 | 2 & 3 | 1 & 2 | 1 & 2 |
| 120MHz | 60MHz | 1.5x | 1 & 2 | 2 & 3 | 2 & 3 | 1 & 2 | 1 & 2 |
| 133MHz | 66MHz | 1.5x | 2 & 3 | 1 & 2 | 2 & 3 | 1 & 2 | 1 & 2 |
| 150MHz | 60MHz | 1.75x | 1 & 2 | 2 & 3 | 2 & 3 | 2 & 3 | 2 & 3 |
| 166MHz | 66MHz | 1.75x | 2 & 3 | 1 & 2 | 2 & 3 | 2 & 3 | 2 & 3 |
| 200MHz | 66MHz | 2x | 2 & 3 | 1 & 2 | 2 & 3 | 2 & 3 | 1 & 2 |

Note: Pins designated should be in the closed position.

| CPU SPEED SELECTION (INTEL) | | | | | | | |
|-----------------------------|-------------|------------|-------|-------|-------|-------|-------|
| CPU speed | Clock speed | Multiplier | CLK1 | CLK2 | CLK3 | FREQ1 | FREQ2 |
| 75MHz | 50MHz | 1.5x | 2 & 3 | 2 & 3 | 2 & 3 | 1 & 2 | 1 & 2 |
| 90MHz | 60MHz | 1.5x | 1 & 2 | 2 & 3 | 2 & 3 | 1 & 2 | 1 & 2 |
| 100MHz | 66MHz | 1.5x | 2 & 3 | 1 & 2 | 2 & 3 | 1 & 2 | 1 & 2 |
| 120MHz | 60MHz | 2x | 1 & 2 | 2 & 3 | 2 & 3 | 2 & 3 | 1 & 2 |
| 133MHz | 66MHz | 2x | 2 & 3 | 1 & 2 | 2 & 3 | 2 & 3 | 1 & 2 |
| 150MHz | 60MHz | 2.5x | 1 & 2 | 2 & 3 | 2 & 3 | 2 & 3 | 2 & 3 |
| 166MHz | 66MHz | 2.5x | 2 & 3 | 1 & 2 | 2 & 3 | 2 & 3 | 2 & 3 |
| 200MHz | 66MHz | 3x | 2 & 3 | 1 & 2 | 2 & 3 | 1 & 2 | 2 & 3 |

Note: Pins designated should be in the closed position.

| CPU VOLTAGE SELECTION (SINGLE) | | |
|--------------------------------|-------------------|--------------------------|
| Voltage | VR1 | VR2 |
| 3.3v | Pins 3 & 4 closed | Pins 1 & 2, 3 & 4 closed |
| 3.4v – 3.6v | Pins 1 & 2 closed | Pins 1 & 2, 3 & 4 closed |

| CPU VOLTAGE SELECTION (CYRIX, DUAL) | | | |
|-------------------------------------|--------|--------|--------------|
| Voltage | V core | VR1 | VR2 |
| 3.3v | 2.8v | 9 & 10 | 5 & 6, 7 & 8 |

Note: Pins designated should be in the closed position.

| CPU VOLTAGE SELECTION (IBM, DUAL) | | | |
|-----------------------------------|--------|--------|--------------|
| Voltage | V core | VR1 | VR2 |
| 3.3v | 2.8v | 9 & 10 | 5 & 6, 7 & 8 |

Note: Pins designated should be in the closed position.

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| CPU VOLTAGE SELECTION (AMD, DUAL) | | | |
|-----------------------------------|--------|--------|--------------|
| Voltage | V core | VR1 | VR2 |
| 3.3v | 2.5v | 9 & 10 | 5 & 6, 7 & 8 |
| 3.3v | 2.8v | 7 & 8 | 5 & 6, 7 & 8 |
| 3.3v | 2.9v | 5 & 6 | 5 & 6, 7 & 8 |

Note: Pins designated should be in the closed position.

| CPU VOLTAGE SELECTION (INTEL, DUAL) | | | |
|-------------------------------------|--------|-------|--------------|
| Voltage | V core | VR1 | VR2 |
| 3.3v | 2.8v | 7 & 8 | 5 & 6, 7 & 8 |

Note: Pins designated should be in the closed position.

| FLASH BIOS SELECTION | | |
|----------------------|-------------------|-------------------|
| Setting | EP1 | EP2 |
| AMTEL 29C010A | Pins 1 & 2 closed | Pins 2 & 3 closed |
| Intel 28F001 | Pins 1 & 2 closed | Pins 1 & 2 closed |
| MXIC 28F1000 | Pins 1 & 2 closed | Pins 2 & 3 closed |
| MXIC 28F2000 | Pins 2 & 3 closed | Pins 2 & 3 closed |
| SST 29EE010 | Pins 1 & 2 closed | Pins 2 & 3 closed |
| SST 29EE020 | Pins 2 & 3 closed | Pins 2 & 3 closed |