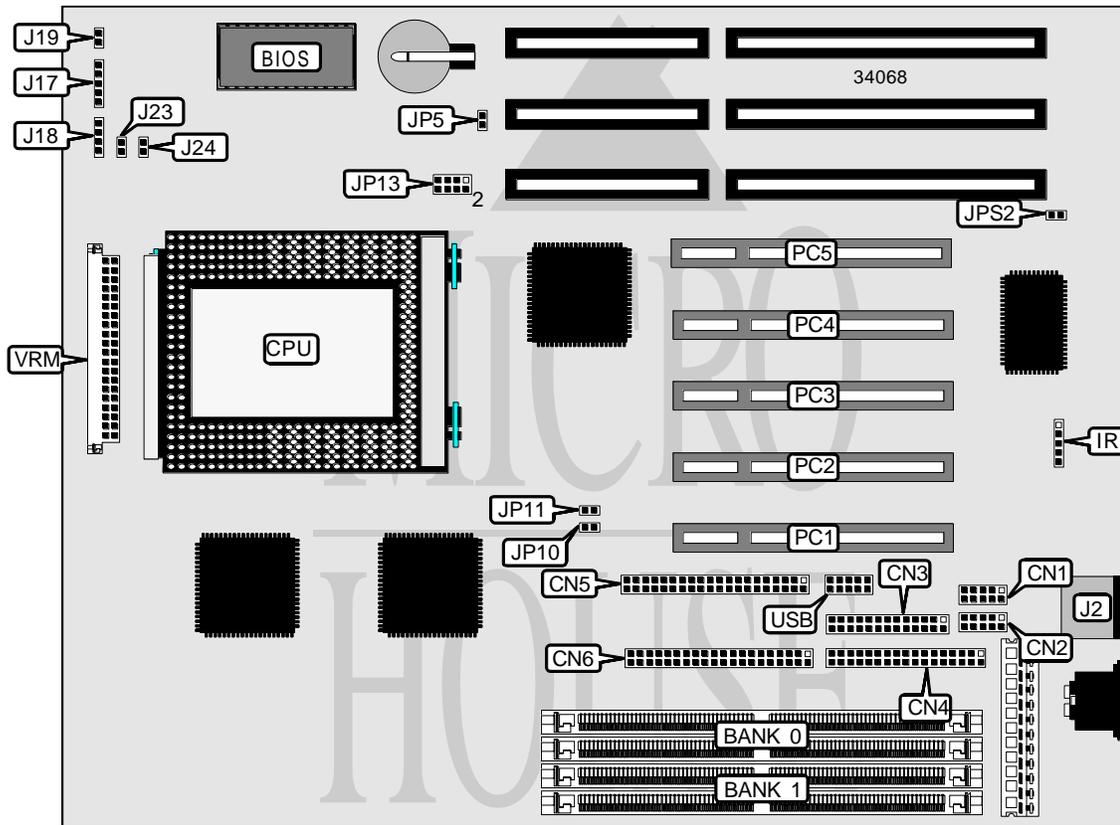


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6 F B

Processor	Pentium Pro
Processor Speed	150/166/180/200MHz
Chip Set	Intel
Video Chip Set	None
Maximum Onboard Memory	512MB (EDO supported)
Maximum Video Memory	None
Cache	256/512KB (located on Pentium Pro CPU)
BIOS	Award
Dimensions	254mm x 218mm
I/O Options	32-bit PCI slots (5), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), IR connector, VRM connector, USB connector
NPU Options	None



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6 F B

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CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 2	CN1	Power LED & keylock	J17
Serial port 1	CN2	Speaker	J18
Parallel port	CN3	Reset switch	J19
Floppy drive interface	CN4	Green PC connector	J23
IDE interface 1	CN5	IDE interface LED	J24
IDE interface 2	CN6	32-bit PCI slots	PC1 – PC5
IR connector	IR	USB connector	USB
PS/2 mouse port	J2	VRM connector	VRM

USER CONFIGURABLE SETTINGS		
Function	Label	Position
í CMOS memory normal operation	JP5	Open
CMOS memory clear	JP5	Closed
í PS/2 mouse disabled	JPS2	Open
PS/2 mouse enabled	JPS2	Closed

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36
128MB	(2) 16M x 36	None
136MB	(2) 16M x 36	(2) 1M x 36
144MB	(2) 16M x 36	(2) 2M x 36
160MB	(2) 16M x 36	(2) 4M x 36
192MB	(2) 16M x 36	(2) 8M x 36
256MB	(2) 16M x 36	(2) 16M x 36

Note: Board accepts EDO memory. Board also accepts x 32 SIMMs. Banks are interchangeable.

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6 F B

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CACHE CONFIGURATION

Note: 256KB/512KB cache is located on the Pentium Pro CPU.

CPU SPEED SELECTION

CPU speed	Clock speed	Multiplier	JP10	JP11	JP13
150MHz	60MHz	2.5x	Closed	Open	3 & 4, 5 & 6, 7 & 8
166MHz	66MHz	2.5x	Open	Closed	3 & 4, 5 & 6, 7 & 8
180MHz	60MHz	3x	Closed	Open	1 & 2, 5 & 6, 7 & 8
200MHz	66MHz	3x	Open	Closed	1 & 2, 5 & 6, 7 & 8

Note: Pins designated should be in the closed position.