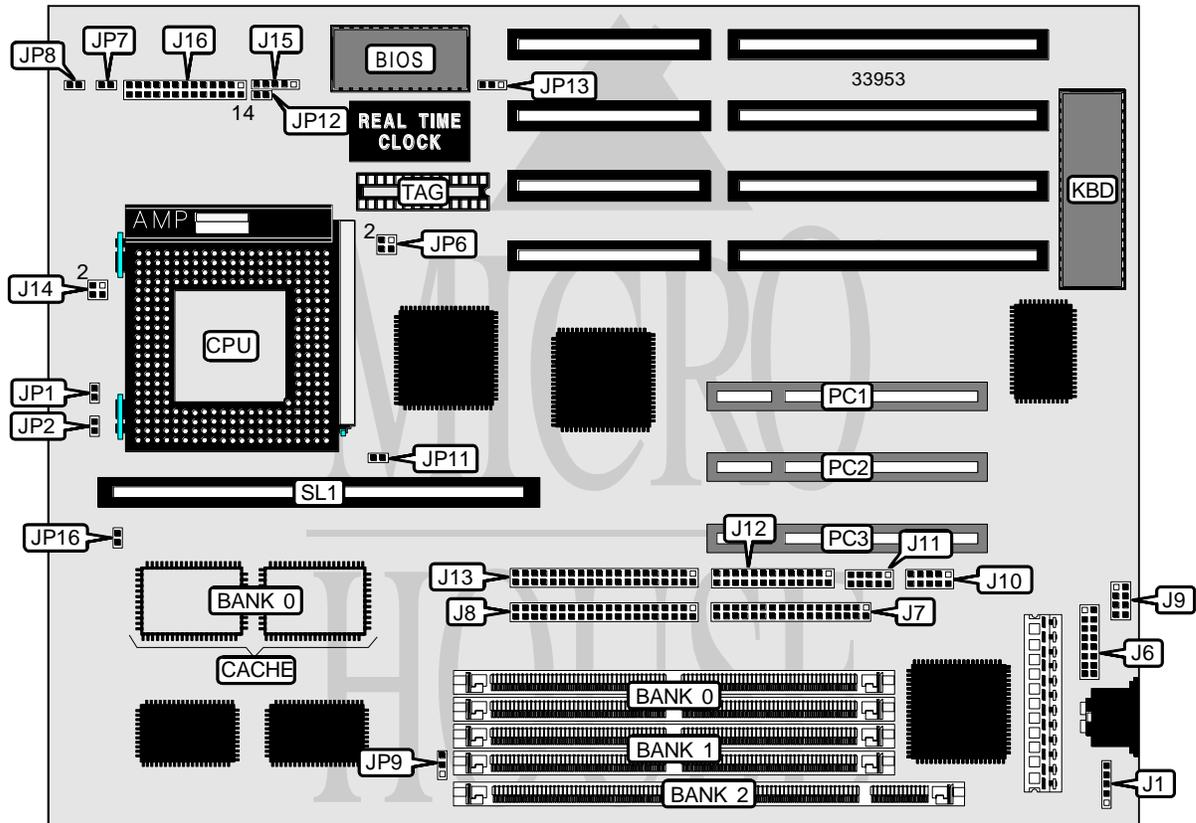


BIOSTAR MICROTECH INTERNATIONAL CORPORATION

MB-8500TVR

Processor	CX M1/AM K5/Pentium
Processor Speed	75/90/100/120/133/150/166/180/200MHz
Chip Set	Unidentified
Video Chip Set	None
Maximum Onboard Memory	128MB (EDO supported)
Maximum Video Memory	None
Cache	256/512KB
BIOS	AMI
Dimensions	285mm x 220mm
I/O Options	32-bit PCI slots (3), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse interface, serial ports (2), VGA interface, cache slot, IR connector, USB connector
NPU Options	None



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MB - 8500TVR

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CONNECTIONS			
Purpose	Location	Purpose	Location
PS/2 mouse interface	J1	IR connector (optional)	J15
VGA interface	J6	Speaker	J16 pins 1 - 4
Floppy drive interface	J7	Power LED & keylock	J16 pins 5 - 9
IDE interface 1	J8	Turbo LED	J16 pins 10 & 11
USB connector	J9	Reset switch	J16 pins 12 & 13
Serial port 1	J10	Green PC connector	J16 pins 17 & 18
Serial port 2	J11	IDE interface LED	J16 pins 20 & 21
Parallel port	J12	+5v ground	J16 pins 25 & 26
IDE interface 2	J13	32-bit PCI slots	PC1 – PC3
Chassis fan power	J14	Cache slot	SL1

USER CONFIGURABLE SETTINGS		
Function	Label	Position
? CMOS memory normal operation	JP12	Open
CMOS memory clear	JP12	Closed
Flash BIOS voltage select 12v	JP13	Pins 2 & 3 closed
Flash BIOS voltage select 5v	JP13	Pins 1 & 2 closed
? Factory configured - do not alter	JP16	Unidentified

DIMM/DRAM CONFIGURATION			
Size	Bank 0	Bank 1	Bank 2
8MB	None	None	(1) 1M x 64
8MB	(2) 1M x 36	None	None
16MB	None	None	(1) 2M x 64
16MB	(2) 2M x 36	None	None
16MB	(2) 1M x 36	(2) 1M x 36	None
24MB	(2) 2M x 36	(2) 1M x 36	None
32MB	None	None	(1) 4M x 64
32MB	(2) 4M x 36	None	None
32MB	(2) 2M x 36	(2) 2M x 36	None
40MB	(2) 4M x 36	(2) 1M x 36	None
48MB	(2) 4M x 36	(2) 2M x 36	None
64MB	None	None	(1) 8M x 64
64MB	(2) 8M x 36	None	None
64MB	(2) 4M x 36	(2) 4M x 36	None
72MB	(2) 8M x 36	(2) 1M x 36	None
80MB	(2) 8M x 36	(2) 2M x 36	None
96MB	(2) 8M x 36	(2) 4M x 36	None
128MB	(2) 8M x 36	(2) 8M x 36	None

Note: Board accepts EDO memory.

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DIMM VOLTAGE CONFIGURATION	
Voltage	JP9
3.3v	Pins 2 & 3 closed
5v	Pins 1 & 2 closed

CACHE CONFIGURATION			
Size	Bank 0	SL1	TAG
256KB (A)	(2) 32K x 32	Not installed	(1) 8K x 8
256KB (B)	None	256KB module installed	None
512KB (A)	(2) 32K x 32	256KB module installed	(1) 8K x 8
512KB (B)	(2) 64K x 32	Not installed	(1) 32K x 8
512KB (C)	None	512KB module installed	None

CACHE JUMPER CONFIGURATION	
Size	JP11
256KB (A)	Open
256KB (B)	Open
512KB (A)	Closed
512KB (B)	Closed
512KB (C)	Closed

CPU SPEED SELECTION (CYRIX)					
CPU speed	Clock speed	Multiplier	JP1	JP2	JP6
120MHz	50MHz	2x	Closed	Open	1 & 2, 3 & 4
133MHz	55MHz	2x	Closed	Open	Open
150MHz	60MHz	2x	Closed	Open	1 & 2
166MHz	66MHz	2x	Closed	Open	3 & 4

Note: Pins designated should be in the closed position.

CPU SPEED SELECTION (AMD)					
CPU speed	Clock speed	Multiplier	JP1	JP2	JP6
75MHz	50MHz	1.5x	Open	Open	1 & 2, 3 & 4
90MHz	60MHz	1.5x	Open	Open	1 & 2
100MHz	66MHz	1.5x	Open	Open	3 & 4
120MHz	60MHz	1.5x	Open	Open	1 & 2
133MHz	66MHz	1.5x	Open	Open	3 & 4
150MHz	60MHz	2x	Closed	Open	1 & 2
166MHz	66MHz	2x	Closed	Open	3 & 4

Note: Pins designated should be in the closed position.

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CPU SPEED SELECTION (INTEL)					
CPU speed	Clock speed	Multiplier	JP1	JP2	JP6
75MHz	50MHz	1.5x	Open	Open	1 & 2, 3 & 4
90MHz	60MHz	1.5x	Open	Open	1 & 2
100MHz	66MHz	1.5x	Open	Open	3 & 4
120MHz	60MHz	2x	Closed	Open	1 & 2
133MHz	66MHz	2x	Closed	Open	3 & 4
150MHz	60MHz	2.5x	Closed	Closed	1 & 2
166MHz	66MHz	2.5x	Closed	Closed	3 & 4
180MHz	60MHz	3x	Open	Closed	1 & 2
200MHz	66MHz	3x	Open	Closed	3 & 4

Note: Pins designated should be in the closed position.

CPU VOLTAGE SELECTION (SINGLE)	
Voltage	JP8
3.4v	Closed
3.5v	Open

CPU VOLTAGE SELECTION (DUAL)			
Voltage	V core	JP7	JP8
3.4v	2.6v	Closed	Closed
3.4v	2.9v	Open	Closed
3.5v	2.6v	Closed	Open
3.5v	2.9v	Open	Open