

CT CONTINENTAL CORPORATION

TC-VX1

... continued from previous page

CONNECTIONS			
Purpose	Location	Purpose	Location
Serial port 1	CN1	Reset switch	J10
Serial port 2	CN2	Power LED & keylock	J11
Parallel port	CN3	Speaker	J12
Floppy drive interface	CN4	USB connector 1	JP3
IDE interface 2	CN5	USB connector 2	JP4
IDE interface 1	CN6	Green PC connector	JP22
PS/2 mouse interface	J3	32-bit PCI slots	PC1 - PC4
IDE interface LED	J8	Cache slot	SL1
IR connector	J9		

USER CONFIGURABLE SETTINGS		
Function	Label	Position
í Factory configured - do not alter	JP11	Unidentified
Cache type select write through	JP17	Pins 2 & 3 closed
Cache type select write back	JP17	Pins 1 & 2 closed
Flash BIOS voltage select 12v	JP18	Pins 1 & 2 closed
Flash BIOS voltage select 5v	JP18	Pins 2 & 3 closed
í CMOS memory normal operation	JP19	Open
CMOS memory clear	JP19	Closed

DRAM/DIMM CONFIGURATION			
Size	Bank 0	Bank 1	Bank 2
8MB	None	None	(1) 1M x 64
8MB	(2) 1M x 36	None	None
16MB	(2) 2M x 36	None	None
16MB	(2) 1M x 36	(2) 1M x 36	None
16MB	None	None	(1) 2M x 64
24MB	(2) 1M x 36	(2) 2M x 36	None
24MB	(2) 1M x 36	(2) 2M x 36	None
32MB	None	None	(1) 4M x 64
32MB	(2) 4M x 36	None	None
32MB	(2) 2M x 36	(2) 2M x 36	None
40MB	(2) 4M x 36	(2) 1M x 36	None
40MB	(2) 1M x 36	(2) 4M x 36	None
48MB	(2) 2M x 36	(2) 4M x 36	None
48MB	(2) 4M x 36	(2) 2M x 36	None
64MB	None	None	(1) 8M x 64
64MB	(2) 8M x 36	None	None
64MB	(2) 4M x 36	(2) 4M x 36	None
72MB	(2) 8M x 36	(2) 1M x 36	None
72MB	(2) 1M x 36	(2) 8M x 36	None
80MB	(2) 2M x 36	(2) 8M x 36	None

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CT CONTINENTAL CORPORATION

TC-VX1

... continued from previous page

DRAM/DIMM CONFIGURATION (CON'T)			
Size	Bank 0	Bank 1	Bank 2
80MB	(2) 8M x 36	(2) 2M x 36	None
96MB	(2) 8M x 36	(2) 4M x 36	None
96MB	(2) 4M x 36	(2) 8M x 36	None
128MB	(2) 8M x 36	(2) 8M x 36	None

Note: Board accepts EDO memory. Board also accepts x 32 SIMMs.

CACHE CONFIGURATION			
Size	Bank 0	SL1	TAG
256KB (A)	(2) 32K x 32	Not installed	(1) 8K x 8
512KB (A)	(2) 64K x 32	Not installed	(1) 32K x 8
256KB (B)	None	256KB module installed	None
512KB (B)	None	512KB module installed	None
512KB (C)	(2) 32K x 32	256KB module installed	(1) 8K x 8

Note: Module must be version 1.4 or above..

CACHE JUMPER CONFIGURATION		
Size	JP10	JP12
256KB (A)	Pins 2 & 3 closed	Pins 1 & 2 closed
512KB (A)	Pins 1 & 2 closed	Pins 1 & 2 closed
256KB (B)	Open	Pins 2 & 3 closed
512KB (B)	Open	Pins 2 & 3 closed
512KB (C)	Pins 1 & 2 closed	Pins 1 & 2 closed

CPU SPEED SELECTION (CYRIX)								
CPU speed	Clock speed	Multiplier	JP5	JP6	JP7	JP16	JP26	JP27
120MHz	50MHz	1.5x	Open	Closed	Closed	Closed	Open	Closed
133MHz	55MHz	1.5x	Open	Closed	Open	Closed	Open	Closed
150MHz	60MHz	1.5x	Closed	Open	Closed	Closed	Open	Closed
166MHz	66MHz	1.5x	Closed	Open	Open	Open	Open	Closed

CPU SPEED SELECTION (AMD)								
CPU speed	Clock speed	Multiplier	JP5	JP6	JP7	JP16	JP26	JP27
75MHz	66MHz	3x	Closed	Open	Open	Open	Closed	Open
75MHz	50MHz	1.5x	Open	Closed	Closed	Closed	Open	Open
90MHz	55MHz	1.5x	Open	Closed	Open	Closed	Open	Open
90MHz	60MHz	1.5x	Closed	Open	Closed	Closed	Open	Open
100MHz	66MHz	1.5x	Closed	Open	Open	Open	Open	Open

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CT CONTINENTAL CORPORATION

TC - VX 1

... continued from previous page

CPU SPEED SELECTION (INTEL)								
CPU speed	Clock speed	Multiplier	JP5	JP6	JP7	JP16	JP26	JP27
75MHz	50MHz	1.5x	Open	Closed	Closed	Closed	Open	Open
90MHz	60MHz	1.5x	Closed	Open	Closed	Closed	Open	Open
100MHz	66MHz	1.5x	Closed	Open	Open	Open	Open	Open
120MHz	60MHz	2x	Closed	Open	Closed	Closed	Open	Closed
133MHz	66MHz	2x	Closed	Open	Open	Open	Open	Closed
150MHz	60MHz	2.5x	Closed	Open	Closed	Closed	Closed	Closed
166MHz	66MHz	2.5x	Closed	Open	Open	Open	Closed	Closed
180MHz	60MHz	3x	Closed	Open	Closed	Closed	Closed	Open
200MHz	66MHz	3x	Closed	Open	Open	Open	Closed	Open

CPU VOLTAGE SELECTION (SINGLE)				
Voltage	JP20	JP21	JP24	JP28
3.3v	Open	1 & 3, 2 & 4	1 & 2	2 & 3
3.45v	Open	1 & 3, 2 & 4	2 & 3	2 & 3

Note: Pins designated should be in the closed position.

CPU VOLTAGE SELECTION (DUAL)					
Voltage	V core	JP20	JP21	JP24	JP28
3.3v	2.5	4 & 5	Open	1 & 2	1 & 2
3.45v	2.5	4 & 5	Open	2 & 3	1 & 2
3.3v	2.8	3 & 4	Open	1 & 2	1 & 2
3.45v	2.8	3 & 4	Open	2 & 3	1 & 2

Note: Pins designated should be in the closed position.