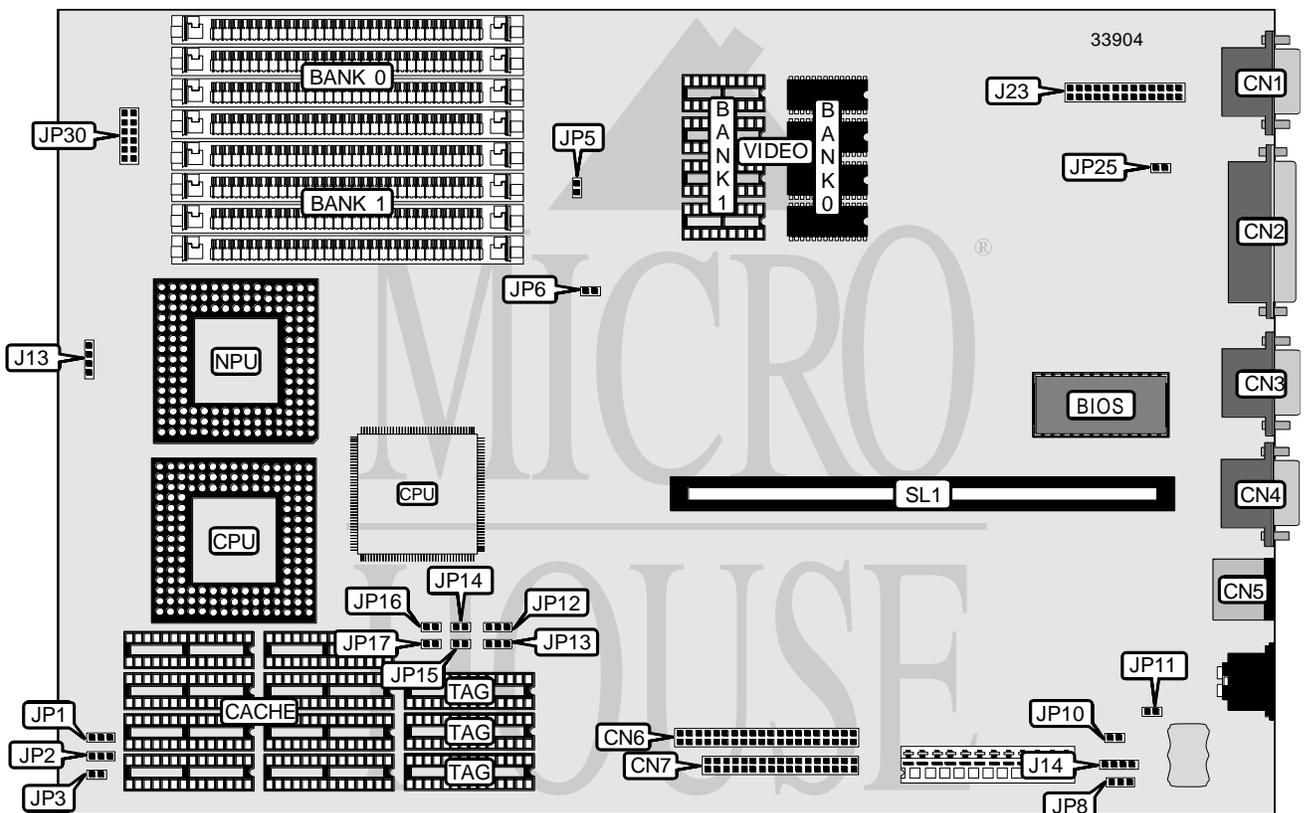


# SIEMENS NIXDORF INFORMATIONSSYSTEME AG SYSTEM BOARD PCD-4LSX, SYSTEM BOARD PCD-H

<b>Processor</b>	80486SX/80487SX/80486DX/80486DX2
<b>Processor Speed</b>	25/33/40/50(internal)/66(internal)MHz
<b>Chip Set</b>	Unidentified
<b>Video Chip Set</b>	Unidentified
<b>Maximum Onboard Memory</b>	32MB
<b>Maximum Video Memory</b>	2MB
<b>Cache</b>	64/128/256KB
<b>BIOS</b>	Unidentified
<b>Dimensions</b>	330mm x 218mm
<b>I/O Options</b>	Floppy drive interface, IDE interface, parallel port, PS/2 mouse port, serial ports (2), VGA feature connector, VGA port, riser slot
<b>NPU Options</b>	None



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# SIEMENS NIXDORF INFORMATIONSSYSTEME AG

## SYSTEM BOARD PCD-4LSX, SYSTEM BOARD PCD-H

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CONNECTIONS			
Purpose	Location	Purpose	Location
VGA port	CN1	Floppy drive interface	CN7
Parallel port	CN2	Speaker	J13
Serial port 2	CN3	External battery	J14
Serial port 1	CN4	VGA feature connector	J23
PS/2 mouse port	CN5	Front panel connector	JP30
IDE interface	CN6	Riser slot	SL1

USER CONFIGURABLE SETTINGS		
Function	Label	Position
í Onboard battery enabled	JP8	Pins 1 & 2 closed
Onboard battery disabled	JP8	Pins 2 & 3 closed
í Monitor type select color	JP10	Closed
Monitor type select monochrome	JP10	Open
í Mouse IRQ12 enabled	JP11	Closed
Mouse IRQ12 disabled	JP11	Open
í On board video enabled	JP25	Closed
On board video disabled	JP25	Open

DRAM CONFIGURATION		
Size	Bank 0	Bank 1
4MB	(4) 1M x 9	None
8MB	(4) 1M x 9	(4) 1M x 9
16MB	(4) 4M x 9	None
20MB	(4) 1M x 9	(4) 4M x 9
20MB	(4) 4M x 9	(4) 1M x 9
32MB	(4) 4M x 9	(4) 4M x 9

CACHE CONFIGURATION					
Size	Bank 0	Bank 1	TAG (U48)	TAG (U49)	TAG (U50)
64KB	(4) 8K x 8	(4) 8K x 8	(4) 8K x 8	None	(1) 16K x 8
128KB	(4) 32K x 8	None	(4) 8K x 8	None	(1) 16K x 8
256KB	(4) 32K x 8	(4) 32K x 8	(4) 8K x 8	(4) 8K x 8	(1) 16K x 8

Note: The location of banks 0 & 1 and the TAG are unidentified.

CACHE JUMPER CONFIGURATION						
Size	JP12	JP13	JP14	JP15	JP16	JP17
None	2 & 3	2 & 3	Open	Open	Open	Open
64KB	2 & 3	2 & 3	Open	Open	Open	Open
128KB	1 & 2	1 & 2	Closed	Open	Open	Closed
256KB	2 & 3	2 & 3	Closed	Closed	Closed	Closed

Note: Pins designated should be in the closed position.

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# SIEMENS NIXDORF INFORMATIONSSYSTEME AG

## SYSTEM BOARD PCD-4LSX, SYSTEM BOARD PCD-H

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VIDEO MEMORY CONFIGURATION		
Size	Bank 0	Bank 1
1MB	1MB	None
2MB	1MB	1MB

Note: The size of the video memory chips are unidentified.

CPU SPEED SELECTION		
Speed	JP5	JP6
25MHz	Closed	Open
33MHz	Open	Closed
50iMHz	Closed	Open
66iMHz	Open	Closed

CPU TYPE SELECTION			
Type	JP1	JP2	JP3
80486SX (PQFP)	Pins 1 & 2 closed	Pins 2 & 3 closed	Closed
80486SX (PGA)	Open	Pins 1 & 2 closed	Open
80486DX (PGA)	Pins 1 & 2 closed	Pins 2 & 3 closed	Closed
80486DX2 (PGA)	Pins 1 & 2 closed	Pins 2 & 3 closed	Closed