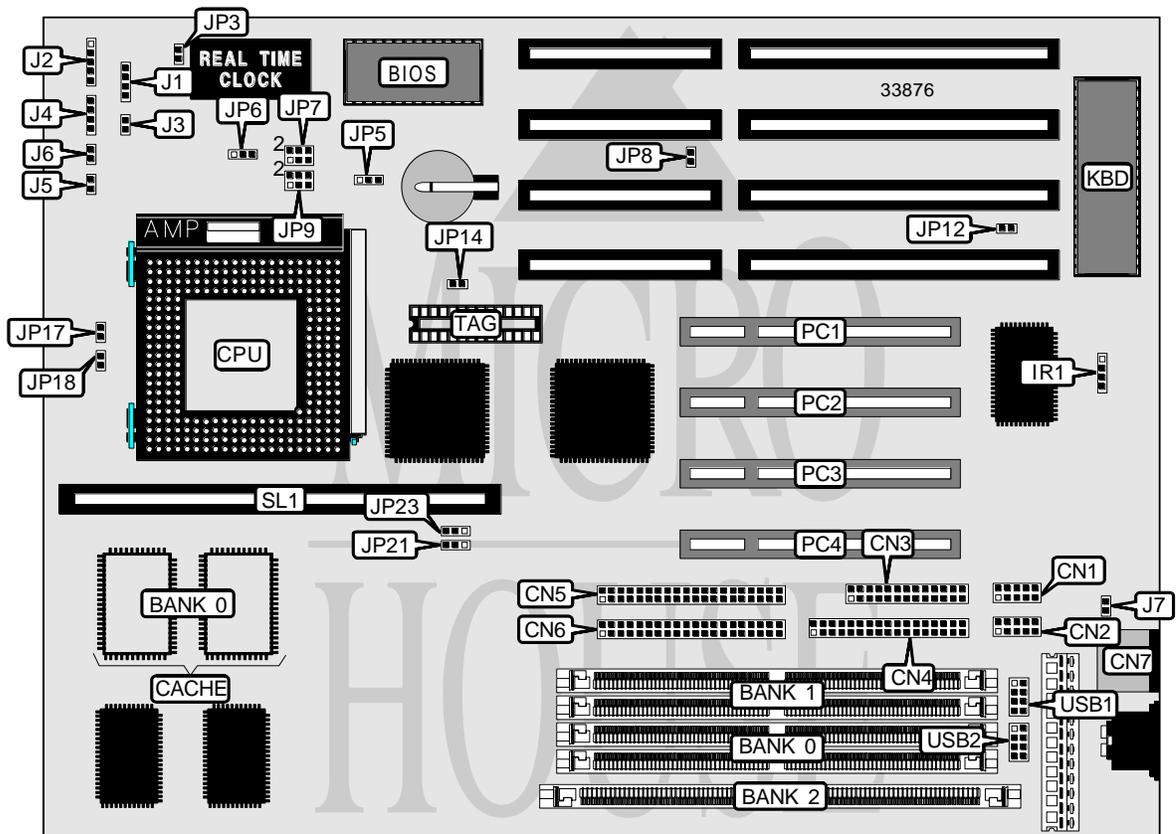


FLASH TECH, INC. SL-52D2

| | |
|-------------------------------|--|
| Processor | CX M1/AM K5/Pentium |
| Processor Speed | 75/90/100/120/133/150/166/180/200MHz |
| Chip Set | Intel |
| Video Chip Set | None |
| Maximum Onboard Memory | 128MB (EDO supported) |
| Maximum Video Memory | None |
| Cache | 256/512KB |
| BIOS | Award |
| Dimensions | 280mm x 220mm |
| I/O Options | 32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), cache slot, IR connector, USB connectors (2) |
| NPU Options | None |



Continued on next page. . .

FLASH TECH, INC.

SL-52D2

... continued from previous page

| CONNECTIONS | | | |
|------------------------|----------|---------------------|-----------|
| Purpose | Location | Purpose | Location |
| Serial port 2 | CN1 | Power LED & keylock | J2 |
| Serial port 1 | CN2 | Reset switch | J3 |
| Parallel port | CN3 | IDE interface LED | J4 |
| Floppy drive interface | CN4 | Turbo LED | J5 |
| IDE interface 2 | CN5 | Green PC connector | J6 |
| IDE interface 1 | CN6 | 32-bit PCI slots | PC1 - PC4 |
| PS/2 mouse port | CN7 | USB connector 1 | USB1 |
| IR connector | IR1 | USB connector 2 | USB2 |
| Speaker | J1 | Cache slot | SL1 |

| USER CONFIGURABLE SETTINGS | | |
|-------------------------------------|-------|----------|
| Function | Label | Position |
| í Factory configured - do not alter | J7 | Closed |
| í CMOS memory normal operation | JP3 | Open |
| CMOS memory clear | JP3 | Closed |
| í Monitor type select color | JP12 | Closed |
| Monitor type select monochrome | JP12 | Open |

| DRAM CONFIGURATION | | | |
|--------------------|-------------|-------------|-------------|
| Size | Bank 0 | Bank 1 | Bank 2 |
| 8MB | (2) 1M x 36 | None | None |
| 8MB | None | None | (1) 1M x 64 |
| 16MB | (2) 2M x 36 | None | None |
| 16MB | (2) 1M x 36 | (2) 1M x 36 | None |
| 16MB | None | (2) 1M x 36 | (1) 1M x 64 |
| 16MB | None | None | (1) 2M x 64 |
| 24MB | None | (2) 2M x 36 | (1) 1M x 64 |
| 24MB | (2) 2M x 36 | (2) 1M x 36 | None |
| 32MB | None | (2) 2M x 36 | (1) 2M x 64 |
| 32MB | None | None | (1) 4M x 64 |
| 32MB | (2) 4M x 36 | None | None |
| 32MB | (2) 2M x 36 | (2) 2M x 36 | None |
| 40MB | None | (2) 4M x 36 | (1) 1M x 64 |
| 40MB | (2) 1M x 36 | (2) 4M x 36 | None |
| 48MB | None | (2) 4M x 36 | (1) 2M x 64 |
| 48MB | (2) 4M x 36 | (2) 2M x 36 | None |
| 64MB | None | (2) 4M x 36 | (1) 4M x 64 |
| 64MB | None | None | (1) 8M x 64 |
| 64MB | (2) 8M x 36 | None | None |
| 64MB | (2) 4M x 36 | (2) 4M x 36 | None |
| 72MB | None | (2) 8M x 36 | (1) 1M x 64 |

Continued on next page. . .

FLASH TECH, INC.

SL-52D2

... continued from previous page

| DRAM CONFIGURATION (CON'T) | | | |
|----------------------------|-------------|-------------|-------------|
| Size | Bank 0 | Bank 1 | Bank 2 |
| 72MB | (2) 1M x 36 | (2) 8M x 36 | None |
| 80MB | None | (2) 8M x 36 | (1) 2M x 64 |
| 80MB | (2) 8M x 36 | (2) 2M x 36 | None |
| 96MB | None | (2) 8M x 36 | (1) 4M x 64 |
| 96MB | (2) 8M x 36 | (2) 4M x 36 | None |
| 128MB | None | (2) 8M x 36 | (1) 8M x 64 |
| 128MB | (2) 8M x 36 | (2) 8M x 36 | None |

Note: Board accepts EDO memory.

| CACHE CONFIGURATION | | | |
|---------------------|--------------|------------------------|--------------|
| Size | Bank 0 | SL1 | TAG |
| 256KB | (2) 32K x 32 | Not installed | Unidentified |
| 512KB | (2) 32K x 32 | 256KB module installed | Unidentified |

| CACHE JUMPER CONFIGURATION | |
|----------------------------|--------|
| Size | JP14 |
| 256KB | Open |
| 512KB | Closed |

| CPU SPEED SELECTION (CYRIX) | | | | | | | |
|-----------------------------|-------------|------------|--------|------|--------|-------|-------|
| CPU speed | Clock speed | Multiplier | JP8 | JP17 | JP18 | JP21 | JP23 |
| 120MHz | 50MHz | 2x | Open | Open | Closed | 1 & 2 | 1 & 2 |
| 133MHz | 55MHz | 2x | Open | Open | Closed | 2 & 3 | 1 & 2 |
| 150MHz | 60MHz | 2x | Closed | Open | Closed | 1 & 2 | 2 & 3 |
| 166MHz | 66MHz | 2x | Closed | Open | Closed | 2 & 3 | 2 & 3 |

Note: Pins designated should be in the closed position.

| CPU SPEED SELECTION (AMD) | | | | | | | |
|---------------------------|-------------|------------|--------|------|--------|-------|-------|
| CPU speed | Clock speed | Multiplier | JP8 | JP17 | JP18 | JP21 | JP23 |
| 75MHz | 50MHz | 1.5x | Open | Open | Open | 1 & 2 | 1 & 2 |
| 90MHz | 60MHz | 1.5x | Closed | Open | Open | 1 & 2 | 2 & 3 |
| 100MHz | 66MHz | 1.5x | Closed | Open | Open | 2 & 3 | 2 & 3 |
| 120MHz | 60MHz | 1.5x | Closed | Open | Open | 1 & 2 | 2 & 3 |
| 133MHz | 66MHz | 2x | Closed | Open | Open | 2 & 3 | 2 & 3 |
| 150MHz | 60MHz | 2x | Closed | Open | Closed | 1 & 2 | 2 & 3 |
| 166MHz | 66MHz | 2x | Closed | Open | Closed | 2 & 3 | 2 & 3 |

Note: Pins designated should be in the closed position.

Continued on next page. . .

FLASH TECH, INC.
SL-52D2

... continued from previous page

| CPU SPEED SELECTION (INTEL) | | | | | | | |
|-----------------------------|-------------|------------|--------|--------|--------|-------|-------|
| CPU speed | Clock speed | Multiplier | JP8 | JP17 | JP18 | JP21 | JP23 |
| 75MHz | 50MHz | 1.5x | Open | Open | Open | 1 & 2 | 1 & 2 |
| 90MHz | 60MHz | 1.5x | Closed | Open | Open | 1 & 2 | 2 & 3 |
| 100MHz | 66MHz | 1.5x | Closed | Open | Open | 2 & 3 | 2 & 3 |
| 120MHz | 60MHz | 2x | Closed | Open | Closed | 1 & 2 | 2 & 3 |
| 133MHz | 66MHz | 2x | Closed | Open | Closed | 2 & 3 | 2 & 3 |
| 150MHz | 60MHz | 2.5x | Closed | Closed | Closed | 1 & 2 | 2 & 3 |
| 166MHz | 66MHz | 2.5x | Closed | Closed | Closed | 2 & 3 | 2 & 3 |
| 180MHz | 60MHz | 3x | Closed | Closed | Open | 1 & 2 | 2 & 3 |
| 200MHz | 66MHz | 3x | Closed | Closed | Open | 2 & 3 | 2 & 3 |

Note: Pins designated should be in the closed position.

| CPU TYPE SELECTION | | | | |
|-----------------------|-------|-------|--------------|--------------|
| Type | JP5 | JP6 | JP7 | JP9 |
| CX M1 | 1 & 2 | 1 & 2 | 1 & 3, 2 & 4 | 1 & 3, 2 & 4 |
| CX M2 (2.5v) | 2 & 3 | 2 & 3 | 3 & 5, 4 & 6 | 3 & 5, 4 & 6 |
| CX M2 (2.8v) | 1 & 2 | 2 & 3 | 3 & 5, 4 & 6 | 3 & 5, 4 & 6 |
| AM K5 (<= 133MHz) | 1 & 2 | 1 & 2 | 1 & 3, 2 & 4 | 1 & 3, 2 & 4 |
| AM K5 (> 166MHz 2.5v) | 2 & 3 | 2 & 3 | 3 & 5, 4 & 6 | 3 & 5, 4 & 6 |
| AM K5 (> 166MHz 2.8v) | 1 & 2 | 2 & 3 | 3 & 5, 4 & 6 | 3 & 5, 4 & 6 |
| P54C/CTB | 1 & 2 | 1 & 2 | 1 & 3, 2 & 4 | 1 & 3, 2 & 4 |
| P55C (2.5v) | 2 & 3 | 2 & 3 | 3 & 5, 4 & 6 | 3 & 5, 4 & 6 |
| P55C (2.8v) | 1 & 2 | 2 & 3 | 3 & 5, 4 & 6 | 3 & 5, 4 & 6 |

Note: Pins designated should be in the closed position.