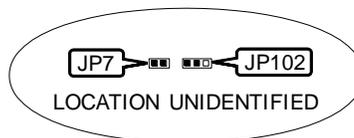
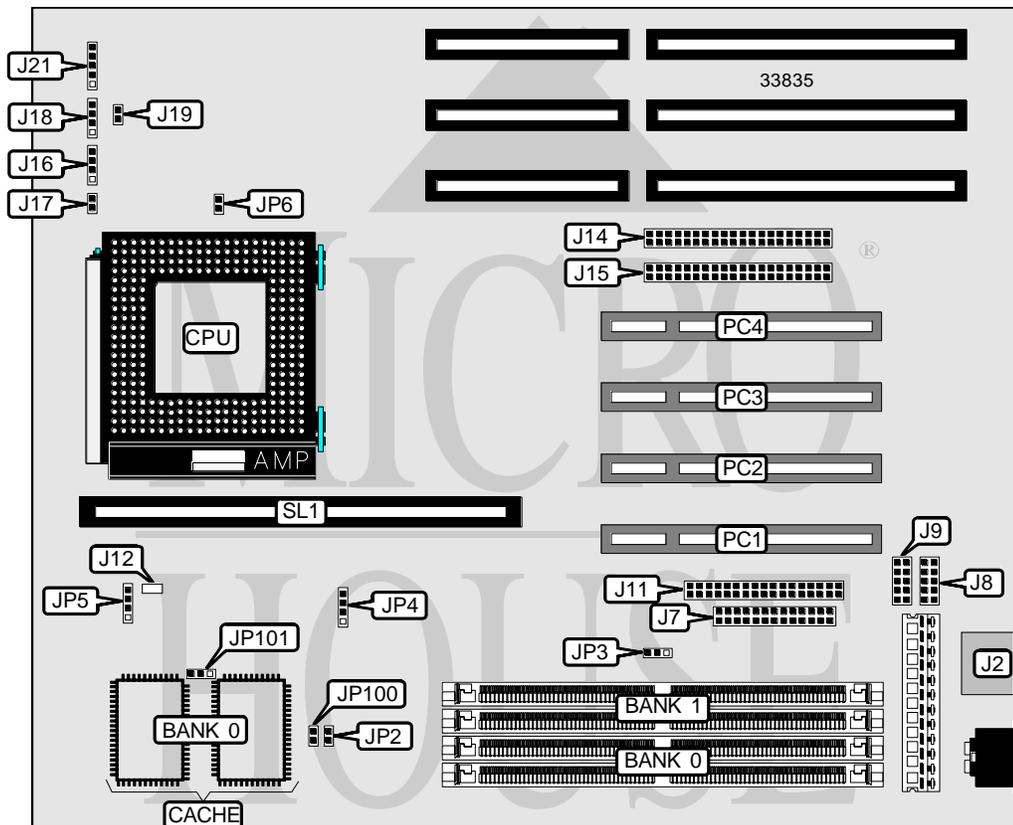


MITAC INTERNATIONAL CORPORATION

P B 5 4 0 0 T

| | |
|-------------------------------|--|
| Processor | Pentium |
| Processor Speed | 75/90/100/120/133/150/166MHz |
| Chip Set | Intel |
| Video Chip Set | None |
| Maximum Onboard Memory | 256MB (EDO supported) |
| Maximum Video Memory | None |
| Cache | 256/512KB |
| BIOS | Award |
| Dimensions | 254mm x 218mm |
| I/O Options | 32-bit PCI slots (4), floppy drive interface, green PC connector, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), cache slot |
| NPU Options | None |



Continued on next page . . .

MITAC INTERNATIONAL CORPORATION

P B 5 4 0 0 T

... continued from previous page

| CONNECTIONS | | | |
|------------------------|----------|---------------------|-----------|
| Purpose | Location | Purpose | Location |
| PS/2 mouse port | J2 | Speaker | J16 |
| Parallel port | J7 | Reset switch | J17 |
| Serial port 2 | J8 | IDE interface LED | J18 |
| Serial port 1 | J9 | Green PC connector | J19 |
| Floppy drive interface | J11 | Power LED & keylock | J21 |
| Chassis fan power | J12 | Cache slot | SL1 |
| IDE interface 1 | J14 | 32-bit PCI slots | PC1 - PC4 |
| IDE interface 2 | J15 | | |

| USER CONFIGURABLE SETTINGS | | |
|---------------------------------|-------|-------------------|
| Function | Label | Position |
| CMOS memory normal operation | JP3 | Pins 2 & 3 closed |
| CMOS memory clear | JP3 | Pins 1 & 2 closed |
| Voltage source select VCC2/VCC3 | JP6 | Open |
| Voltage source select VCC3 | JP6 | Closed |

| DRAM CONFIGURATION | | |
|--------------------|---------------|---------------|
| Size | Bank 0 | Bank 1 |
| 4MB | (2) 512K x 36 | None |
| 8MB | (2) 1M x 36 | None |
| 8MB | (2) 512K x 36 | (2) 512K x 36 |
| 16MB | (2) 2M x 36 | None |
| 16MB | (2) 1M x 36 | (2) 1M x 36 |
| 32MB | (2) 4M x 36 | None |
| 32MB | (2) 2M x 36 | (2) 2M x 36 |
| 64MB | (2) 8M x 36 | None |
| 64MB | (2) 4M x 36 | (2) 4M x 36 |
| 128MB | (2) 8M x 36 | (2) 8M x 36 |
| 128MB | (2) 16M x 36 | None |
| 256MB | (2) 16M x 36 | (2) 16M x 36 |

Note: Board accepts EDO memory. Board also accepts x 32 SIMMs. Banks are interchangeable.

| CACHE CONFIGURATION | | |
|---------------------|--------------|------------------------|
| Size | Bank 0 | SL1 |
| 256KB | (2) 32K x 32 | Not installed |
| 512KB | (2) 32K x 32 | 256KB module installed |
| 512KB | None | 512KB module installed |

| CACHE JUMPER CONFIGURATION | |
|----------------------------|--------|
| Size | JP100 |
| 256KB | Open |
| 512KB | Closed |

Continued on next page. . .

MITAC INTERNATIONAL CORPORATION

P B 5 4 0 0 T

... continued from previous page

| ON BOARD CACHE SIZE CONFIGURATION | |
|-----------------------------------|--------|
| Size | JP2 |
| 0KB | Open |
| 256KB | Closed |

| ON BOARD CACHE CONFIGURATION | |
|------------------------------|-------------------|
| Size | JP101 |
| Enabled | Pins 1 & 2 closed |
| Disabled | Pins 2 & 3 closed |

Note: If 512KB cache is installed at SL1, disable on board cache.

| CPU SPEED SELECTION | | | | |
|---------------------|-------------|------------|--------------|--------------|
| CPU speed | Clock speed | Multiplier | JP4 | JP5 |
| 75MHz | 50MHz | 1.5x | 1 & 2, 3 & 4 | Open |
| 90MHz | 60MHz | 1.5x | 3 & 4 | Open |
| 100MHz | 66MHz | 1.5x | 1 & 2 | Open |
| 120MHz | 60MHz | 2x | 3 & 4 | 1 & 2 |
| 133MHz | 66MHz | 2x | 1 & 2 | 1 & 2 |
| 150MHz | 60MHz | 2.5x | 3 & 4 | 3 & 4 |
| 166MHz | 66MHz | 2.5x | 1 & 2 | 1 & 2, 3 & 4 |

Note: Pins designated should be in the closed position.

| CPU TYPE SELECTION | |
|--------------------|-------------------|
| Type | JP102 |
| P54C | Pins 1 & 2 closed |
| P55C | Pins 2 & 3 closed |

| CPU VOLTAGE SELECTION | |
|-----------------------|--------|
| Voltage | JP7 |
| 3.3v (STD) | Open |
| 3.5v (VRE) | Closed |