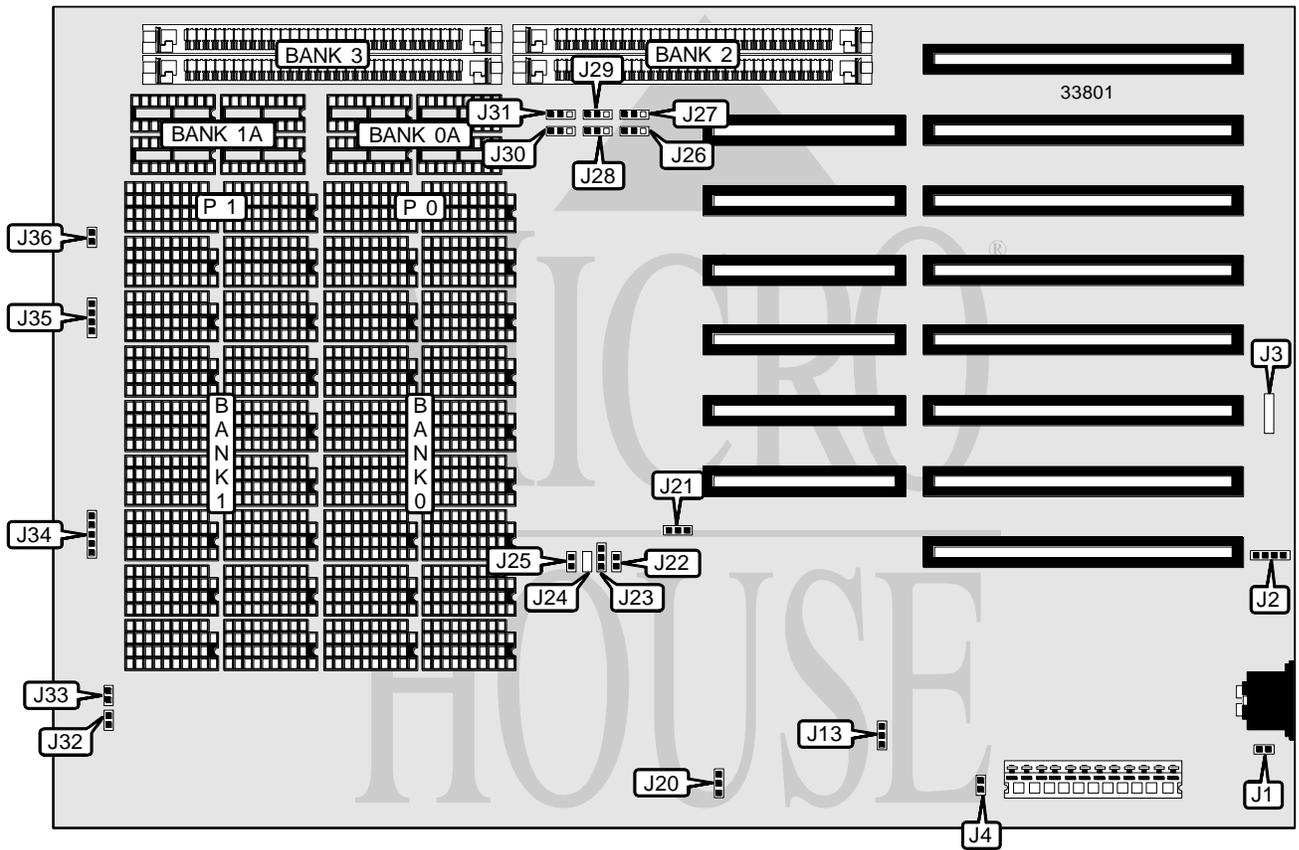


TAI HSIN GROUP 286/EMS

Processor	80286
Processor Speed	12/16MHz
Chip Set	G-2
Video Chip Set	None
Maximum Onboard Memory	8MB
Maximum Video Memory	None
Cache	None
BIOS	AMI/Award/Phoenix/Quadtel
Dimensions	324mm x 222mm
I/O Options	None
NPU Options	80287



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CONNECTIONS			
Purpose	Location	Purpose	Location
External battery	J2	Power LED & keylock	J34
Turbo switch	J32	Speaker	J35
Turbo LED	J33	Reset switch	J36

USER CONFIGURABLE SETTINGS		
Function	Label	Position
Monitor type select color	J1	Closed
Monitor type select monochrome	J1	Open
í Factory configured - do not alter (S/W speed select)	J3	Unidentified
Battery type select Dallas	J4	Closed
Battery type select 146818	J4	Open
Power good signal detect from power supply	J13	Pins 1 & 2 closed
Power good signal detect from board	J13	Pins 2 & 3 closed
NPU clock select PROCCLK	J20	Pins 1 & 2 closed
NPU clock select CLK-2	J20	Pins 2 & 3 closed
BIOS type select 27128	J21	Pins 1 & 2 closed
BIOS type select 27256	J21	Pins 2 & 3 closed
Delay line select external	J22	Closed
Delay line select internal	J22	Open
Chip type select 103	J23	Pins 1 & 2 closed
Chip type select 113	J23	Pins 2 & 3 closed
í Factory configured - do not alter	J24	Unidentified
Wait state select 0 wait states	J25	Closed
Wait state select 1 wait state	J25	Open
I/O speed select half speed	SW1/1	On
I/O speed select full speed	SW1/1	Off

DRAM CONFIGURATION						
Size	Bank 0	Bank 0A	Bank 1	Bank 1A	Bank 2	Bank 3
512KB (A)	(16) 41256	None	None	None	None	None
512KB (B)	None	(4) 44256	None	None	None	None
512KB	None	None	None	None	(2) 256K x 9	None
1MB (A)	(16) 41256	None	(16) 41256	None	None	None
1MB (B)	None	(4) 44256	(4) 44256	None	None	None
1MB (C)	None	None	None	None	(2) 256K x 9	(2) 256K x 9
2MB (A)	(16) 411000	None	None	None	None	None
2MB (B)	None	None	None	None	(2) 1M x 9	None
2MB (C)	(16) 41256	None	(16) 41256	None	(2) 256K x 9	(2) 256K x 9
2MB (D)	None	(4) 44256	(4) 44256	None	(2) 256K x 9	(2) 256K x 9
4MB (A)	(16) 411000	None	(16) 411000	None	None	None
4MB (B)	None	None	None	None	(2) 1M x 9	(2) 1M x 9
5MB (A)	(16) 41256	None	(16) 41256	None	(2) 1M x 9	(2) 1M x 9
5MB (B)	None	(4) 44256	(4) 44256	None	(2) 1M x 9	(2) 1M x 9
8MB	(16) 411000	None	(16) 411000	None	(2) 1M x 9	(2) 1M x 9

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DRAM PARITY CONFIGURATION		
Size	P 0	P 1
512KB (A)	(2) 41256	None
512KB (B)	(2) 41256	None
512KB (C)	None	None
1MB (A)	(2) 41256	(2) 41256
1MB (B)	(2) 41256	(2) 41256
1MB (C)	None	None
2MB (A)	(2) 411000	None
2MB (B)	None	None
2MB (C)	(2) 41256	(2) 41256
2MB (D)	(2) 41256	(2) 41256
4MB (A)	(2) 411000	(2) 411000
4MB (B)	None	None
5MB (A)	(2) 41256	(2) 41256
5MB (B)	(2) 41256	(2) 41256
8MB	(2) 411000	(2) 411000

DRAM JUMPER CONFIGURATION						
Size	J26	J27	J28	J29	J30	J31
512KB (A)	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
512KB (B)	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
512KB (C)	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
1MB (A)	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
1MB (B)	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
1MB (C)	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
2MB (A)	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
2MB (B)	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
2MB (C)	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
2MB (D)	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
4MB (A)	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
4MB (B)	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2	1 & 2
5MB (A)	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
5MB (B)	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3
8MB	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3	2 & 3

Note: Pins designated should be in the closed position.

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DRAM SWITCH CONFIGURATION					
Size	SW1/2	SW1/3	SW1/4	SW1/5	SW1/6
512KB (A)	Off	Off	Off	Off	Off
512KB (B)	Off	Off	Off	Off	Off
512KB (C)	Off	Off	Off	Off	Off
1MB (A)	On	Off	Off	Off	Off
1MB (B)	On	Off	Off	Off	Off
1MB (C)	On	Off	Off	Off	Off
2MB (A)	Off	Off	On	Off	Off
2MB (B)	Off	Off	On	Off	Off
2MB (C)	On	On	Off	Off	On
2MB (D)	On	On	Off	Off	On
4MB (A)	On	Off	On	Off	Off
4MB (B)	On	Off	On	Off	Off
5MB (A)	On	On	Off	On	On
5MB (B)	On	On	Off	On	On
8MB	On	On	Off	Off	On