AST RESEARCH, INC.
ADVANTAGE + (CHINA) 50179

Processor	CX M1/Pentium
Processor Speed	75/90/100/120/133/150/166/200MHz
Chip Set	Unidentified
Video Chip Set	SIS
Maximum Onboard Memory	128MB
Maximum Video Memory	2MB
Cache	256KB
BIOS	AST
Dimensions	254mm x 218mm
I/O Options	Floppy drive interface, IDE interfaces (2), parallel port, PS/2 mouse port, serial ports (2), video feature connector, VGA port, riser slot, cache slot
NPU Options	None
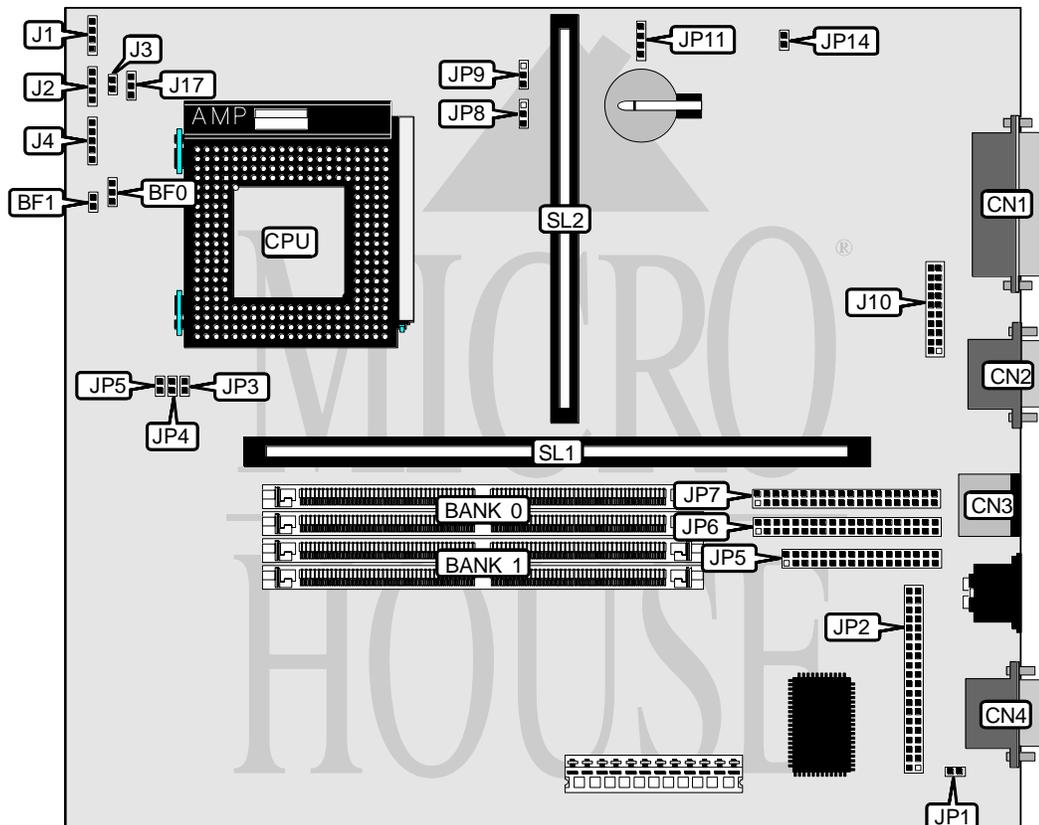
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CONNECTIONS			
Purpose	**Location**	**Purpose**	**Location**
Parallel port	CN1	Serial port 2	J10
Serial port 1	CN2	Chassis fan power	J17
PS/2 mouse port	CN3	Video feature connector	JP2
VGA port	CN4	Floppy drive interface	JP5
IDE interface LED	J1	IDE interface 2	JP6
Speaker	J2	IDE interface 1	JP7
Reset switch	J3	Riser slot	SL1
Power LED & keylock	J4	Cache slot	SL2

USER CONFIGURABLE SETTINGS		
Function	**Label**	**Position**
í VGA IRQ disabled	JP1	Open
VGA IRQ enabled	JP1	Closed
í Factory configured - do not alter	JP11	Unidentified
í Password normal operation	JP14	Closed
Password clear	JP14	Open

DRAM CONFIGURATION		
Size	**Bank 0**	**Bank 1**
8MB	(2) 1M x 36	None
16MB	(2) 2M x 36	None
16MB	(2) 1M x 36	(2) 1M x 36
24MB	(2) 2M x 36	(2) 1M x 36
32MB	(2) 4M x 36	None
32MB	(2) 2M x 36	(2) 2M x 36
40MB	(2) 4M x 36	(2) 1M x 36
48MB	(2) 4M x 36	(2) 2M x 36
64MB	(2) 8M x 36	None
64MB	(2) 4M x 36	(2) 4M x 36
72MB	(2) 8M x 36	(2) 1M x 36
80MB	(2) 8M x 36	(2) 2M x 36
96MB	(2) 8M x 36	(2) 4M x 36
128MB	(2) 8M x 36	(2) 8M x 36

CACHE CONFIGURATION	
Size	**SL2**
256KB	256KB module installed

CACHE JUMPER CONFIGURATION	
Type	**JP8**
í Asynchronous	Pins 1 & 2 closed
Synchronous, pipeline burst	Pins 2 & 3 closed

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VIDEO MEMORY CONFIGURATION		
Size	**Bank 0**	**Bank 1**
1MB	1MB	None
2MB	1MB	1MB
Note: The location of banks 0 & 1 are unidentified. The size of the chips are unidentified.		

CPU SPEED SELECTION (CYRIX)							
CPU speed	**Clock speed**	**Multiplier**	**BF0**	**BF1**	**JP3**	**JP4**	**JP5**
100MHz	50MHz	2x	2 & 3	Open	Closed	Open	Open
120MHz	60MHz	2x	2 & 3	Open	Closed	Closed	Open
Note: Pins designated should be in the closed position.							

CPU SPEED SELECTION (INTEL)							
CPU speed	**Clock speed**	**Multiplier**	**BF0**	**BF1**	**JP3**	**JP4**	**JP5**
75MHz	50MHz	1.5x	1 & 2	Open	Open	Open	Open
100MHz	66MHz	1.5x	1 & 2	Open	Closed	Open	Closed
133MHz	66MHz	2x	2 & 3	Open	Closed	Open	Closed
150MHz	60MHz	2.5x	2 & 3	Closed	Closed	Closed	Open
166MHz	66MHz	2.5x	2 & 3	Closed	Closed	Open	Closed
200MHz	66MHz	3x	1 & 2	Closed	Closed	Open	Closed
Note: Pins designated should be in the closed position.							

CPU TYPE SELECTION	
Type	**JP9**
VR (3.3v - 3.4v)	Closed
VRE (3.4v - 3.6v)	Open